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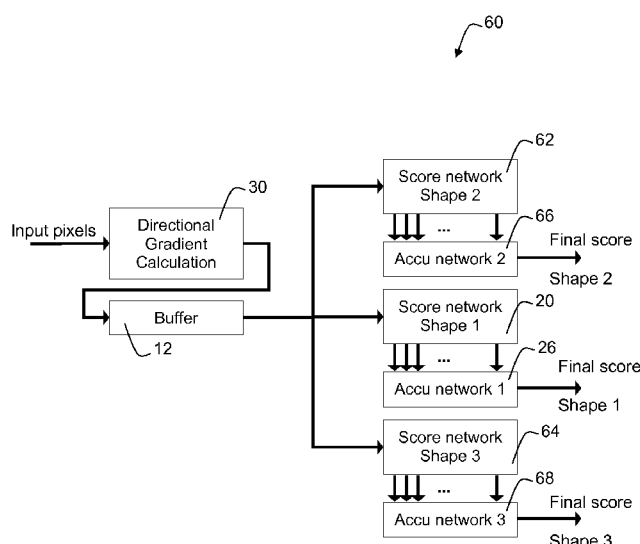


FIG. 4

(57) Abstract: A system (10) for efficient image feature extraction comprises a buffer (12) for storing a slice of at least n lines of gradient direction pixel values of a directional gradient image. The buffer has an input (14) for receiving the first plurality n of lines and an output (16) for providing a second plurality m of columns of gradient direction pixel values of the slice to an input (18) of a score network (20), which comprises comparators for comparing the gradient direction pixel values of the second plurality of columns with corresponding reference values of a reference directional gradient pattern of a shape and adders for providing partial scores depending on output values of the comparators to score network outputs (22) which are coupled to corresponding inputs (24) of an accumulation network (26) having an output (28) for providing a final score depending on the partial scores.

**Title: System and method for efficient image feature extraction****Description**5 Field of the invention

This invention in general relates to digital image processing and more specifically to a system and a method for image feature extraction.

Background of the invention

10 Many applications employing image processing typically rely on the evaluation of information about the content of a digital image or video. In order to provide these information, many techniques for automatically extracting information about the content have been developed. A common problem is to find primitive shapes, for example lines, circles, ellipses or curves in an image frame. For example, many Advanced Driver Assistance Systems (ADAS), i.e. systems for  
15 supporting the driver of a vehicle for increased driving safety, for example systems for lane departure warning, collision warning, automatic parking or traffic sign recognition, require reliable and fast detection of shapes, such as lines or circles. The detection of eyes is another example application for shape detection in digital image frames, for example in portraits or for systems for detection of vehicle driver drowsiness.

20 Due to the amount of image data and the unknown variety of information contained, the data may be difficult to be processed. One approach of reduction is feature extraction, i.e. a transformation of the image data into a reduced representation set of features. The features extracted should contain the relevant information from the input data to perform the desired task, for example to detect a shape such as a line or a circle based on this reduced representation  
25 instead of the original data.

An example of a feature extraction technique is the Hough transform. The algorithm for computing the Hough transform comprises a voting procedure carried out in a parameter space, from which object candidates are obtained as local maxima.

The Hough transform for circles (HTFC) is a method for detecting circle shapes in a digital  
30 image and may therefore for example be applied when detecting eyes in portrait images or traffic signs in video-based driver assistance systems.

For HTFC, a pre-processing step is required for a digital gray-value input image  $I(x,y)$ , wherein the gradient of the image intensity function is calculated for each pixel (picture element) and the image  $I(x,y)$  is replaced by a directional gradient image containing gradient vectors  $g(x,y)$ .  
35 Gradients may be calculated using two-dimensional gradient filter operators.

The first step of the HTFC itself is the generation of 2-dimensional histograms of circle-center probability scores. Typically, one histogram has to be generated for every radius of interest.

For a single radius  $r$ , the following applies: As the gradient  $g$  points towards the center of the circle (or to the opposite direction), at every pixel position  $x,y$  the histogram entries at  $(x,y)+r\cdot g/|g|$   
40 and  $(x,y)-r\cdot g/|g|$  are increased, i.e. for HTFC every gradient direction pixel value votes for two circle

centers. If the values belong to a circle, scores at the center may accumulate and the maxima in the histogram designate circle centers. In a second step, this histogram is searched for the maximum entries.

The implementation of the Hough transform and especially the HTFC typically requires a lot of buffer-space, since each calculated two-dimensional histogram usually has the same dimensions as the image. For HTFC, one 2D histogram per circle diameter is required. The calculation of the HTFC for an image frame may require considerable processing time, since the sequential read-modify-write accesses may limit the speed of operation.

Usually, the processing of a complete image frame is performed in two passes. The effect of a single input pixel to a  $2r \times 2r$  region of the histogram is rendered. The histogram entries reach their final value when all relevant pixels have been processed. After the histogram calculation is finished the maxima in the histogram have to be searched in a second pass.

#### Summary of the invention

The present invention provides a system and a method for efficient image feature extraction as described in the accompanying claims.

Specific embodiments of the invention are set forth in the dependent claims.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter.

#### Brief description of the drawings

Further details, aspects and embodiments of the invention will be described, by way of example only, with reference to the drawings. Elements in the figures are illustrated for simplicity and clarity and have not necessarily been drawn to scale. Identical reference numerals in different figures refer to identical or similar components.

FIG. 1 shows a schematic block diagram of an example of a first embodiment of a system for efficient image feature extraction.

FIG. 2 shows a schematic block diagram of an example of an implementation of a system for efficient image feature extraction using an accumulator chain.

FIG. 3 shows a schematic diagram of an example of a fraction of a directional gradient image.

FIG. 4 shows a schematic block diagram of a second embodiment of a system for efficient feature extraction.

FIG. 5 shows a schematic flow diagram of an example of an embodiment of an image feature extraction method.

FIG. 6 shows a schematic diagram of an example of an embodiment of a vehicle.

Detailed description of the preferred embodiments

Referring to FIG. 1, a schematic block diagram of an example of a first embodiment of a system 10 for efficient image feature extraction is shown. The system comprises a buffer 12 for storing a slice of at least a first plurality  $n$  of lines of gradient direction pixel values of a directional gradient image. The buffer has a buffer input 14 for receiving the first plurality  $n$  of lines and a buffer output 16 for providing a second plurality  $m$  of columns of gradient direction pixel values of the slice to a score network input 18 of a score network 20. The score network comprises a plurality of comparators for comparing the gradient direction pixel values of the second plurality of columns with corresponding reference gradient direction pixel values of a reference directional gradient pattern of a shape and a plurality of adders for providing partial score values depending on output values of the comparators to one or more score network outputs 22. The one or more score network outputs are coupled to one or more corresponding accumulation network inputs 24 of an accumulation network 26 having an accumulation network output 28 for providing a final score value depending on the partial score values.

The number of lines of the reference directional gradient pattern may be  $n$ , therefore corresponding to the number of lines of the slice, i.e. the number of gradient direction pixel values in a column of the slice sent to the score network. A number  $d$  of columns of the reference directional gradient pattern may be up to  $m$ . However, usually the number  $d$  will be smaller, for example  $n$  in order to apply a square reference pattern.

The reference pattern comprises the gradient direction pixel values of a shape. Hence, the calculated final score is an indicator of similarity between the shape and the input image pixels corresponding to the directional gradient pixel values sent to the score network.

In one embodiment of the system, a number  $d$  of columns may be applied to the score network, each of the  $d \times n$  gradient direction pixel values may be compared to the corresponding of the  $d \times n$  reference gradient direction pixel values. If the result of a comparison shows that corresponding values of a pair are identical, a partial score value may be increased. Typically, lines or columns of values may be compared, and a partial score may be calculated for each line or column, for example by counting the number of comparison results corresponding to identical compared values.

A gradient direction pixel value of a set of values of  $d$  columns and  $n$  lines sent to the score network may be said to be corresponding to its reference gradient direction pixel value, if they share the same position in said set and in the reference directional gradient pattern having  $d$  columns and  $n$  lines.

The partial score values may be provided to the accumulation network 26. In the described embodiment the accumulation network may accumulate all partial scores to a final score value, for example by adding the partial score values, therefore providing a matching score for the part of the directional gradient image sent to the score network and the reference directional gradient pattern. Each final score value may therefore be regarded as a histogram entry of a Hough transform of the input image for the reference shape for the input image position corresponding to the gradient direction pixel values currently applied to the score network 20. Therefore, this image feature

extraction system 10 may be used for calculating a Hough transform with respect to the chosen shape, for example a HTFC.

In order to provide final score values for a line of  $m$  gradient direction pixel values, all possible sets of  $d$  columns differing from all other sets by one line may be sent to the score network. For processing a complete image, the slice may be processed, the first line may be removed from the buffered slice and the next line of the directional gradient image being processed may be added to the buffered slice. Although any kind of buffer may be used, for efficient implementation, the buffer 12 may be a ringbuffer. And the buffer 12 may allow for storing at least one additional line of gradient direction pixel values. Hence, a new line may be inserted in the buffer while processing the slice.

The system 10 may repeat this procedure until all lines of the image have been processed. When using the system in a digital video based application, all or a part of the image frames of a video sequence may be processed.

For performing the required pre-processing of a digital image and providing the required directional gradient representation of a digital input image, the system 10 may comprise a gradient direction calculation module 30 having a module input 32 for receiving input pixels of an input image and a module output 34 for providing the gradient direction pixel values of the directional gradient image calculated from the input image to the buffer input 14.

The score network 20 provides the partial score values. The score network 20 may provide partial score values for columns of compared values. A partial score value may for example be the sum of all gradient direction pixel values of a column found to be identical to the corresponding reference values of the reference pattern. Therefore, comparators and adders may be coupled to form a plurality of column comparator networks 36, 38, 40 and each column comparator network may provide one of the partial scores to the accumulation network 26.

Referring now to FIG. 2, a schematic block diagram of an example of an implementation of a system for efficient image feature extraction using an accumulator chain 26 is shown. The system may be implemented for fast execution by re-ordering the sequence of calculation steps, therefore allowing to render the effects of a column of  $n$  gradient vectors or gradient direction pixel values to a row of  $d$  histogram entries at once. For circular shapes of radius  $r$  as applied in HTFC, the effects of a column of  $2r$  gradient vectors, i.e. gradient direction pixel values, to a row of  $2r$  histogram entries may be rendered at once. For every pixel clock cycle, a column of  $2r$  directions or gradient direction pixel values may be read from the buffer 12. The score network, which in this example may be a logic network with  $2r$  inputs and  $2r$  outputs then may calculate a required update of a row of  $2r$  histogram entries.

For this, the second plurality  $m$  of columns of the slice may be applied to the score network 20 consecutively and each column of the second plurality  $m$  of columns may be applied in parallel to each of the column comparator networks 36, 38 40, i.e. each column entered in the score network may be applied to all column comparator networks 36, 38, 40 concurrently at the same time. All processing depending on gradient direction pixel values of the column may therefore be performed at once without any need of re-entering the same column in the score network for

another processing. Columns may therefore be sent to the score network 20 consecutively, for example in a stream of columns. The system may even be able to process live video.

The score network 20 may be generated a priori depending on the geometry of the shape. As this network may be mostly sparse, it may be efficiently synthesized.

5 In order to calculate the final score, the accumulation network 26 may comprise a plurality of accumulate-and-shift registers A15, A14, A13, A12, A11, A10, A-15 coupled to form an accumulator chain. An accumulate-and-shift register may receive a first input value, add it to a second input value and provide the result at an output on reception of a shift command, for example by a pixel clock signal.

10 Each of the accumulate-and-shift registers may be coupled at one of its inputs to a different column comparator network 36, 38, 40 for receiving one of the partial score values, thereby enabling a correlation-like calculation of the final scores corresponding to the d histogram entries of a Hough transform for the shape. The histogram entries may be calculated using a chain of accumulator registers. This chain may be shifted after the partial score column entries have been  
15 received. After a setup delay of d shifts the final result, i.e. the final score, pops out of the last accumulator of the chain and may directly be entered into a list of the biggest score values to be searched directly for maximum values indicating highly probable positions of shapes similar to the reference shape in the input image.

The system shown in FIG. 2 shows an example of an accumulator chain comprising 31  
20 accumulators corresponding to a directional gradient pattern having 31 columns. In the example shown in FIG. 2, the input image may contain a perfect circle. For example, a first input column to the score network for a shape of a perfect circle having a diameter of 31 pixels may be 15 pixels away from the circle center and a comparison may result in a partial score of 7 to be added to the corresponding accumulator A15. A second input column may be 14 pixels away from the center  
25 and a comparison may result in a partial score of 13 to be added to the corresponding accumulator A14. A third input column may be 13 pixels away from the center and a comparison may result in a partial score of 8 to be added to the corresponding accumulator A13. The partial scores may be accumulated in the shifting register chain 26 and after 31 adds to the accumulators A15 to A-15 and 31 shifts, the final score for the 31x31 circle may be shifted out of A-15.

30 For easier mathematical description, a possible operation of a system using an accumulator chain having only a first, a second and a third accumulator and processing columns of 3 lines is

described: A directional reference gradient reference pattern may be given by 
$$\begin{bmatrix} a & b & c \\ d & e & f \\ g & h & i \end{bmatrix}$$
 and

three columns of input gradient direction pixel values may be given by a first column 
$$\begin{bmatrix} u_0 \\ v_0 \\ w_0 \end{bmatrix},$$
 a

second column  $\begin{bmatrix} u_1 \\ v_1 \\ w_1 \end{bmatrix}$ , and a third column  $\begin{bmatrix} u_2 \\ v_2 \\ w_2 \end{bmatrix}$ . For the sake of compact notation, a term such as

$au$  may be regarded as an angular comparison operation. If  $a$  and  $u$  are both valid and are of the same gradient direction, the comparison result  $au$  may be 1, otherwise 0. In a first cycle, the first column may be applied to the score network comprising the reference pattern, only the partial scores produced by the first column may be calculated by comparing them to the three columns of the reference pattern. The first accumulator may comprise a first value  $au_0+dv_0+gw_0$ , the second accumulator may comprise a second value  $bu_0+ev_0+hw_0$ , and the third accumulator may comprise a third value  $cu_0+dv_0+gw_0$ , and the final score value may be invalid. Then the accumulator chain may be shifted once to the right, the second column may be applied to the score network and the partial score values corresponding to the second column may be added to the first, second and third values of the three accumulators, giving a first accumulator value of  $au_1+dv_1+gw_1$ , a second accumulator value of  $au_0+dv_0+gw_0+bu_1+ev_1+hw_1$ , and a third accumulator value  $bu_0+ev_0+hw_0+cu_1+fv_1+iw_1$ , and an invalid final score. After another shift to the right and during another cycle, the partial score values depending on the third column applied to the score network may be added to the first, second and third values of the three accumulators, giving a first accumulator value of  $au_2+dv_2+gw_2$ , a second accumulator value of  $au_1+dv_1+gw_1+bu_2+ev_2+hw_2$ , and a third accumulator value  $au_0+dv_0+gw_0+bu_1+ev_1+hw_1+cu_2+fv_2+iw_2$ , which may be shifted out of the chain as the first final score, being identical to a result of a calculation intense single score calculation. Additional columns consecutively sent to the score network may result in further valid final scores values.

The maximum search may then for example be efficiently implemented by sorting the list of final scores on the fly each time a newly calculated final score becomes available. Therefore, a big histogram buffer may not be required. Geometric calculations may be avoided, and only a single search pass may be required when processing an input image.

Still referring to FIG. 2 and now also to FIG. 3, a schematic diagram of an example of a fraction of a directional gradient image 42 is shown. It may also be regarded as an example of a reference directional gradient pattern of a shape. The shape may be a circle having a diameter of  $n$  or less pixel values, therefore allowing the system to efficiently calculate the HTFC for circles of the chosen diameter. Each final score may refer to a possible circle center of the position given by the center of the gradient direction pixels used for calculating the final score. The chosen diameter may for example be 31 pixels corresponding to a circle radius of 15 pixels. However, other diameters are possible, such as more than or less than 31 pixels, for example 15 or 7 pixels.

Although the shape in the shown example is a circle of the chosen diameter, it is in the scope of the system to use other shapes, such as an ellipse of a chosen radius combination, circles with different diameters or other shapes with all parameters describing the shape being fix except the horizontal and vertical positions.

To enable efficient comparison of values, the gradient direction pixel values and the reference gradient direction pixel values may be quantized depending on the same quantization

characteristic. The gradient direction pixel values may be coarsely quantized. For example, for a circle opposite directions may be added to the same quantization step. For example, a circle having a diameter of 31 pixels may be quantized sufficiently with 3 + 1 bits, allowing for encoding 8 directions 44, 46, 48, 50, 52, 54, 56, 58 (opposite directions are the same) plus 1 validity bit disclosing, whether or not the gradient is above a detection acceptance threshold. If a gradient is found to be invalid, it is set to 0, shown in FIG. 3 as black gradient direction values.

Referring now to FIG. 4, a schematic block diagram of a second embodiment of a system for efficient feature extraction is illustrated. Only components differing from those of the shown first embodiment will be described. The system may comprise one or more additional score networks 62, 64. Each additional score network may provide score values corresponding to a different shape and may be coupled to a corresponding additional accumulation network 66, 68.

It should be noted that a different shape may also refer to a similar shape characterized by a different parameter value. For example, circles of different diameters may be considered to be different shapes. Hence, this embodiment of the system may for example provide final score values and therefore histogram entries for a HTFC of an input image for a plurality of diameters concurrently at the same time. This may enable the extraction of circle positions of circles having different diameters from input images at the same time. A processing of the image using concurrent processors may not be required. And only one single buffer 12 may be used for providing the gradient direction pixel values to the plurality of score networks 20, 62, 64. This may reduce the required buffer memory and therefore the required chip space. For example, when calculating an HTFC for eight different circle diameters, the buffer chip space required for storing 8 full histograms may be more than eight times bigger than the chip space required for a ringbuffer 12 according to the described second embodiment of a system 60 for efficient image feature extraction.

Referring now to FIG. 5, a schematic flow diagram of an example of an embodiment of an image feature extraction method is shown. The illustrated method allows implementing the advantages and characteristics of the described system for efficient image feature extraction as part of an image feature extraction method.

The method comprises storing 70 a slice of at least a first plurality  $n$  of lines of gradient direction pixel values of a directional gradient image in a buffer; providing 72 a second plurality  $m$  of columns of gradient direction pixel values of the slice to a score network input of a score network; comparing 74 the gradient direction pixel values of the second plurality of columns with corresponding reference gradient direction pixel values of a reference directional gradient pattern of a shape; providing 76 partial score values depending on result values of the comparisons; and providing 78 a final score value depending on the partial score values.

The step of providing 76 partial score values may comprise providing each of the partial score values as a result of a comparison of a column of the reference gradient direction pattern and a column of the second plurality  $m$  of columns.

And the step 72 of providing a second plurality  $m$  of columns of gradient direction pixel values of the slice to a score network input of a score network may then comprise providing each of

the second plurality  $m$  of columns of the slice to the score network consecutively; and applying each column of the second plurality  $m$  of columns in parallel to each of a plurality of column comparator networks.

Although the system may be implemented or the method may be executed using any kind of suitable hardware and/or software, for many applications it may be desired to have it integrated in a system-on-a-chip, comprising a system for efficient image feature extraction or implementing a method as described above.

Many applications employing image processing may use an image feature extraction system or a method as described above. When using it for HTFC, it may for example be used in applications for eye pupil detection, video surveillance systems or for the detection of traffic signs. The system may for example be used in advanced driver assistance systems (ADAS). ADAS may be In-vehicle navigation systems. Video-based ADAS such as adaptive cruise control (ACC), lane departure detection/warning systems, lane change assistance, intelligent speed adaptation or intelligent speed advice (ISA), night vision systems, pedestrian protection systems, or driver drowsiness detection systems may for example use traffic sign detection based on circle detection. However other shapes may be extracted and evaluated as well. Therefore, an advanced driver assistance system (ADAS) may comprise a system-on-a-chip or a system for efficient image feature extraction or may implement a method as described above.

Referring now also to FIG. 6, a schematic diagram of an example of a vehicle 84 is shown. The vehicle 84 comprises an advanced driver assistance system 82 using a system-on-a-chip 80 having a system 10, 60 for efficient image feature extraction. It is an example for a vehicle that may comprise a system-on-a-chip or an advanced driver assistance system or a system for efficient image feature extraction as described above. A vehicle 84 may be a car. However, it may be any other vehicle such as a truck, a motor bike, a train, a ship, a helicopter, a plane, or a bicycle.

The system may be implemented or the method may be executed using hardware components. However, a computer program product may comprise code portions for executing steps of a method or for implementing parts of an image feature extraction system as described above when run on a programmable apparatus.

The invention may also be implemented in a computer program for running on a computer system, at least including code portions for performing steps of a method according to the invention when run on a programmable apparatus, such as a computer system or enabling a programmable apparatus to perform functions of a device or system according to the invention. The computer program may for instance include one or more of: a subroutine, a function, a procedure, an object method, an object implementation, an executable application, an applet, a servlet, a source code, an object code, a shared library/dynamic load library and/or other sequence of instructions designed for execution on a computer system. The computer program may be provided on a data carrier, such as a CD-rom or diskette, stored with data loadable in a memory of a computer system, the data representing the computer program. The data carrier may further be a data connection, such as a telephone cable or a wireless connection.

In the foregoing specification, the invention has been described with reference to specific examples of embodiments of the invention. It will, however, be evident that various modifications and changes may be made therein without departing from the broader spirit and scope of the invention as set forth in the appended claims. For example, the connections may be any type of connection suitable to transfer signals from or to the respective nodes, units or devices, for example via intermediate devices. Accordingly, unless implied or stated otherwise the connections may for example be direct connections or indirect connections.

Also, at least portions of the system 10, 60 may be implemented using a programmable logic device (PLD), e.g. a field programmable gate array (FPGA), an application specific integrated circuit (ASIC) etc., or as a program code executable by a processing device, such as a digital signal processor (DSP), a microcontroller unit (MCU), a general purpose processor (GPP), a central processing unit (CPU), a graphics processing unit (GPU), etc.

Because the apparatus implementing the present invention is, for the most part, composed of electronic components and circuits known to those skilled in the art, circuit details will not be explained in any greater extent than that considered necessary as illustrated above, for the understanding and appreciation of the underlying concepts of the present invention and in order not to obfuscate or distract from the teachings of the present invention.

Some of the above embodiments, as applicable, may be implemented using a variety of systems. For example, although Figure 1 and the discussion thereof describe an exemplary feature extraction system, this exemplary system is presented merely to provide a useful reference in discussing various aspects of the invention. Of course, the description of the architecture has been simplified for purposes of discussion, and it is just one of many different types of appropriate architectures that may be used in accordance with the invention. Those skilled in the art will recognize that the boundaries between logic blocks are merely illustrative and that alternative embodiments may merge logic blocks or circuit elements or impose an alternate decomposition of functionality upon various logic blocks or circuit elements.

Thus, it is to be understood that the architectures depicted herein are merely exemplary, and that in fact many other architectures can be implemented which achieve the same functionality. In an abstract, but still definite sense, any arrangement of components to achieve the same functionality is effectively "associated" such that the desired functionality is achieved. Hence, any two components herein combined to achieve a particular functionality can be seen as "associated with" each other such that the desired functionality is achieved, irrespective of architectures or intermedial components. Likewise, any two components so associated can also be viewed as being "operably connected," or "operably coupled," to each other to achieve the desired functionality.

Also for example, in one embodiment, the illustrated elements of system 10 are circuitry located on a single integrated circuit or within a same device. Alternatively, system 10 may include any number of separate integrated circuits or separate devices interconnected with each other.

Also for example, system 10 or portions thereof may be soft or code representations of physical circuitry or of logical representations convertible into physical circuitry. As such, system 10 may be embodied in a hardware description language of any appropriate type.

5 However, other modifications, variations and alternatives are also possible. The specifications and drawings are, accordingly, to be regarded in an illustrative rather than in a restrictive sense.

10 In the claims, any reference signs placed between parentheses shall not be construed as limiting the claim. The word 'comprising' does not exclude the presence of other elements or steps than those listed in a claim. Furthermore, the terms "a" or "an," as used herein, are defined as one or more than one. Also, the use of introductory phrases such as "at least one" and "one or more" in the claims should not be construed to imply that the introduction of another claim element by the indefinite articles "a" or "an" limits any particular claim containing such introduced claim element to inventions containing only one such element, even when the same claim includes the introductory phrases "one or more" or "at least one" and indefinite articles such as "a" or "an." The same holds  
15 true for the use of definite articles. Unless stated otherwise, terms such as "first" and "second" are used to arbitrarily distinguish between the elements such terms describe. Thus, these terms are not necessarily intended to indicate temporal or other prioritization of such elements. The mere fact that certain measures are recited in mutually different claims does not indicate that a combination of these measures cannot be used to advantage.

20 While the principles of the invention have been described above in connection with specific apparatus, it is to be clearly understood that this description is made only by way of example and not as a limitation on the scope of the invention.

**Claims**

1. A system (10) for efficient image feature extraction, comprising  
a buffer (12) for storing a slice of at least a first plurality n of lines of gradient direction pixel  
5 values of a directional gradient image, said buffer having  
a buffer input (14) for receiving said first plurality n of lines and  
a buffer output (16) for providing a second plurality m of columns of gradient  
direction pixel values of said slice to a score network input (18) of  
a score network (20), said score network comprising  
10 a plurality of comparators for comparing said gradient direction pixel values of said second  
plurality of columns with corresponding reference gradient direction pixel values of a reference  
directional gradient pattern of a shape and  
a plurality of adders for providing partial score values depending on output values of said  
comparators to one or more score network outputs (22), said one or more score network outputs  
15 coupled to one or more corresponding accumulation network inputs (24) of  
an accumulation network (26) having  
an accumulation network output (28) for providing a final score value depending on  
said partial score values.
- 20 2. The system as claimed in claim 1, wherein said buffer (12) is a ringbuffer.
3. The system as claimed in claim 1 or claim 2, wherein said buffer (12) allows for storing at  
least one additional line of gradient direction pixel values.
- 25 4. The system as claimed in any of the preceding claims, comprising  
a gradient direction calculation module (30) having  
a module input (32) for receiving input pixels of an input image and  
a module output (34) for providing said gradient direction pixel values of said  
directional gradient image calculated from said input image to said buffer input (14).  
30
5. The system as claimed in any of the preceding claims, wherein said comparators and  
adders are coupled to form a plurality of column comparator networks (36, 38, 40), each column  
comparator network providing one of said partial scores to said accumulation network (26).
- 35 6. The system as claimed in claim 5, wherein said second plurality m of columns of said slice  
is applied to said score network (20) consecutively and each column of said second plurality m of  
columns is applied in parallel to each of said column comparator networks (36, 38, 40).

7. The system as claimed in claim 6, wherein said accumulation network comprises a plurality of accumulate-and-shift registers (A15, A14, A13, A12, A10, A-15) coupled to form an accumulator chain.

5 8. The system as claimed in claim 7, wherein each of said accumulate-and-shift registers is coupled to a different column comparator network (36, 38, 40) for receiving one of said partial score values.

9. The system as claimed in any of the preceding claims wherein said shape is a circle having  
10 a diameter of n or less pixel values.

10. The system as claimed in any of the preceding claims wherein said gradient direction pixel values and said reference gradient direction pixel values are quantized depending on the same quantization characteristic.

11. The system as claimed in any of the preceding claims, comprising  
one or more additional score networks (62, 64), each additional score network for providing  
score values corresponding to a different shape, coupled to a corresponding  
additional accumulation network (66, 68).

12. An image feature extraction method, comprising  
storing (70) a slice of at least a first plurality n of lines of gradient direction pixel values of a  
directional gradient image in a buffer;  
providing (72) a second plurality m of columns of gradient direction pixel values of said slice  
to a score network input of a score network;  
comparing (74) said gradient direction pixel values of said second plurality of columns with  
corresponding reference gradient direction pixel values of a reference directional gradient pattern  
of a shape;  
providing (76) partial score values depending on result values of said comparisons; and  
providing (78) a final score value depending on said partial score values.

13. The image feature extraction method as claimed in claim 12, wherein the step of providing  
partial score values comprises  
providing each of said partial score values as a result of a comparison of a column of said  
reference gradient direction pattern and a column of said second plurality m of columns.

14. The image feature extraction method as claimed in claim 13, wherein the step of providing  
a second plurality m of columns of gradient direction pixel values of said slice to a score network  
input of a score network comprises

providing each of said second plurality  $m$  of columns of said slice to said score network consecutively; and

applying each column of said second plurality  $m$  of columns in parallel to each of a plurality of column comparator networks.

5

15. A system-on-a-chip (80), comprising

a system for efficient image feature extraction as claimed in any of claims 1 to 11 or implementing a method as claimed in any of claims 12 to 14.

10

16. An advanced driver assistance system (ADAS) (82), comprising

a system-on-a-chip as claimed in claim 15 or a system for efficient image feature extraction as claimed in any of claims 1 to 11 or implementing a method as claimed in any of claims 12 to 14.

17. A vehicle (84), comprising

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a system-on-a-chip as claimed in claim 15 or an advanced driver assistance system as claimed in claim 16 or a system for efficient image feature extraction as claimed in any of claims 1 to 11.

18. A computer program product, comprising

20

code portions for executing steps of a method as claimed in any of claims 12 to 14 or for implementing parts of a system as claimed in any of claims 1 to 11 when run on a programmable apparatus.

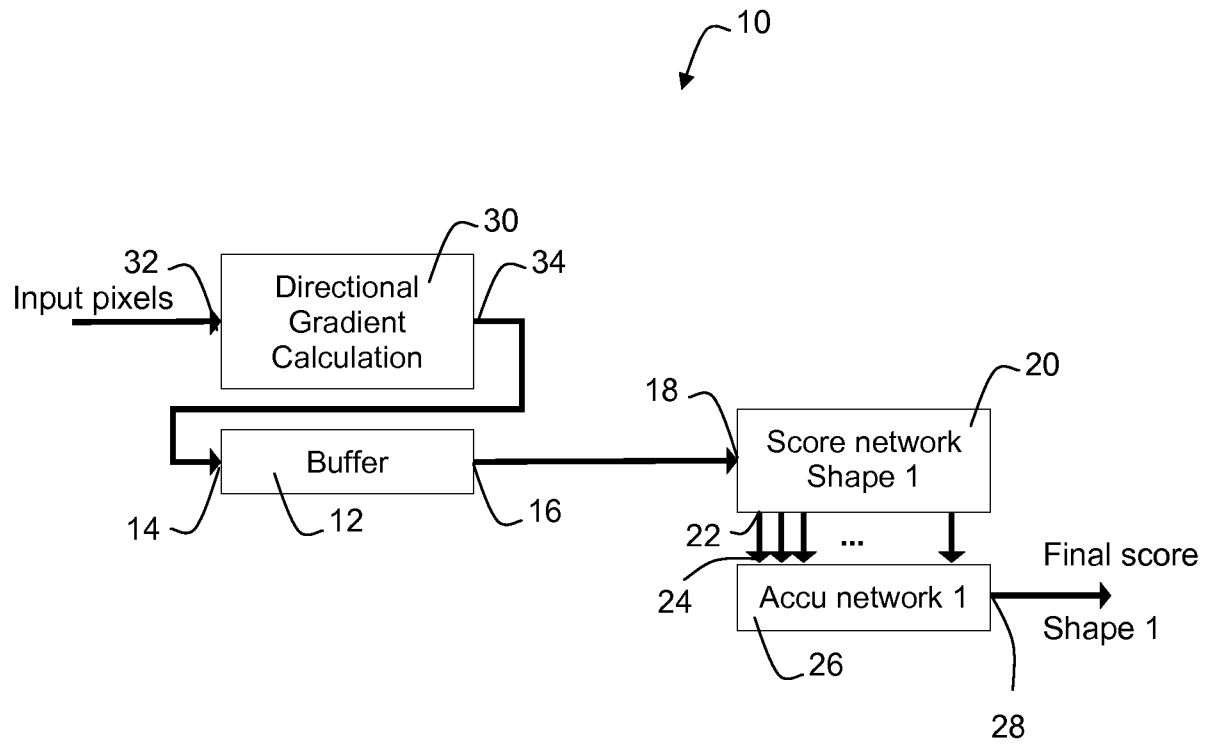


FIG. 1

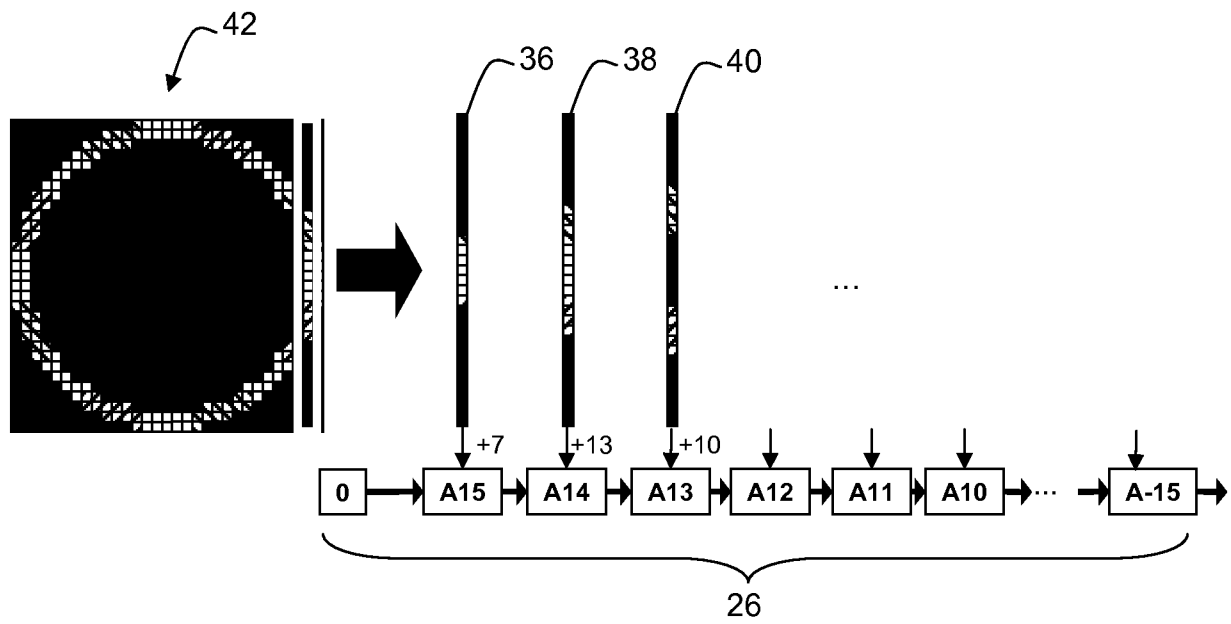


FIG. 2

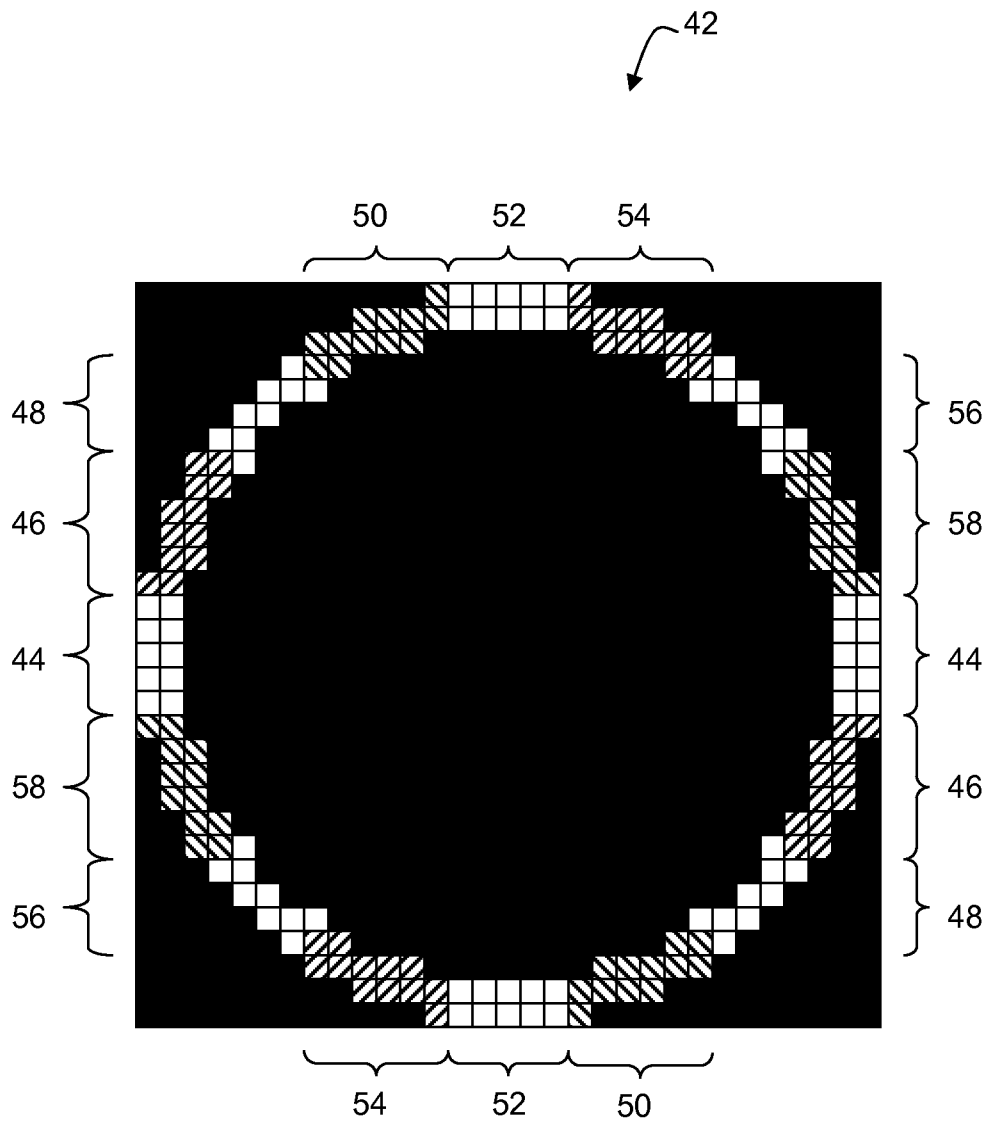


FIG. 3

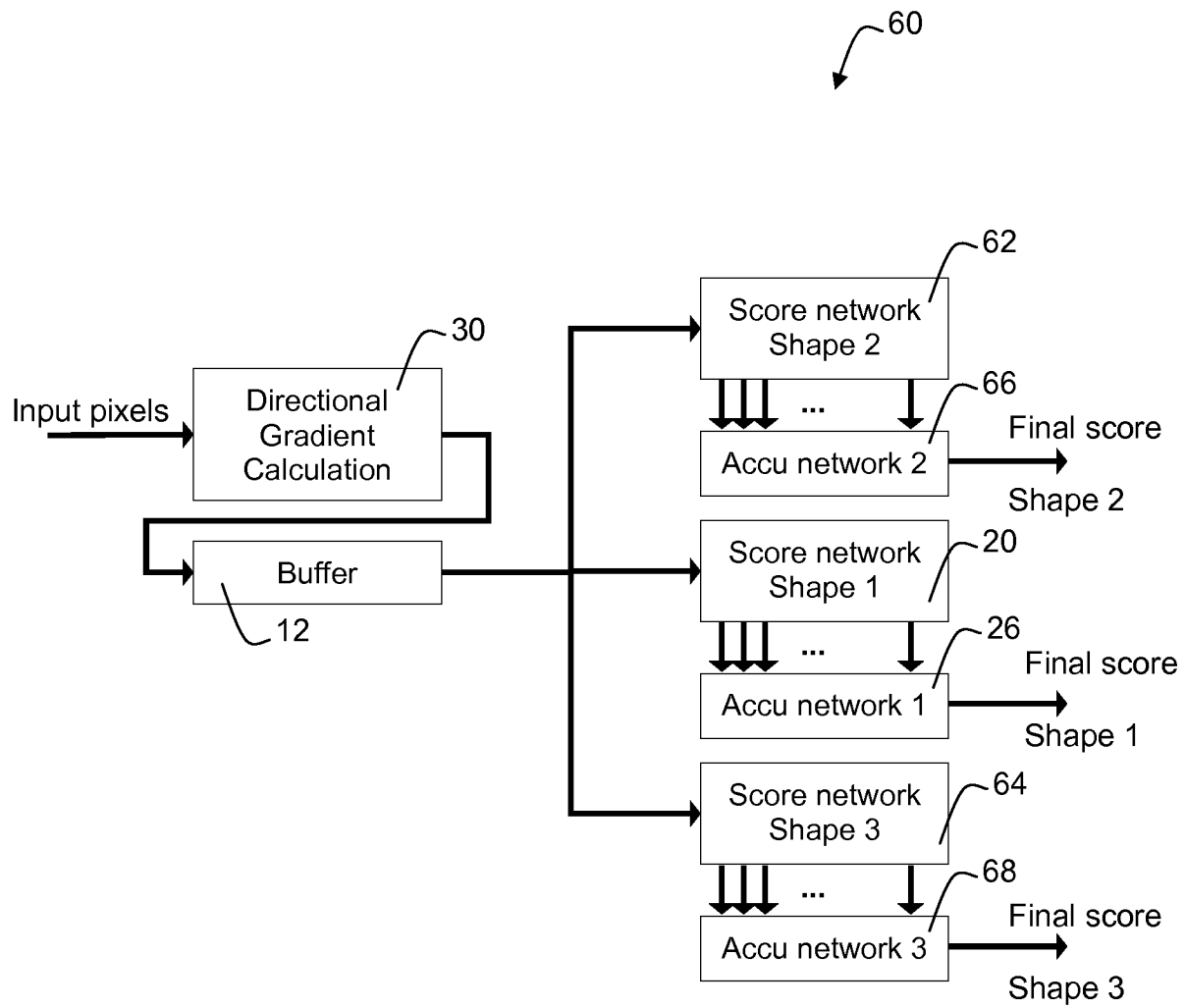


FIG. 4

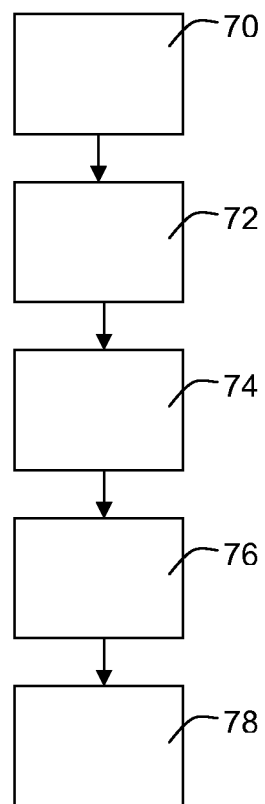


FIG. 5

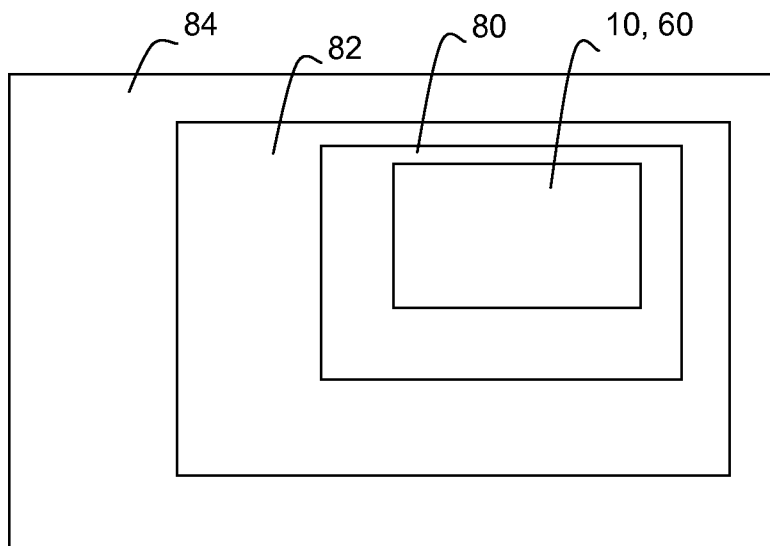


FIG. 6

## INTERNATIONAL SEARCH REPORT

International application No

PCT/IB2009/050012

## A. CLASSIFICATION OF SUBJECT MATTER

INV. G06K9/00

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G06K G08G G06T G06F G07D G01S

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category* | Citation of document, with indication, where appropriate, of the relevant passages                                                                                                                                                                                                                                                                                                                                                                                 | Relevant to claim No. |
|-----------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|
| X         | <p>EP 1 394 726 A (YAGI MASAKAZU [JP]; SHIBATA TADASHI [JP]; ROHM CO LTD [JP])<br/> 3 March 2004 (2004-03-03)<br/> abstract<br/> paragraphs [0001] - [0022]<br/> paragraph [0024]<br/> paragraph [0026]<br/> paragraph [0033]<br/> paragraph [0035]<br/> paragraphs [0041] - [0043]<br/> paragraph [0069]<br/> figure 1A<br/> figures 2,3<br/> figure 8<br/> figure 15<br/> figure 12<br/> figures 23B,24-29</p> <p style="text-align: center;">-----<br/>-/--</p> | 1-18                  |



Further documents are listed in the continuation of Box C.



See patent family annex.

## \* Special categories of cited documents:

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\*O\* document referring to an oral disclosure, use, exhibition or other means

\*P\* document published prior to the international filing date but later than the priority date claimed

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\*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

\*&amp;\* document member of the same patent family

Date of the actual completion of the international search

16 September 2009

Date of mailing of the international search report

05/10/2009

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Authorized officer

Celik, Hasan

## INTERNATIONAL SEARCH REPORT

International application No

PCT/IB2009/050012

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

| Category* | Citation of document, with indication, where appropriate, of the relevant passages                                                                                                                                                                                                                                                                                                                                                                                                             | Relevant to claim No. |
|-----------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|
| A         | <p>M. YAGI, T. SHIBATA: "An associative-processor-based mixed signal system for robust grayscale image recognition"</p> <p>INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS,</p> <p>vol. 5, 26 May 2002 (2002-05-26), - 29 May 2002 (2002-05-29) pages 137-140, XP002545898</p> <p>Page 138, Section 2, Grayscale image recognition system</p>                                                                                                                                                  | 1-18                  |
| X         | <p>ALMUDENA LINDOSO AND LUIS ENTRENA: "High performance FPGA-based image correlation"</p> <p>JOURNAL OF REAL-TIME IMAGE PROCESSING,</p> <p>vol. 2, no. 4,</p> <p>4 December 2007 (2007-12-04), pages 223-233, XP002545755</p> <p>abstract</p> <p>Pages 223-224, Section 1, Introduction, Paragraph 6</p> <p>Pages 224-225, Section 2, Image correlation</p> <p>Pages 225-226, Section 3, Correlation architecture, Subsection 3.1, Paragraphs 2 and 4</p> <p>figure 1</p>                      | 1-18                  |
| A         | <p>CECCARELLI M ET AL: "The orientation matching approach to circular object detection"</p> <p>PROCEEDINGS 2001 INTERNATIONAL CONFERENCE ON IMAGE PROCESSING. ICIP 2001.</p> <p>THESSALONIKI, GREECE, OCT. 7 - 10, 2001; [INTERNATIONAL CONFERENCE ON IMAGE PROCESSING], NEW YORK, NY : IEEE, US,</p> <p>vol. 3, 7 October 2001 (2001-10-07), pages 712-715, XP010563449</p> <p>ISBN: 978-0-7803-6725-8</p> <p>abstract</p> <p>Pages 712-713, Section 2, The orientation matching approach</p> | 1-18                  |
| A         | <p>STEVEN W. SMITH: "Digital Signal Processing: A Practical Guide for Engineers and Scientists" [Online]</p> <p>2003, , XP002545756</p> <p>ISBN: 978 0750674447</p> <p>Retrieved from the Internet:</p> <p>URL:<a href="http://www.dspguide.com/">http://www.dspguide.com/</a>&gt;</p> <p>[retrieved on 2008-09-14]</p> <p>Chapter 28, pages 503-514</p>                                                                                                                                       | 1-18                  |

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## INTERNATIONAL SEARCH REPORT

International application No

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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

| Category* | Citation of document, with indication, where appropriate, of the relevant passages                                                                                                                                                                                                                     | Relevant to claim No. |
|-----------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|
| A         | <p>KIMME C ET AL: "Finding Circles by an Array of Accumulators"<br/>COMMUNICATIONS OF THE ASSOCIATION FOR COMPUTING MACHINERY, ACM, NEW YORK, NY, US,<br/>vol. 18, no. 2,<br/>1 February 1975 (1975-02-01), pages<br/>120-122, XP002196698<br/>ISSN: 0001-0782<br/>the whole document</p> <p>-----</p> | 1-18                  |

# INTERNATIONAL SEARCH REPORT

### Information on patent family members

International application No

PCT/IB2009/050012

| Patent document<br>cited in search report | Publication<br>date | Patent family<br>member(s)          | Publication<br>date      |
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| EP 1394726      A                         | 03-03-2004          | JP 2004094644 A<br>US 2004197023 A1 | 25-03-2004<br>07-10-2004 |
| <hr/>                                     |                     |                                     |                          |