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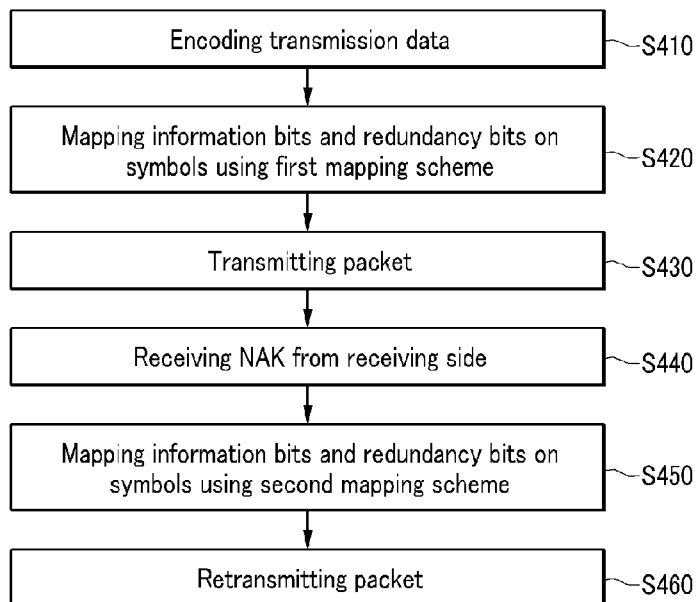
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[Continued on next page]

(54) Title: APPARATUS AND METHOD FOR MAPPING SYMBOL

[Fig. 4]



(57) Abstract: In a symbol mapping method, transmission data is encoded to output information bits and redundancy bits. The information bits and the redundancy bits are mapped to a symbol according to a first mapping scheme at a first transmission, and the information bits and the redundancy bits are mapped to a symbol according to a second mapping scheme at a second transmission.

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B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC⁸: H04L Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EPODOC, WPI, NPL		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 1 619 801 A1 (MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD) 25 January 2006 (25.01.2006) <i>paragraphs 0015 - 0018</i> --	1-20
A	US 2004/081073 A1 (Walton et al.) 29 April 2004 (29.04.2004) <i>abstract, paragraphs 0036, 0037</i> --	1-20
A	EP 1 189 380 A2 (Texas Instruments Inc.) 20 March 2002 (20.03.2002) <i>paragraphs 0013, 0018, claims 1, 2</i> ----	1-20
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