



(51) International Patent Classification:

H05K 1/03 (2006.01) *H05K 3/10* (2006.01)
H05K 1/02 (2006.01) *H05K 3/38* (2006.01)

(21) International Application Number:

PCT/US2016/064398

(22) International Filing Date:

1 December 2016 (01.12.2016)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

14/957,426 2 December 2015 (02.12.2015) US

(71) Applicant: **HONEYWELL FEDERAL MANUFACTURING & TECHNOLOGIES, LLC** [US/US]; 14520 Botts Road, Kansas City, Missouri 64147 (US).

(72) Inventors: **HATCH, Jonathan Douglas**; 22809 South State Line Rd., Cleveland, Missouri 64734 (US). **HATCH, Stephen McGarry**; 3308 Lake Shore Drive, Blue Springs, Missouri 64014 (US).

(74) Agent: **LUEBBERING, Thomas B.**; Hovey Williams LLP, 10801 Mastin Blvd, Suite 1000, 84 Corporate Woods, Overland Park, Kansas 66210 (US).

(81) Designated States (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY,

BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

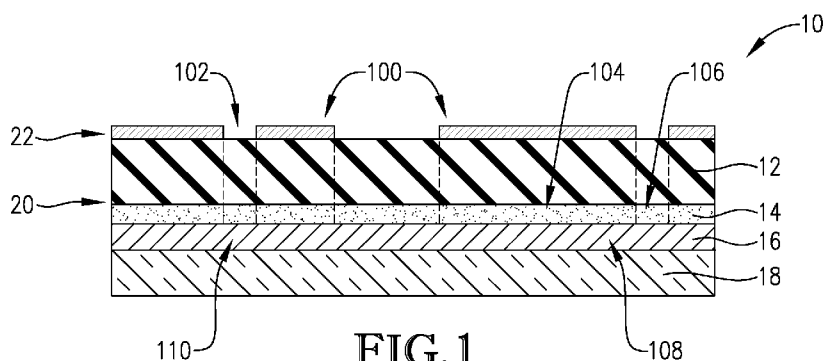
Declarations under Rule 4.17:

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))

Published:

- with international search report (Art. 21(3))

(54) Title: RAPID PCB PROTOTYPING BY SELECTIVE ADHESION



(57) Abstract: A PCB page blank includes a flexible substrate, a curable adhesive, a conductive layer, and a conductive layer support. The flexible substrate receives an opaque negative circuit pattern thereon. Portions of the curable adhesive not obscured by the circuit pattern may bond to portions of the conductive layer when exposed to light. The bonded portions of the conductive layer shear or tear from non-bonded portions of the conductive layer such that the bonded portions remain with the flexible substrate and the non-bonded portions remain with the conductive layer support when the flexible substrate and the conductive layer support are separated. The flexible substrate and the bonded portions of the conductive layer thus form a PCB prototype with the bonded portions of the conductive layer forming circuit traces of the circuit pattern.

RAPID PCB PROTOTYPING BY SELECTIVE ADHESION

BACKGROUND

[0001] Printed circuit boards (PCBs) are often used for prototyping circuit designs. However, constructing circuit patterns on PCBs is expensive and time consuming. For example, conventional methods used in the manufacturing of PCBs typically employ etchants, washes, and chemical deposition processes to selectively form layers of conductive material that produce the desired patterns. These chemical methods often require special processing or outsourcing to other manufacturers which can complicate and lengthen the PCB prototyping process. Other methods of manufacturing PCBs include milling operations or conductive ink printing, both of which require specialized equipment and have a high cost for materials. Also, most conductive ink processes lack the durability to solder electrical components to a physical circuit or electrical component.

SUMMARY

[0002] Embodiments of the present invention solve the above-mentioned problems and provide a distinct advancement in the art of PCB prototyping and manufacturing. More particularly, the present invention provides a prototyping method that is less expensive, less time consuming, and more readily available than conventional PCB prototyping processes.

[0003] Embodiments of the present invention provide a method of producing a fully functional and flexible PCB that can be used for testing, prototyping, and durable applications in a short amount of time. Resulting PCB prototypes may be utilized in engineering, manufacturing, education, and/or other environments where quick turnaround and low cost is important.

[0004] An embodiment of the present invention is a PCB page blank for prototyping a circuit design. The PCB page blank broadly includes a flexible substrate, a curable adhesive, a conductive layer, and a conductive layer support.

[0005] The flexible substrate may include a first side adjacent the curable adhesive and a second side for receiving an opaque negative circuit pattern thereon.

The flexible substrate may be transparent or translucent for receiving light between gaps in the opaque negative circuit pattern.

[0006] The curable adhesive may be positioned between the first side of the flexible substrate and the conductive layer and may be a glue, caulk, or similar adhesive material. Portions of the curable adhesive may be selectively cured via light, as described below.

[0007] The conductive layer may be positioned between the curable adhesive and the conductive layer support and may be a thin, flexible or rigid conductive sheet or film. Portions of the conductive layer may bond to the curable adhesive, as described below.

[0008] The conductive layer support protects and supports the conductive layer and may be positioned adjacent the conductive layer opposite the curable adhesive. The conductive layer support may be lightly adhered to the conductive layer so that portions of the conductive layer that do not bond to the curable adhesive remain with the conductive layer support, as described below.

[0009] Use of the PCB page blank will now be summarized. First, an opaque negative circuit pattern may be printed or formed on the second side of the flexible substrate so as to obscure portions of the flexible substrate and portions of the curable adhesive. Gaps in the opaque negative circuit pattern will leave portions of the flexible substrate and corresponding portions of the curable adhesive unobscured.

[0010] The unobscured portions of the flexible substrate and curable adhesive may then be exposed to light such as UV or near-UV light. This exposure may cause the unobscured portions of the curable adhesive to cure and thereby bond to corresponding portions of the conductive layer. The obscured portions of the curable adhesive will remain uncured and thus will not bond to the conductive layer.

[0011] The flexible substrate and the conductive layer may then be separated such that the portions of the conductive layer that are bonded to the curable adhesive are retained by the curable adhesive and the portions of the conductive layer that are not bonded to the curable adhesive are retained by the conductive layer support. That is, the conductive layer may be sheared or torn between bonded and non-bonded portions. The flexible substrate, curable adhesive, and bonded portions of the

conductive layer thus will form a prototyping circuit board with the bonded portions of the conductive layer thus forming one or more desired circuit traces of the circuit pattern. The PCB prototype may then be finished by soldering or connecting wires or other electronic components to the circuit traces.

[0012] The PCB page blank may be used with readily-available commercial or industrial-grade printers and computer systems such that the resulting circuit board may be formed without any additional tools. The circuit design may be produced on the PCB page blank without the need for major industrial equipment or harsh etching chemicals. The PCB page blank may be sized as needed to fit an available printer or to fit a desired application. The PCB page blank may also be one of a set of packaged PCB page blanks so that a number of prototyping iterations may be performed.

[0013] The above-described invention provides many advantages over conventional PCB prototyping and manufacturing methods. For example, the present invention circumvents outsourcing, complex processes, and the use of expensive materials. The present invention requires only a pre-formed sheet or "PCB page blank" and widely available consumer printing technology to create a fully functional PCB. The present invention only requires the ability to print an image (or negative image) of the desired circuit, selectively cure the adhesive, and then mechanically separate the layers of the PCB page blank. The resulting PCB is fully functional, solderable, and flexible. The present invention involves no chemical washes, etches, depositions, or machining processes and results in little to no material waste.

[0014] This summary is provided to introduce a selection of concepts in a simplified form that are further described below in the detailed description. This summary is not intended to identify key features or essential features of the claimed subject matter, nor is it intended to be used to limit the scope of the claimed subject matter. Other aspects and advantages of the present invention will be apparent from the following detailed description of the embodiments and the accompanying drawing figures.

BRIEF DESCRIPTION OF THE DRAWING FIGURES

[0015] Embodiments of the present invention are described in detail below with reference to the attached drawing figures, wherein:

[0016] FIG. 1 is a vertical cross section view of a PCB page blank constructed in accordance with an embodiment of the present invention;

[0017] FIG. 2 is a vertical cross section view of the PCB page blank of FIG. 1 with the flexible substrate and conductive layer support being partially separated;

[0018] FIG. 3 is a perspective view of a PCB prototype constructed from the PCB page blank of FIG. 1; and

[0019] FIG. 4 is a vertical cross section view of a double-sided PCB page blank constructed in accordance with another embodiment of the present invention.

[0020] The drawing figures do not limit the present invention to the specific embodiments disclosed and described herein. The drawings are not necessarily to scale, emphasis instead being placed upon clearly illustrating the principles of the invention.

DETAILED DESCRIPTION OF THE EMBODIMENTS

[0021] The following detailed description of the invention references the accompanying drawings that illustrate specific embodiments in which the invention can be practiced. The embodiments are intended to describe aspects of the invention in sufficient detail to enable those skilled in the art to practice the invention. Other embodiments can be utilized and changes can be made without departing from the scope of the present invention. The following detailed description is, therefore, not to be taken in a limiting sense. The scope of the present invention is defined only by the appended claims, along with the full scope of equivalents to which such claims are entitled.

[0022] In this description, references to “one embodiment”, “an embodiment”, or “embodiments” mean that the feature or features being referred to are included in at least one embodiment of the technology. Separate references to “one embodiment”, “an embodiment”, or “embodiments” in this description do not necessarily refer to the same embodiment and are also not mutually exclusive unless so stated and/or except

as will be readily apparent to those skilled in the art from the description. For example, a feature, structure, act, etc. described in one embodiment may also be included in other embodiments, but is not necessarily included. Thus, the current technology can include a variety of combinations and/or integrations of the embodiments described herein.

[0023] Turning now to FIGS. 1-3, a PCB page blank 10 constructed in accordance with an embodiment of the invention is illustrated. The PCB page blank 10 broadly comprises a flexible substrate 12, a curable adhesive 14, a conductive layer 16, and a conductive layer support 18.

[0024] The flexible substrate 12 may include a first side 20 adjacent the curable adhesive 14 and a second side 22 opposite the first side 20 for receiving an opaque negative circuit pattern 100 thereon, as shown in FIG. 1. The flexible substrate 12 may be transparent or translucent for receiving light therethrough and may be an electrical insulator. The flexible substrate 12 may be formed of flexible plastic, polyester, or any other suitable flexible material. For example, the flexible substrate 12 may be formed of Mylar, Kapton, polyimide, polyether ether ketone, or a similar material. The flexible substrate 12 may be easily sheared, torn, or cut so that the page 10 may be formed from larger sheets of stock material.

[0025] The curable adhesive 14 may be positioned between the first side 20 of the flexible substrate 12 and the conductive layer 16 and may cure locally when exposed to near-UV light, UV light, blue wavelength light, light in any other range on the light wavelength spectrum such as infrared light or ultraviolet light, heat, or any other stimulant. That is, portions of the curable adhesive 14 exposed to the light may cure while adjacent portions of the curable adhesive 14 not exposed to the light may remain uncured. Thus, selective bonding to the conductive layer 16 may be achieved, as described in more detail below. The curable adhesive 14 may be Loctite® brand adhesive or any suitable curable adhesive and may be applied to the flexible substrate 12 as a spray, dipped, or rolled-on material.

[0026] The conductive layer 16 may be positioned between the curable adhesive 14 and the conductive layer support 18 and may be configured to bond to portions of the curable adhesive 14, as described in more detail below. The conductive layer 16

may be formed of a thin conductive foil, metal, or any other suitable material and may be applied as a foil or spray. The conductive layer 16 may easily shear or tear when the flexible substrate 12 and the conductive layer support 18 are separated, as described below.

[0027] The conductive layer support 18 protects and supports the conductive layer 16 before use and may be positioned adjacent the conductive layer 16 opposite the curable adhesive 14. The conductive layer support 18 may be lightly adhered to the conductive layer 16 via a low-tack adhesive such that the adherence between the conductive layer support 18 and the conductive layer 14 is less than the adherence from the bond between the conductive layer 16 and the cured portions of the curable adhesive 14, as described in more detail below. The conductive layer support 18 may be a flexible or rigid material.

[0028] The PCB page blank 10 thus may be a multi-layered laminate page and may be configured to be sheared, torn, or cut from larger stock sheets, rolls, or strips. Similarly, the PCB page blank 10 may be sheared, torn, or cut into smaller pages or cards to fit into a printer or to suit a specific application. For example, the PCB page blank 10 may be a standard 8.5" by 11" or similar page size. The PCB page blank 10 may be packaged with additional pages as a multi-pack and may be packaged with printer ink, CAD software (described below), instructions, and/or additional circuit components and circuit prototyping tools.

[0029] Use of the PCB page blank 10 will now be described in more detail. First, a circuit pattern may be created using CAD software. The circuit pattern may be an entire circuit or only portions of a complete circuit design (i.e., a circuit board layout). The CAD software may include templates or forms for creating the circuit pattern. That is, a page size, page type, page thickness, and other parameters may be selected so that the CAD software may arrange and modify the circuit pattern to be compatible with and to fit onto the PCB page blank 10. The CAD software may also take into account the type of printer and the type of ink being used. Once the circuit pattern is created, the CAD software may generate a negative or positive circuit pattern to be printed in opaque ink onto the flexible substrate 12 of the PCB page blank 10.

[0030] The PCB page blank 10 may then be inserted or fed into the printer such that the opaque negative circuit pattern 100 is printed onto the second side 22 of the flexible substrate 12. The opaque negative circuit pattern 100 may alternatively be stamped, laser printed, or manually drawn onto the flexible substrate 12. The flexible substrate 12 may include portions of the opaque negative circuit pattern 100 pre-printed thereon such that only sections of the opaque negative circuit pattern 100 need to be printed. The opaque negative circuit pattern 100 may be partially or completely opaque so as to obscure portions of the flexible substrate 12 and corresponding portions 104 of the curable adhesive 14. Gaps 102 in the opaque negative circuit pattern 100 will leave other portions of the flexible substrate 12 and corresponding portions 106 of the curable adhesive 14 unobscured (delineated from obscured portions 104 by dashed lines in FIG. 1).

[0031] The PCB page blank 10 may then be exposed to light such that the light penetrates the portions of the flexible substrate 12 and the portions 106 of the curable adhesive 14 unobscured via the gaps 102 in the opaque negative circuit pattern 100. As mentioned above, the light may be UV light, near-UV light, blue wavelength light, or light of any suitable range in the wavelength spectrum. The unobscured portions 106 of the curable adhesive 14 exposed to the light may then cure so as to bond to corresponding portions 108 of the conductive layer 16.

[0032] The flexible substrate 12 and the conductive layer support 18 may then be separated from each other after the curable adhesive 14 has cured such that the bonded portions 108 of the conductive layer 16 are retained by the flexible substrate 12 and the non-bonded portions 110 of the conductive layer 16 are retained by the conductive layer support 18, as shown in FIG. 2. That is, the bonded portions 108 of the conductive layer 16 may be sheared or torn from the non-bonded portions 110 of the conductive layer 16. The separation of the flexible substrate 12 and the conductive layer support 18 may be performed via manual or machine peeling, or via any other suitable separation process.

[0033] The flexible substrate 12, curable adhesive 14, and bonded portions 108 of the conductive layer 16, after the bonded portions 108 of the conductive layer 16 are sheared or torn from the non-bonded portions 110, thus form a PCB prototype 200 with

the bonded portions 108 of the conductive layer 16 forming circuit traces 202 corresponding to the circuit pattern. Spaces 204 between the circuit traces 202 insulate the circuit traces 202 from each other. Wires and other electronic circuit components may then be soldered or connected to the circuit traces 202 to create a completed circuit. The conductive layer support 18 and the non-bonded portions 110 of the conductive layer 16 (that is, the sacrificial portions of the conductive layer 16 remaining with the conductive layer support 18) may be disposed or recycled.

[0034] The PCB prototype 200 may be used as a permanent circuit board if the prototyping process is complete or may be disposed or recycled if the circuit pattern needs refining or if a more permanent circuit board incorporating the desired circuit pattern is required. That is, the PCB prototype 200 may be configured for prototyping only or may be configured for use in long-term or robust applications. The PCB page blank 10 may be used with additional pages and/or other circuit components to create multiple layered flat circuits or circuits which may be applied to three dimensional contoured surfaces. The PCB prototype 200 may be rolled, bent, or flexed, or firm and rigid. The PCB prototype 200 may also be bonded to a surface having a three-dimensional contour via the uncured regions of the curable adhesive 14 or via another adhesive.

[0035] It will be understood that the layers of the PCB page blank 10 may be arranged in different orders or utilized in opposite fashion for creating the PCB prototype 200. For example, the conductive layer support 18 may form the base of the PCB prototype 200 while the flexible substrate 12 is removed. In this case, an opaque positive circuit pattern instead of an opaque negative circuit pattern would be deposited on the PCB page blank 10.

[0036] The above-described PCB page blank 10 provides several advantages over conventional circuit board prototyping systems. For example, the PCB page blank 10 may be used for rapidly producing a circuit prototype without the need for specialized equipment or harsh etching chemicals. To that end, the PCB page blank 10 may be used with readily-available commercial or industrial-grade printers and computer systems. The PCB page blank 10 may be sized as needed to fit an available printer or to fit a desired application. The PCB page blank 10 may be cut or stamped into any

desired two-dimensional shape such as a square, rectangle, circle, or irregular shape. The PCB page blank 10 may also be one of a set of packaged pages so that a number of prototyping iterations may be performed without the need for multiple purchases.

[0037] Another embodiment of the present invention is a double-sided PCB page blank 300, similar to the PCB page blank 10, except that the double-sided PCB page blank 300 may have an inverted set of layers on the second side of the conductive layer support 18, as shown in FIG. 4. Opaque circuit patterns may be printed on the surfaces of both flexible substrates such that the resulting double-sided PCB prototype has circuit traces on both sides. The conductive layer support 18 may include pre-formed vias (electrical paths) or interconnects extending between conductive layers 16 for connecting the opposing circuit traces. In this way, more complex circuit prototypes may be created with the double-sided PCB page blank 300

[0038] Although the invention has been described with reference to the embodiments illustrated in the attached drawing figures, it is noted that equivalents may be employed and substitutions made herein without departing from the scope of the invention as recited in the claims.

[0039] Having thus described various embodiments of the invention, what is claimed as new and desired to be protected by Letters Patent includes the following:

CLAIMS:

1. A PCB page blank comprising:
 - a flexible substrate having a first side and a second side opposite the first side;
 - a curable adhesive adjacent the first side of the flexible substrate, the flexible substrate being configured to receive an opaque negative circuit pattern on its second side such that the negative circuit pattern obscures portions of the curable adhesive and gaps in the negative circuit pattern render other portions of the curable adhesive unobscured, the unobscured portions of the curable adhesive being configured to cure when exposed to light traversing through the gaps in the negative circuit pattern;
 - a conductive layer adjacent the curable adhesive opposite the flexible substrate, portions of the conductive layer being configured to be bonded to the cured portions of the curable adhesive; and
 - a conductive layer support adjacent the conductive layer opposite the curable adhesive, the conductive layer support being configured to be separated from the flexible substrate such that the bonded portions of the conductive layer are retained by the flexible substrate and other portions of the conductive layer are retained by the conductive layer support so that the flexible substrate, curable adhesive, and bonded portions of the conductive layer form a circuit board with the bonded portions of the conductive layer forming one or more circuit traces.
2. The PCB page blank of claim 1, wherein the PCB page blank is configured to be fed through an ink printer for receiving the negative circuit pattern therefrom.
3. The PCB page blank of claim 1, wherein the flexible substrate and the conductive layer support are configured to be peeled apart.
4. The PCB page blank of claim 1, wherein the circuit traces are configured to be soldered to electrical components.

5. The PCB page blank of claim 1, wherein the first side of the flexible substrate includes pre-printed opaque circuit patterns of common connections or components.

6. The PCB page blank of claim 1, wherein the PCB page blank is configured to contain pre-formed conductive vias between layers.

7. The PCB page blank of claim 1, wherein the circuit board is configured to be bonded to a surface having a three-dimensional contour.

8. A method for producing a prototyping circuit board, the method comprising:

providing a flexible substrate having a curable adhesive adjacent a first side of the flexible substrate;

providing a conductive layer adjacent the curable adhesive opposite the flexible substrate;

providing a conductive layer support adjacent the conductive layer opposite the curable adhesive;

forming an opaque circuit pattern on a second side of the flexible substrate opposite the first side such that the circuit pattern obscures portions of the curable adhesive and other portions of the curable adhesive are unobscured via gaps in the circuit pattern;

exposing the flexible substrate and the circuit pattern to light such that some of the light traverses through the gaps in the opaque circuit pattern and through the flexible substrate so that portions of the curable adhesive not obscured by the circuit pattern cure and thereby bond to corresponding portions of the conductive layer and the opaque circuit pattern blocks at least some of the light so that portions of the curable adhesive obscured by the opaque circuit pattern do not cure and thereby do not bond to other portions of the conductive layer; and

separating the flexible substrate and the conductive layer support such that the portions of the conductive layer bonded to the curable adhesive are retained by the curable adhesive and the portions of the conductive layer not bonded to the curable adhesive are retained by the conductive layer support, the flexible substrate, curable adhesive, bonded portions of the conductive layer forming a circuit board with bonded portions of the conductive layer forming one or more circuit traces.

9. The method of claim 8, wherein the step of forming the opaque circuit pattern includes printing the opaque circuit pattern onto the flexible substrate.

10. The method of claim 9, wherein the step of forming the opaque circuit pattern includes printing the opaque circuit pattern via an ink printer.

11. The method of claim 8, further comprising the steps of designing a circuit pattern and generating the circuit pattern according to the circuit pattern via computer-aided design software.

12. The method of claim 8, wherein the step of separating the flexible substrate and the conductive layer support includes peeling the flexible substrate and the conductive layer apart from each other.

13. The method of claim 8, wherein the flexible substrate, curable adhesive, conductive layer, and conductive layer support cooperatively form a laminate page.

14. The method of claim 8, wherein the opaque circuit pattern is a positive circuit pattern.

15. The method of claim 8, wherein the step of forming the opaque circuit pattern includes manually drawing the opaque circuit pattern onto the flexible substrate.

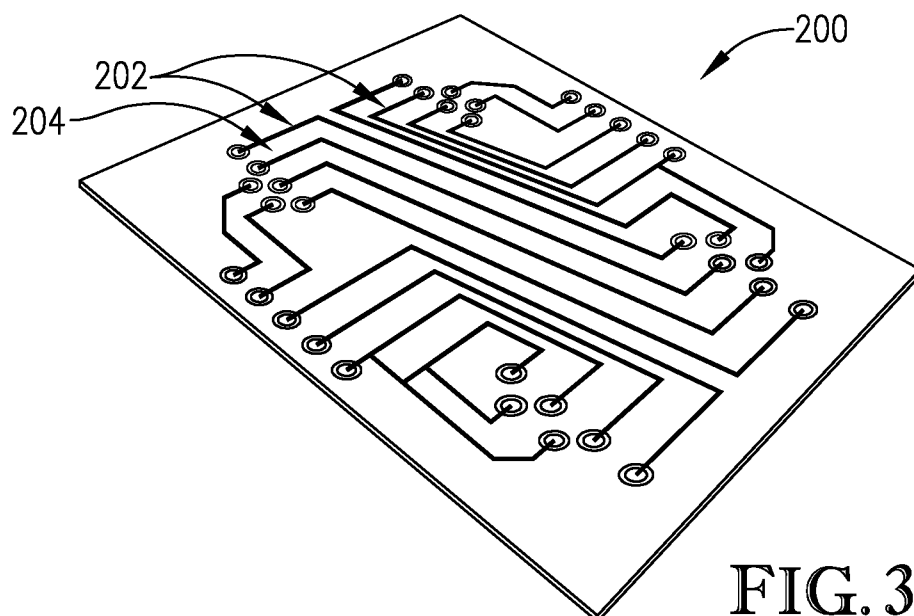
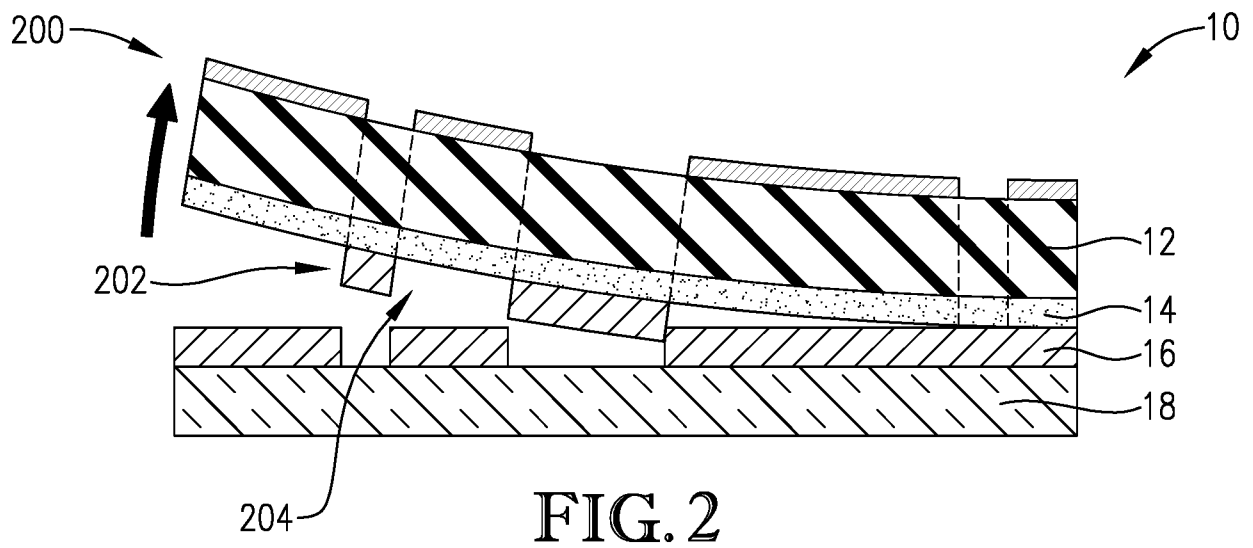
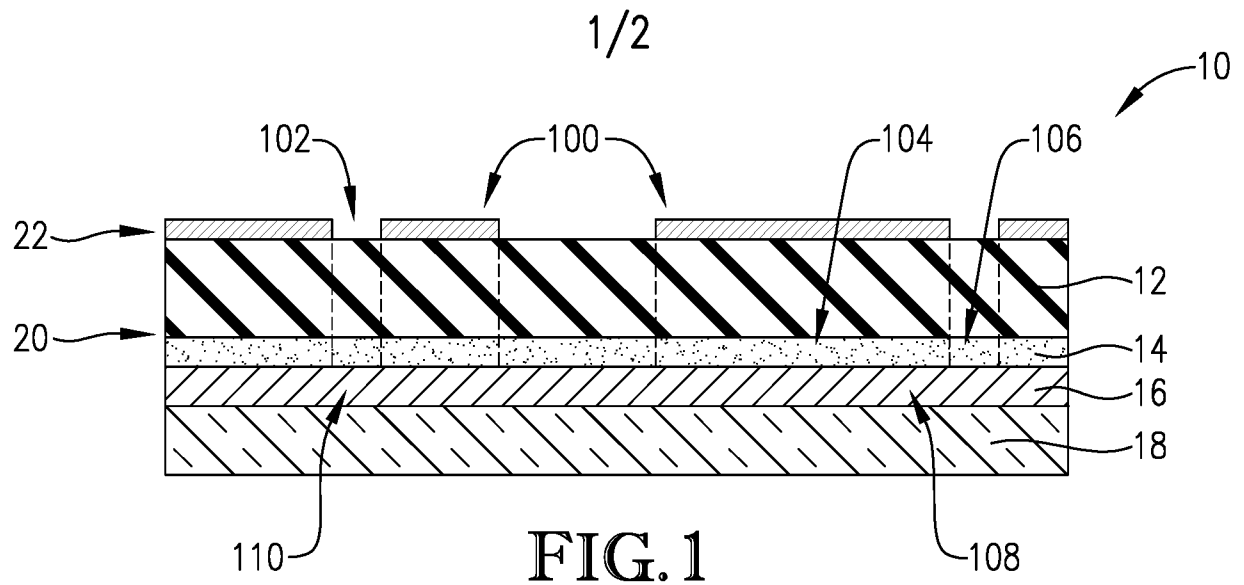
16. The method of claim 8, further comprising the step of bonding the circuit board to a surface having a three-dimensional contour.

17. The method of claim 8, wherein the conductive layer includes pre-cut regions.

18. The method of claim 8, wherein the second side of the flexible substrate includes pre-printed circuit design components.

19. The method of claim 8, wherein the circuit board is configured to be laminated so as to provide a protective layer or non-conductive barrier.

20. A PCB page blank comprising:
- a flexible substrate having a first side and a second side opposite the first side;
 - a curable adhesive adjacent the first side of the flexible substrate, the flexible substrate being configured to receive an opaque negative circuit pattern on its second side via a consumer-grade printer such that the negative circuit pattern obscures portions of the curable adhesive and gaps in the negative circuit pattern render other portions of the curable adhesive unobscured, the unobscured portions of the curable adhesive being configured to cure when exposed to near-UV light traversing through the gaps in the negative circuit pattern;
 - a conductive foil layer adjacent the curable adhesive opposite the flexible substrate, portions of the conductive foil layer being configured to be bonded to the cured portions of the curable adhesive; and
 - a conductive layer support adjacent the conductive foil layer opposite the curable adhesive, the conductive layer support being configured to be separated from the flexible substrate such that the bonded portions of the conductive foil layer are retained by the flexible substrate and other portions of the conductive foil layer are retained by the conductive layer support so that the flexible substrate, curable adhesive, and bonded portions of the conductive foil layer form a circuit board with the bonded portions of the conductive layer forming one or more circuit traces, the circuit traces being configured to be soldered to circuit wires.



2/2

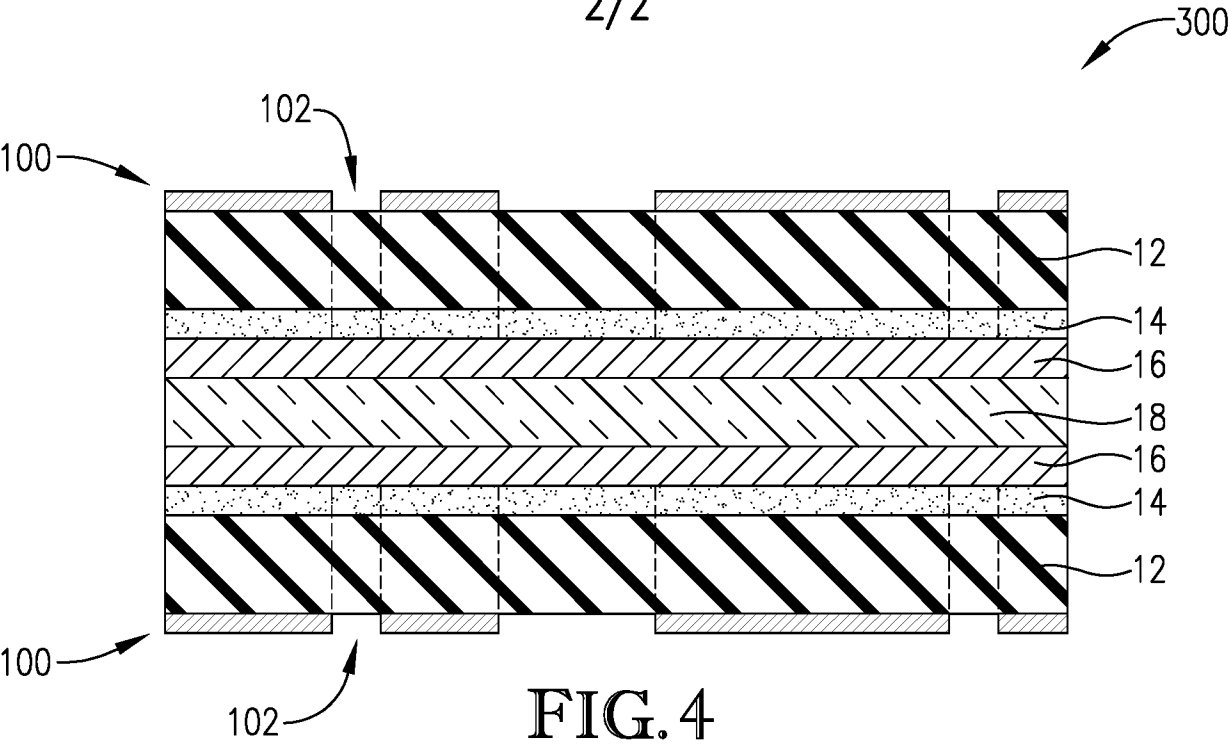


FIG. 4

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2016/064398**A. CLASSIFICATION OF SUBJECT MATTER****H05K 1/03(2006.01)i, H05K 1/02(2006.01)i, H05K 3/10(2006.01)i, H05K 3/38(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H05K 1/03; H05K 1/18; B32B 31/14; H01L 21/60; B44C 1/17; B32B 37/30; C09J 7/00; H05K 1/02; H05K 3/10; H05K 3/38

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: PCB, page blank, flexible substrate, curable adhesive, conductive layer, support, opaque negative circuit pattern

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2007-0102103 A1 (TOM KU) 10 May 2007 See paragraphs [0017]-[0025], claim 1 and figures 1A-1F.	1-20
A	US 2009-0120676 A1 (MICHAEL ROHM et al.) 14 May 2009 See paragraphs [0049]-[0052], claim 1 and figures 3a-3d.	1-20
A	US 5017255 A (CLYDE D. CALHOUN et al.) 21 May 1991 See column 3, lines 40-66 and figures 1-5.	1-20
A	JP 2001-345338 A (NITTO DENKO CORP. et al.) 14 December 2001 See paragraphs [0022]-[0040] and figures 1-4.	1-20
A	EP 0185998 A1 (DYNAMICS RESEARCH CORPORATION) 02 July 1986 See page 13 and figures 2L-2M.	1-20



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

20 February 2017 (20.02.2017)

Date of mailing of the international search report

20 February 2017 (20.02.2017)

Name and mailing address of the ISA/KR

International Application Division

Korean Intellectual Property Office

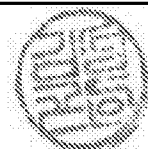
189 Cheongsa-ro, Seo-gu, Daejeon, 35208, Republic of Korea

Facsimile No. +82-42-481-8578

Authorized officer

KIM, Sung Gon

Telephone No. +82-42-481-8746



INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2016/064398

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2007-0102103 A1	10/05/2007	None	
US 2009-0120676 A1	14/05/2009	DE 102007051930 A1 EP 2056656 A2 EP 2056656 A3	30/04/2009 06/05/2009 14/03/2012
US 5017255 A	21/05/1991	EP 0384072 A3 JP 02-235681 A KR 10-1990-0011604 A US 5219655 A US 5328534 A	26/09/1990 18/09/1990 01/08/1990 15/06/1993 12/07/1994
JP 2001-345338 A	14/12/2001	JP 4477767 B2	09/06/2010
EP 0185998 A1	02/07/1986	JP 61-144093 A KR 10-1986-0005563 A	01/07/1986 23/07/1986