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- (81) **Designated States** (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
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(54) **Title:** GAS PHASE OXIDE REMOVAL AND PASSIVATION OF GERMANIUM-CONTAINING SEMICONDUCTORS AND COMPOUND SEMICONDUCTORS

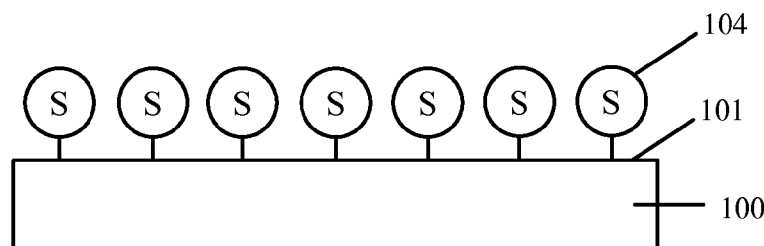


FIG. 1C

(57) **Abstract:** A method for gas phase oxide removal and passivation of germanium-containing semiconductors and compound semiconductors are disclosed in various embodiments. According to one embodiment of the invention, a method is provided for processing a semiconductor substrate. The method includes providing a substrate containing a germanium-containing semiconductor or a compound semiconductor, and exposing the substrate to a process gas containing a sulfur-containing gas and a nitrogen-containing gas that passivates a surface of the germanium-containing semiconductor or the compound semiconductor with sulfur. According to another embodiment, the germanium-containing semiconductor or the compound semiconductor has an oxidized layer thereon and the exposing to the process gas removes the oxidized layer from the substrate. According to another embodiment, the substrate may be treated with hydrogen fluoride (HF) gas and ammonia (NH₃) gas to remove the oxidized layer from the substrate before passivating the germanium-containing semiconductor or compound semiconductor with sulfur.



GAS PHASE OXIDE REMOVAL AND PASSIVATION OF GERMANIUM-
CONTAINING SEMICONDUCTORS AND COMPOUND SEMICONDUCTORS

CROSS-REFERENCE TO RELATED APPLICATIONS

- 5 **[0001]** This application is related to and claims priority to United States Provisional Patent Application serial no. 62/022,827 filed on July 10, 2014, the entire contents of which are herein incorporated by reference.

FIELD OF THE INVENTION

- 10 **[0002]** The present invention generally relates to a method of manufacturing a semiconductor device, and more particularly to a method of passivating surfaces of germanium-containing semiconductors and compound semiconductors during semiconductor device manufacturing.

BACKGROUND OF THE INVENTION

- 15 **[0003]** Semiconductor devices with a high-mobility channel, such as Ge and III-V semiconductors, offer the possibility of increased device performance beyond traditional Si-based devices. In particular, III-V materials such as GaAs, InGaAs, etc., are attractive candidates due to their lower effective mass and higher mobility for charge carriers compared to Si. Various processing steps in semiconductor device manufacturing require that oxides
20 that grow or are deposited on III-V materials be removed. Also, there is a need for the clean, oxide-free III-V surfaces to be passivated so that a reasonable amount of time can pass between removing the oxide and further processing, without significant oxide regrowth on the surfaces.

SUMMARY OF THE INVENTION

- 25 **[0004]** A method for gas phase oxide removal and passivation of germanium-containing semiconductors and compound semiconductors are disclosed in various embodiments.
 [0005] According to one embodiment of the invention, a method is provided for processing a semiconductor substrate. The method includes providing a substrate containing a
30 germanium-containing semiconductor or a compound semiconductor, and exposing the substrate to a process gas containing a sulfur-containing gas and a nitrogen-containing gas to

passivate a surface of the germanium-containing semiconductor or the compound semiconductor with sulfur. According to one embodiment, the germanium-containing semiconductor or the compound semiconductor has an oxidized layer thereon and the exposing to the process gas removes the oxidized layer from the substrate and passivates a surface of the germanium-containing semiconductor or the compound semiconductor with sulfur.

[0006] According to another embodiment, the method includes providing a substrate containing a germanium-containing semiconductor or a compound semiconductor, where the germanium-containing semiconductor or the compound semiconductor has an oxidized layer thereon, and treating the substrate with hydrogen fluoride (HF) gas and ammonia (NH₃) gas to remove the oxidized layer from the substrate. The method further includes exposing the substrate to a process gas containing a sulfur-containing gas and a nitrogen-containing gas to passivate a surface of the germanium-containing semiconductor or compound semiconductor with sulfur.

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] A more complete appreciation of the invention and many of the attendant advantages thereof will be readily obtained as the same becomes better understood by reference to the following detailed description when considered in connection with the accompanying drawings, wherein:

[0008] FIGs. 1A-1C schematically show a process flow for processing a semiconductor substrate according to an embodiment of the invention;

[0009] FIGs. 2A-2C schematically show a process flow for processing a semiconductor substrate according to another embodiment of the invention; and

[0010] FIGs. 3A-3E schematically show a process flow for processing a semiconductor substrate according to yet another embodiment of the invention.

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DETAILED DESCRIPTION OF SEVERAL EMBODIMENTS

[0011] According to one embodiment of the invention, a method is provided for processing a semiconductor substrate. The method includes providing a substrate containing a germanium-containing semiconductor or a compound semiconductor, and exposing the substrate to a process gas containing a sulfur-containing gas and a nitrogen-containing gas to

passivate a surface of the germanium-containing semiconductor or the compound semiconductor with sulfur. According to one embodiment, the germanium-containing semiconductor or the compound semiconductor has an oxidized layer thereon and the exposing to the process gas removes the oxidized layer from the substrate and passivates a surface of the germanium-containing semiconductor or the compound semiconductor with sulfur.

[0012] Liquid ammonium sulfide, $(\text{NH}_4)_2\text{S}$, is an etchant for native oxide on GaAs, and treatment of a GaAs surface by liquid ammonium sulfide passivates a bare (oxide free) GaAs surface. Removal of surface oxidation and subsequent surface passivation can be done using a sulfur-containing solution (e.g., liquid $(\text{NH}_4)_2\text{S}$), or a multistep combination of another liquid chemical (e.g., HCl) and a sulfur containing chemical. The inventors have realized that gas exposure methods with no liquid present are required to achieve oxide removal and/or passivation of semiconductor surfaces. This is in part due to increasingly more demanding requirements for good process control as critical dimensions (CDs) of device features become smaller and smaller.

[0013] FIGs. 1A-1C schematically show a process flow for processing a semiconductor substrate according to an embodiment of the invention. FIG. 1A shows a substrate 100 that can contain a germanium-containing semiconductor or a compound semiconductor. The substrate 100 can contain or consist of germanium (Ge). According to one embodiment, the substrate 100 may contain $\text{Si}_x\text{Ge}_{1-x}$ compounds, where x is the atomic fraction of Si, $1-x$ is the atomic fraction of Ge, and $0 < x < 1$. Exemplary $\text{Si}_x\text{Ge}_{1-x}$ compounds include $\text{Si}_{0.1}\text{Ge}_{0.9}$, $\text{Si}_{0.2}\text{Ge}_{0.8}$, $\text{Si}_{0.3}\text{Ge}_{0.7}$, $\text{Si}_{0.4}\text{Ge}_{0.6}$, $\text{Si}_{0.5}\text{Ge}_{0.5}$, $\text{Si}_{0.6}\text{Ge}_{0.4}$, $\text{Si}_{0.7}\text{Ge}_{0.3}$, $\text{Si}_{0.8}\text{Ge}_{0.2}$, and $\text{Si}_{0.9}\text{Ge}_{0.1}$. In one example, the substrate 100 can include a compressive-strained Ge layer or a tensile-strained $\text{Si}_x\text{Ge}_{1-x}$ ($x > 0.5$) deposited on a relaxed $\text{Si}_{0.5}\text{Ge}_{0.5}$ buffer layer.

[0014] According to one embodiment, the substrate 100 can include a compound semiconductor that contains or consists of a III-V semiconductor, a II-IV semiconductor, or a II-VI semiconductor, or a combination thereof. Examples include GaAs, InGaAs, and AlGaInP.

[0015] In FIG. 1B, a surface 101 of the substrate 100 is exposed to a process gas containing a sulfur-containing gas 103 (e.g., H_2S) and a nitrogen-containing gas 102 (e.g., NH_3). According to one embodiment, the sulfur-containing gas can include H_2S , SO_3 , or SF_6 , or a combination thereof. According to one embodiment, the nitrogen-containing gas can include NH_3 , N_2 , or N_2H_4 , or a combination thereof. According to one embodiment, the exposing to

the process gas can include a non-plasma process. According to another embodiment, the exposing to the process gas can include a light activated process (e.g., UV light), a plasma activated process, or a process that creates chemically reactive free radicals. The method can further include heat-treating the substrate 100 during or following the exposing to sublime one or more by-product materials formed on a surface of the germanium-containing semiconductor or the compound semiconductor.

[0016] Exemplary processing conditions for exposing the substrate 100 to the process gas include a substrate temperature between 20°C and 150°C, a partial pressure between 1mTorr and 3000mTorr for the sulfur-containing gas, a partial pressure between 1mTorr and 3000mTorr for the nitrogen-containing gas, and a total gas pressure between 20mTorr and 5000mTorr for the process gas. The process gas can include a diluent gas (e.g., Ar), where a dilution ratio for diluent gas/sulfur-containing gas can be between 0 and 1000.

[0017] As depicted in FIG. 1C, the exposure to the process gas passivates the surface 101 of the germanium-containing semiconductor or the compound semiconductor with sulfur 104. This surface passivation hinders subsequent oxidation of the surface 101 by the sulfur 104 occupying adsorption sites on the surface 101. This results in slowed oxidation of the surface 101, which allows for increased time to elapse before significant oxide growth occurs on the surface 101. The sulfur 104 may be removed from the surface 101 prior to performing additional device processing, for example by heat-treating the substrate 100 to desorb the sulfur 104. Additionally, or alternatively, the substrate 100 may be subjected to a plasma treatment to remove the sulfur 104 from the surface 101.

[0018] According to another embodiment of the invention, the method includes providing a substrate containing a germanium-containing semiconductor or a compound semiconductor, where the germanium-containing semiconductor or compound semiconductor has an oxidized layer thereon. The method further includes treating the substrate with hydrogen fluoride gas and ammonia gas to remove the oxidized layer from the substrate, and thereafter, exposing the substrate to a process gas containing a sulfur-containing gas and a nitrogen-containing gas that passivates a surface of the germanium-containing semiconductor or the compound semiconductor with sulfur.

[0019] FIGs. 2A-2C schematically show a process flow for processing a semiconductor substrate according to another embodiment of the invention. The embodiment described in FIGs. 2A-2C is similar to the embodiment described in FIGs. 1A-1C but, as depicted in FIG.

2A, the substrate 200 further contains an oxidized layer 205 formed on a surface 201. The oxidized layer 205 can include an oxidized form of the substrate 200 (e.g., SiGeO_x) or a material deposited on the substrate 200.

[0020] In FIG. 2B, the oxidized layer 205 is exposed to a process gas containing a sulfur-containing gas 203 (e.g., H_2S) and a nitrogen-containing gas 202 (e.g., NH_3). As depicted in FIG. 2C, the exposure of the process gas removes the oxidized layer 205 and passivates the surface 201 of the germanium-containing semiconductor or the compound semiconductor with sulfur 204.

5 [0021] FIGs. 3A-3E schematically show a process flow for processing a semiconductor substrate according to yet another embodiment of the invention. The embodiment described in FIGs. 3A-3E is similar to the embodiment described in FIGs. 2A-2C and, as depicted in FIG. 3A, the substrate 300 contains an oxidized layer 305 formed on a surface 301. The oxidized layer 305 can include an oxidized form of the substrate 300 (e.g., SiGeO_x) or a material deposited on the substrate 300.

[0022] In FIG. 3B, the oxidized layer 305 is treated with hydrogen fluoride (HF) gas 307 and ammonia (NH_3) 306 gas to remove the oxidized layer 305 from the substrate 300. Exemplary processing conditions can include a substrate temperature between 20°C and 150°C , a partial pressure between 1mTorr and 3000mTorr for the HF gas, a partial pressure between 1mTorr and 3000mTorr for the NH_3 gas, and a total gas pressure between 20mTorr and 5000mTorr for the process gas. The process gas can include a diluent gas (e.g., Ar), where a dilution ratio of diluent gas/HF gas can be between 0 and 1000. In one example, the substrate 300 may be heat-treated during or following the treating with the HF gas 307 and NH_3 gas 306 to sublime one or more by-product materials formed on the surface 301.

[0023] As depicted in FIG. 3C, the exposure of the removes the oxidized layer 305 from the surface 301.

[0024] Thereafter, as shown in FIG. 3D, the surface 301 is exposed to a process gas containing a sulfur-containing gas 303 (e.g., H_2S) and a nitrogen-containing gas 302 (e.g., NH_3). As depicted in FIG. 3E, the exposure to the process gas passivates the surface 301 with sulfur 304.

[0025] Semiconductor manufacturing equipment such as the CertasTM and Certas WINGTM available from Tokyo Electron Limited, Akasaka, Japan, may be used for performing the gas exposure processes described in embodiments of the invention. These manufacturing

equipment are single wafer processing tools, however it is also possible to perform the gas exposure processes in a batch configuration where multiple substrates are simultaneously processed.

[0026] CertasTM and Certas WINGTM tools include chemical oxide removal (COR) units. COR units may be used in a process that uses NH₃ and HF or NH₃ and H₂S as a process gas to strip a native oxide from a GaAs surface and passivate the stripped surface with sulfur. A post-COR type process may be applied and may comprise a post heat treatment (PHT) to sublime by-products formed on the substrate during the COR type process. The PHT system may be carried out over a range of temperatures, for example, from about 100°C to about 300°C, and at a process pressure between about 1mTorr to about 1 Torr.

[0027] A plurality of embodiments for gas phase oxide removal and passivation of germanium-containing semiconductors and compound semiconductors have been disclosed in various embodiments. The foregoing description of the embodiments of the invention has been presented for the purposes of illustration and description. It is not intended to be
5 exhaustive or to limit the invention to the precise forms disclosed. This description and the claims following include terms that are used for descriptive purposes only and are not to be construed as limiting.

[0028] Persons skilled in the relevant art can appreciate that many modifications and variations are possible in light of the above teaching. Persons skilled in the art will recognize
10 various equivalent combinations and substitutions for various components shown in the Figures. It is therefore intended that the scope of the invention be limited not by this detailed description, but rather by the claims appended hereto.

What is claimed is:

1. A method for forming a semiconductor device, the method comprising:
providing a substrate containing a germanium-containing semiconductor or a
compound semiconductor; and
5 exposing the substrate to a process gas containing a sulfur-containing gas and a
nitrogen-containing gas to passivate a surface of the germanium-containing
semiconductor or the compound semiconductor with sulfur.
2. The method of claim 1, wherein the germanium-containing semiconductor or the
10 compound semiconductor has an oxidized layer thereon and the exposing removes the
oxidized layer from the substrate.
3. The method of claim 2, wherein the oxidized layer includes an oxidized form of the
germanium-containing semiconductor or the compound semiconductor, or a material
15 deposited on the substrate.
4. The method of claim 1, wherein the sulfur-containing gas includes H_2S , SO_3 , or SF_6 ,
or a combination thereof.
- 20 5. The method of claim 1, wherein the nitrogen-containing gas includes NH_3 , N_2 , N_2H_4 ,
or a combination thereof.
6. The method of claim 1, wherein the exposing includes a non-plasma process.
- 25 7. The method of claim 1, wherein the exposing includes a light activated process or a
plasma activated process.
8. The method of claim 1, wherein the germanium-containing semiconductor is selected
from the group consisting of Ge and $\text{Si}_x\text{Ge}_{1-x}$ compounds.
- 30 9. The method of claim 1, wherein the compound semiconductor is selected from the
group consisting of a III-V semiconductor, a II-IV semiconductor, and a II-VI
semiconductor.

10. The method of claim 1, wherein the compound semiconductor is selected from the group consisting of GaAs, InGaAs, and AlGaInP.
- 5 11. The method of claim 1, wherein the germanium-containing semiconductor or the compound semiconductor has an oxidized layer thereon, the method further comprising treating the substrate with hydrogen fluoride gas and ammonia gas to remove the oxidized layer from the substrate.
- 10 12. The method of claim 11, further comprising heat-treating the substrate during or following the treating.
13. The method of claim 1, further comprising heat-treating the substrate during or following the exposing to sublime one or more
15 by-product materials formed on a surface of the germanium-containing semiconductor or the compound semiconductor.
14. A method for forming a semiconductor device, the method comprising:
providing a substrate containing a germanium-containing semiconductor or a
20 compound semiconductor, wherein the germanium-containing semiconductor or compound semiconductor has an oxidized layer thereon;
treating the substrate with hydrogen fluoride gas and ammonia gas to remove the oxidized layer from the substrate; and
thereafter, exposing the substrate to a process gas containing a sulfur-containing gas
25 and a nitrogen-containing gas that passivates a surface of the germanium-containing semiconductor or the compound semiconductor with sulfur.
15. The method of claim 14, wherein the oxidized layer includes an oxidized form of the germanium-containing semiconductor or the compound semiconductor, or a material
30 deposited on the substrate.
16. The method of claim 14, wherein the sulfur-containing gas includes H₂S, SO₃, or SF₆, or a combination thereof.

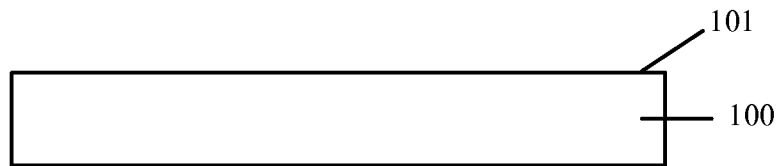
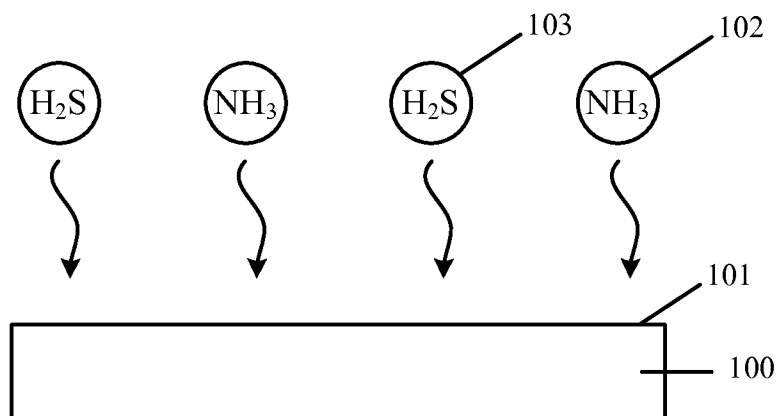
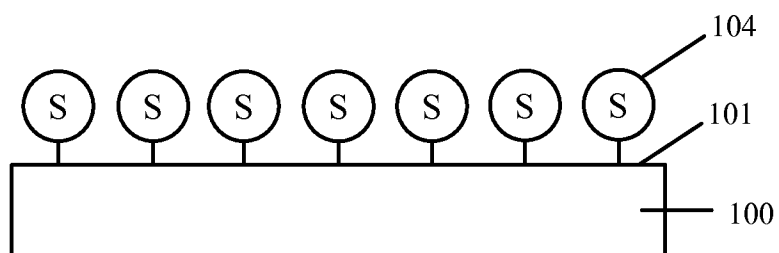
17. The method of claim 14, wherein the nitrogen-containing gas includes NH_3 , N_2 , N_2H_4 , or a combination thereof.

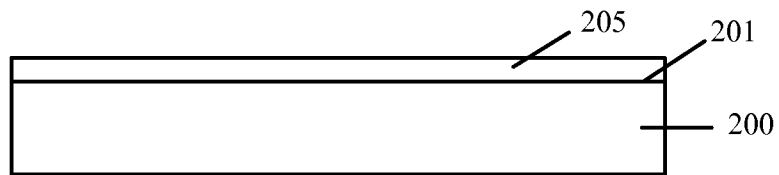
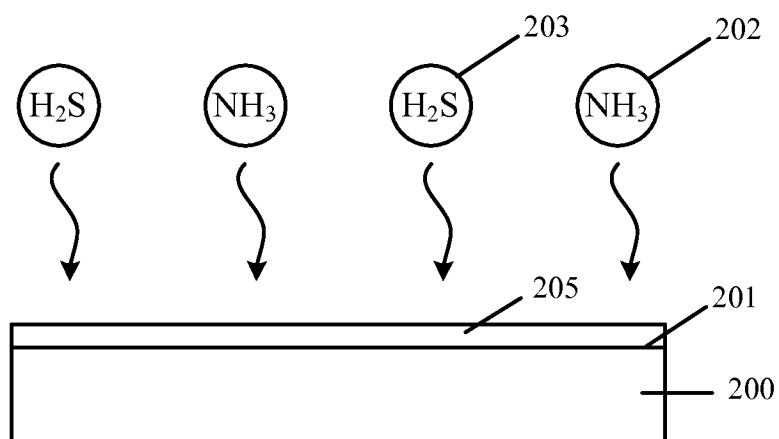
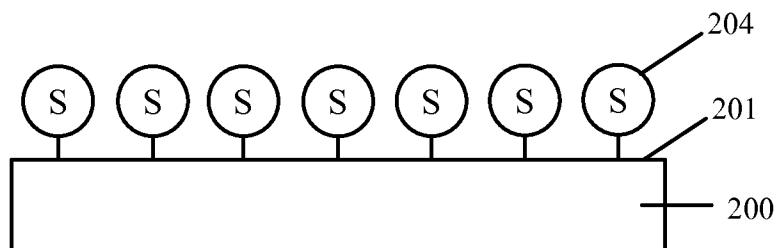
5 18. The method of claim 14, wherein the germanium-containing semiconductor is selected from the group consisting of Ge and $\text{Si}_x\text{Ge}_{1-x}$ compounds.

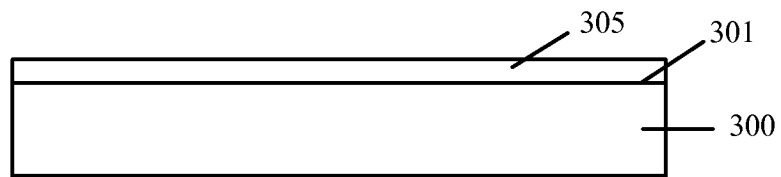
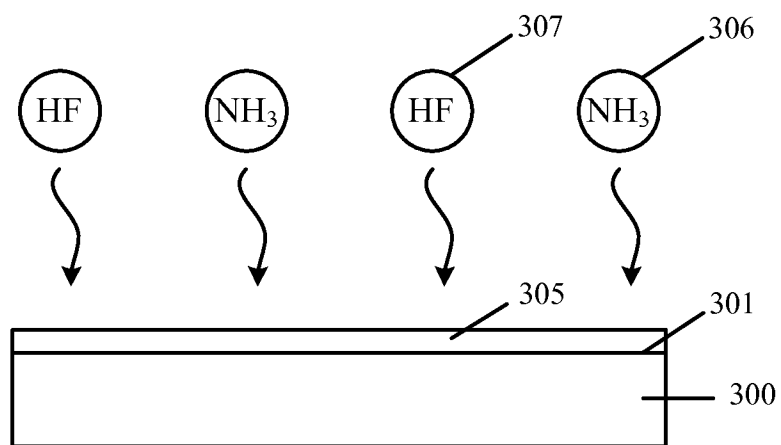
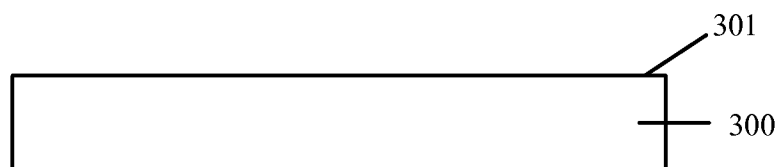
19. The method of claim 14, wherein the compound semiconductor is selected from the group consisting of a III-V semiconductor, a II-IV semiconductor, and a II-VI semiconductor.

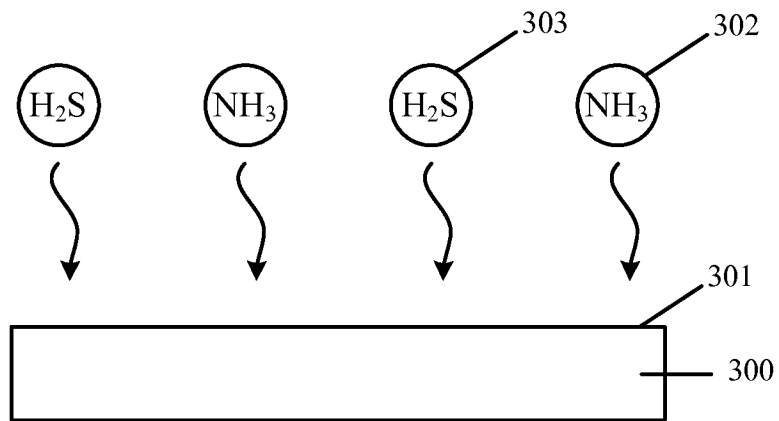
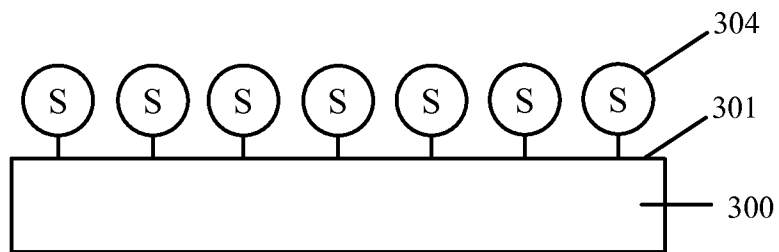
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20. The method of claim 14, wherein the compound semiconductor is selected from the group consisting of GaAs, InGaAs, and AlGaInP.

1/4**FIG. 1A****FIG. 1B****FIG. 1C**

2/4**FIG. 2A****FIG. 2B****FIG. 2C**

3/4**FIG. 3A****FIG. 3B****FIG. 3C**

4/4**FIG. 3D****FIG. 3E**

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2015/039726**A. CLASSIFICATION OF SUBJECT MATTER****H01L 21/302(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/302; H01L 29/47; H01L 21/324; H01L 21/67; H01L 21/30; H01L 21/02; H01L 21/44; H01L 21/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: sulfur, nitrogen, oxide, passivation, germanium, compound, heating and removing

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2014-0027884 A1 (ASM IP HOLDING B.V.) 30 January 2014 See abstract, paragraphs [0023]-[0035], [0050]-[0052] and claim 1.	1-20
A	US 2012-0264309 A1 (JOEL MYRON BARNETT et al.) 18 October 2012 See abstract, paragraphs [0019]-[0030] and claim 1.	1-20
A	US 5407869 A (PAUL H. HOLLOWAY et al.) 18 April 1995 See abstract, column 3, line 57 - column 4, line 59 and claim 1.	1-20
A	KR 10-0757738 B1 (INDUSTRY-ACADEMIC COOPERATION FOUNDATION, YONSEI UNIVERSITY) 12 September 2007 See abstract, paragraphs [0031]-[0032], claim 1 and figures 1-2.	1-20
A	US 2004-0115908 A1 (PHILBERT FRANCIS MARSH et al.) 17 June 2004 See abstract, paragraphs [0017]-[0022], claim 1 and figure 1.	1-20



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

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"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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"&" document member of the same patent family

Date of the actual completion of the international search

20 October 2015 (20.10.2015)

Date of mailing of the international search report

20 October 2015 (20.10.2015)

Name and mailing address of the ISA/KR

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2015/039726

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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