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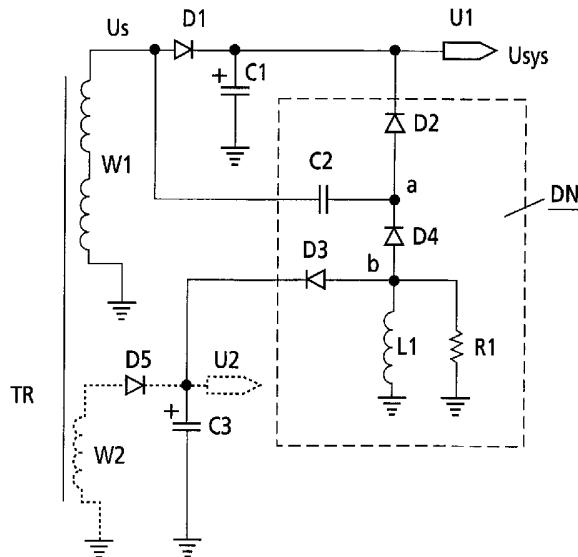
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(54) Title: SWITCHED-MODE POWER SUPPLY WITH A DAMPING NETWORK



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(57) Abstract: The switched-mode power supply contains a transformer (TR), a switching transistor (T1) in series with the primary winding (WP) of the transformer (TR), and a damping network (DN) on the secondary side, which has a connection for a further secondary winding (W2) via which energy is transferred from this damping network, by way of example, to a charge-storage capacitor (C3) which is coupled to this secondary winding. The connection is advantageously produced via a rectifying element, for example a diode (D3), which is coupled after the rectifying diode (D5) for the second secondary winding (W2). In a further exemplary embodiment, the diode (D2) in the series circuit, which is connected in parallel with the rectifier diode (D1) for the first secondary winding (W1), is not coupled to the charge-storage capacitor (C1) for the first secondary winding (W1), but is likewise coupled to the charge-storage capacitor (C3) for the second secondary winding (W2).



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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Switched-mode power supply with a damping network

The invention is based on a switched-mode power supply having a transformer and having a switching transistor,
5 which is connected in series with the primary winding of the transformer. Switched-mode power supplies of this type are used, for example, in televisions, video recorders or computer monitors, and preferably operate as flyback converters, in a power range from about 50 to 150 watts.

10

In the flyback converter mode, energy is stored in the transformer during the phase in which the switching transistor is switched on, and this is subsequently transmitted to the secondary side, during the phase in
15 which the switching transistor is switched off. In this case, however, disturbing voltage spikes are produced when the switching transistor switches off, and these lead to a severe voltage load on the switching transistor, as well as causing radiated interference. It is thus known for a
20 damping network, also referred to as a snubber network, to be provided in parallel with the primary winding, and this network is used to limit the voltage spikes of the current input of the switching transistor when the switching transistor switches off. This network normally has a
25 capacitor in parallel with the primary winding, which capacitor is charged when the switching transistor is switched off, and whose energy is then emitted to an energy-storage capacitor on the input side, or is consumed in a resistor and converted into heat. A damping network of
30 this type is known, by way of example, from DE-A-40 29 221.

EP-A-0 695 023 discloses a switched-mode power supply which, in addition to a damping network in parallel with the primary winding, also has a second damping network in
35 parallel with a winding on the secondary side of the transformer. In this case, a series circuit formed by a capacitor and a second diode is connected in parallel with a rectifying diode, which produces a desired output voltage

across a charge-storage capacitor, and the centre point of this series circuit is connected to a reference potential via a diode and an inductance. Energy is in this case likewise transferred to the charge-storage capacitor via 5 this capacitor, for energy recovery purposes, when the switching transistor switches off.

The method of operation of this switch-mode power supply will now be described in more detail with reference to 10 Figure 1. This switched-mode power supply has a network connection UN, to which a network rectifier 1 followed by an energy-storage capacitor C0 are connected, across which a DC voltage U0 is produced. This is applied to a primary winding WP of a transformer TR, in series with which a 15 switching transistor T1 is connected. The switching transistor T1 is driven in a known manner by a driver circuit 2, illustrated only symbolically here, with switching pulses 3. The switched-mode power supply in this case operates in particular in the flyback converter mode, 20 in which energy is transferred from the transformer TR to one or more secondary windings during the phase in which the switching transistor T1 is switched off.

A damping network for damping voltage spikes when the 25 transistor T1 is switched off is connected in parallel with the primary winding WP. This damping network has a series circuit formed by a capacitor Cs and a diode Ds, with a resistor Rs being connected in parallel with its diode. The voltage spikes which are produced when the switching 30 transistor T1 switches off are in this case absorbed by the capacitor Cs and are partially fed back via the diode Ds to the energy-storage capacitor C0, while the remainder is converted into heat in the resistor Rs.

35 On the secondary side, the switched-mode power supply has a secondary winding W1, which provides a desired output voltage U1 across a rectifier diode D1 and a charge-storage capacitor C1. In order to suppress voltage spikes and in

order to reduce the power losses in the switched-mode power supply, a damping network is likewise coupled to the secondary winding W1. This damping network has a first series circuit with a capacitor C2 and a diode D2 in parallel with the diode D1, as well as a second series circuit with an inductance L1 and a diode D4, which is arranged between a tap a on the first series circuit and a reference potential. A resistor R1 is also connected in parallel with the inductance L1 and is used to damp oscillations between this inductance and the capacitor C2.

The network on the secondary side, as shown in Figure 1, operates as follows: during the phase in which the switching transistor T1 is switched on, the voltage Us on the secondary winding W1 is negative, so that the capacitor C2 is charged via the inductance L1 and the diode D4. Thus, in the process, energy is stored in the capacitor C2, with the inductance L1 limiting the current rise during the charging process. When the switching transistor T1 is switched off, the voltage Us rises rapidly, until, in the end, the voltage U1 is reached, and the diode D1 changes to the forward-biased state, with the charge-storage capacitor C1 thus once again being recharged. However, the output voltage U1 is reached at an earlier stage across the capacitor C2, since the centre point a of the series circuit formed by C2 and D2 is positive with respect to the voltage Us. The diode D2 thus becomes forward-biased before the diode D1, so that the voltage rise of the voltage Us is damped. The energy in the capacitor C2 is likewise transferred to the charge-storage capacitor C1 during this process. In this case, any oscillations which occur between the capacitor C2 and the inductance L1 are damped by the resistor R1. Further details of the operation of this damping network are described in EP-A-0 695 023, which is hereby referred to.

A further damping network with a damping network on the primary side and a damping network on the secondary side is known from EP-A-0 279 335.

5 The object of the present invention is to reduce the power losses in a damping network on the secondary side of the previous type.

10 This object is achieved by a switched-mode power supply according to Claim 1. Advantageous developments of the invention are specified in the dependent claims.

According to the invention, the damping network has a connection for a second secondary winding, via which energy 15 is transferred from this damping network, for example, to a charge-storage capacitor which is coupled to this secondary winding. The connection is advantageously produced via a rectifying element, for example a diode, which is coupled after the rectifying diode of the second secondary winding, 20 thus preventing this secondary winding from having any reaction on the damping network.

In a further exemplary embodiment, the diode in the series circuit which is connected in parallel with the rectifier 25 diode of the first secondary winding is not coupled to the charge-storage capacitor for the first secondary winding, but is likewise coupled to the charge-storage capacitor for the second secondary winding. This is particularly advantageous when the operating voltage of the first 30 secondary winding is considerably higher than the operating voltage of the second secondary winding, so that this capacitor is discharged to a greater extent, and the damping characteristics of the network when the switching transistor is switched off are improved. An LC low-pass 35 filter is also arranged, in particular, in the connection between this capacitor and the charge-storage capacitor for the second secondary winding, and is used to prevent disturbing voltage spikes on this charge-storage capacitor.

The invention will be explained in more detail in the following text with reference, by way of example, to schematic drawings, in which:

5

Figure 1 shows a switched-mode power supply with a damping network according to the prior art arranged on the secondary side,

Figure 2 shows a damping network according to the invention arranged on the secondary side, which supplies energy not only to a charge-storage capacitor for a first secondary winding, but also to a charge-storage capacitor for a second secondary winding, and

15 Figure 3 shows a damping network arranged on the secondary side, which has two connections for a second secondary winding.

The switched-mode power supply according to the invention 20 operates, by way of example, on the flyback converter principle, with a switching transistor in series with a primary winding of the transformer, as explained with reference to Figure 1. However, it is not restricted to this principle. Reference should be made in particular to 25 EP-A-0 695 023 for further details relating to the operation of this switched-mode power supply. Figures 2 and 3 show preferred exemplary embodiments of the invention, which relate to an improved damping network arranged on the secondary side. Identical components are thus provided with 30 the same reference symbols. Furthermore, in Figures 2 and 3, the part of the switched-mode power supply which is on the primary side as well as further details of the secondary side, in particular regulation elements for voltage stabilization and further secondary windings, are 35 not shown, since these do not relate to the present invention.

Figure 2 shows, schematically, a transformer TR for a switched-mode power supply with secondary windings W1 and W2, and arranged on the secondary side. The secondary winding W1 in this case produces a first operating voltage 5 U1 via a diode D1 and a charge-storage capacitor C1, and the secondary winding W2 produces a second operating voltage U2 via a diode D5 and a charge-storage capacitor C3. The operating voltages U1 and U2 are used in particular for supplying electronic circuits in a television.

10

Furthermore, the circuit as shown in Figure 2 has a damping network DN, which is coupled on the input side to the secondary winding W1 and on the output side to the charge-storage capacitors C1 and C3. It contains a series 15 circuit comprising a capacitor C2 and a diode D2 in parallel with the diode D1, by means of which energy is transferred from the secondary winding W1 to the charge-storage capacitor C1, as already described in the introduction. Furthermore, a series circuit comprising an 20 inductance L1, for example a coil with an inductance of 100 μ H, and a diode D4 is coupled to a junction point a between the capacitor C2 and the diode D2, thus allowing a current to flow from a reference potential in the direction of the junction point a. A resistor R1 is also connected in 25 parallel with the inductance L1. To this extent, this circuit corresponds to the damping network as described in Figure 1, with the exception of the change in the sequence of L1 and D4

30 According to the invention, a junction point between the junction point a and the inductance L1, in this exemplary embodiment a junction point b between the diode D4 and L1, is now connected to a second secondary winding W2 of the transformer TR. The connection is in this case produced via 35 a diode D3, by means of which the inductance L1 is coupled to the charge-storage capacitor C3.

This circuit in this case operates as follows: when the switching transistor in the switched-mode power supply is switched on, the voltage U_S on the secondary winding W_1 is negative, so that the capacitor C_2 is charged via the diode 5 D_4 and the inductance L_1 , corresponding to the negative forward voltage on this secondary winding. L_1 in this case limits the charging current and in consequence results in the capacitor C_2 being charged slowly when the switching transistor switches on.

10

When the switching transistor switches off, the voltage U_S rises very rapidly and, when the voltage at the junction point a reaches the voltage U_1 , the diode D_2 becomes forward-biased, so that the capacitor C_2 is discharged in 15 the direction of the charge-storage capacitor C_1 . In the process, the diode D_2 becomes forward-biased as a result of the positive voltage across the capacitor C_2 upstream of the diode D_1 so that, in consequence, the voltage rise of the voltage U_S is limited, and the coupling between the 20 transformer windings W_P , W_1 likewise limits the voltage rise across the switching transistor T_1 . The charge which the capacitor C_2 taps off from the secondary winding W_1 in the process is in consequence fed back into the system once again, and is thus not lost. However, oscillations occur in 25 this case between the capacitor C_2 and the inductance L_1 , and these must be damped by the resistor R_1 . However, these oscillations are now additionally damped by the diode D_3 , so that the power loss in the resistor R_1 is considerably reduced.

30

Figure 3 shows a further exemplary embodiment of a damping network according to the invention, arranged on the secondary side. However, in this case, the diode D_2 is not coupled to the charge-storage capacitor C_1 but, in the same 35 way as the diode D_3 , is likewise coupled to the charge-storage capacitor C_3 for the secondary winding W_2 . This is particularly advantageous when the voltage U_1 is considerably greater than the voltage U_2 . In this exemplary

embodiment, the voltage U_1 is the system voltage U_{sys} of, by way of example, 130 volts, and the operating voltage U_2 is a voltage of 10 volts, so that the capacitor C_2 can be discharged to a greater extent when the voltage U_s on the 5 secondary winding W_1 is high. This achieves a greater damping effect.

Since, in this exemplary embodiment, the operating voltage U_2 is used as a video supply voltage, a filter element, 10 comprising an inductance L_3 and a capacitor C_4 , is also arranged between the diode D_2 and the charge-storage capacitor C_3 . This avoids picture interference resulting from voltage spikes on the supply voltage U_2 .

15 Further refinement forms of the invention are within the scope of a person skilled in the art and, by way of example, the diode D_2 may also be connected to a further secondary winding of the transformer TR , instead of being connected to the secondary winding W_2 . In the exemplary 20 embodiments explained here and in the claims, diodes are preferably used as the rectifying elements. However, within the scope of this invention, other rectifying elements, for example transistors which are switched for voltage rectification purposes, may also be used instead of diodes.

Patent Claims

1. Switched-mode power supply having a transformer (TR) and a switching transistor (T1) in series with the primary winding (WP) of the transformer (TR),
5 whose first secondary winding (W1) is connected via a first diode (D1) to a charge-storage capacitor (C1) which supplies an operating voltage (U1), and is connected via a series circuit with a second diode (D2) to this charge-storage capacitor (C1),
10 and a junction point (a) of the series circuit (D2, C2) is connected via an inductance (L1) to a reference potential, **characterized** in that a second junction point (b) between the inductance (L1) and the first junction point (a) is connected to a second
15 secondary winding (W2) of the transformer (TR).

2. Switched-mode power supply according to Claim 1,
20 **characterized** in that the second junction point (b) is coupled via a rectifying element (D3), in particular a third diode (D3), to a charge-storage capacitor (C3) for the second secondary winding (W2).

3. Switched-mode power supply according to Claim 1 or 2,
25 **characterized** in that a fourth diode (D4) is arranged between the reference potential and the first junction point (a).

4. Switched-mode power supply according to Claim 3,
30 **characterized** in that the fourth diode is arranged between the first inductance (L1) and the first junction point (a), and in that the anode of the rectifying element (D3) is coupled to a junction point (b) between the first inductance (L1) and the fourth diode (D4).

35 5. Switched-mode power supply according to one of the preceding claims, **characterized** in that the series

circuit has a second capacitor (C2) in series with the second diode (D2), and in that the first junction point (a) is arranged between the second capacitor (C2) and the second diode (D2).

5

6. Switched-mode power supply according to one of the preceding claims, **characterized** in that the second diode (D2) is not coupled to the first secondary winding (W1), but is coupled to the second winding (W2) or to a further secondary winding on the transformer (TR).
- 10
7. Switched-mode power supply according to Claim 6, **characterized** in that the cathode of the second diode (D2) is coupled to a charge-storage capacitor (C3) for this secondary winding (W2).
- 15
8. Switched-mode power supply according to Claim 6 or 7, **characterized** in that a filter (L2, C4) is arranged between the second diode (D2) and this secondary winding (W2).
- 20
9. Switched-mode power supply according to Claim 8, **characterized** in that the filter (L2, C4) is an LC low-pass filter.
- 25
10. Switched-mode power supply according to one of the preceding claims, **characterized** in that the first operating voltage (U1) is the system voltage of a television or of a monitor and is, by way of example, more than 100 V, and the second operating voltage (U2) is a low operating voltage, in particular of less than 20 V.
- 30

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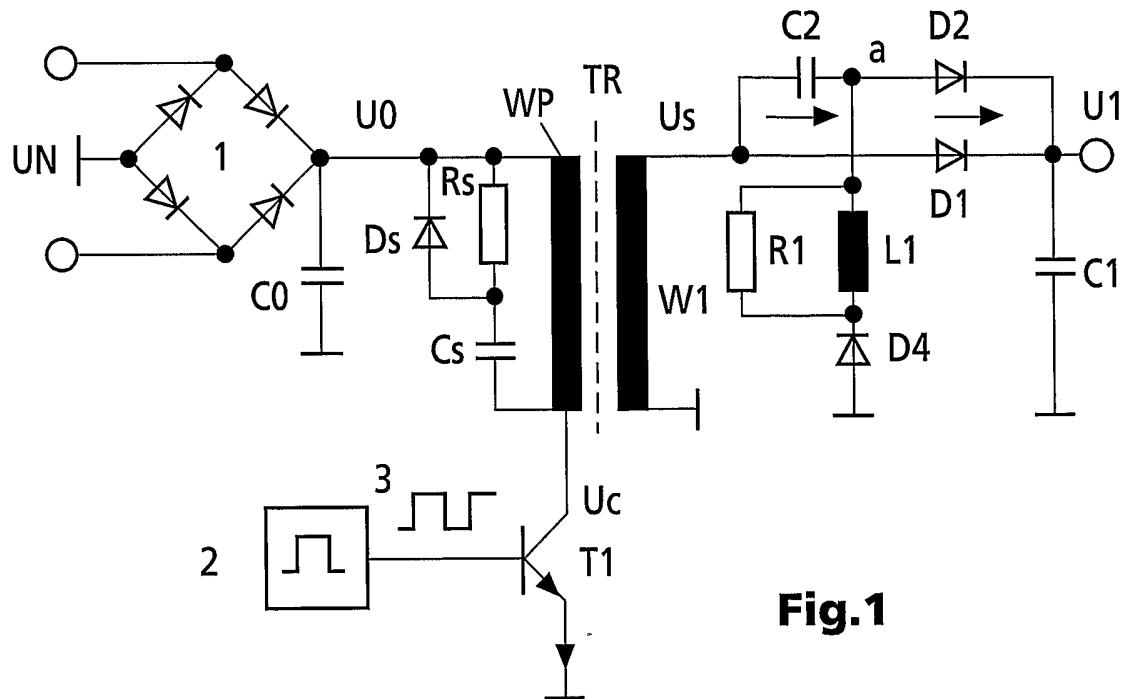


Fig. 1

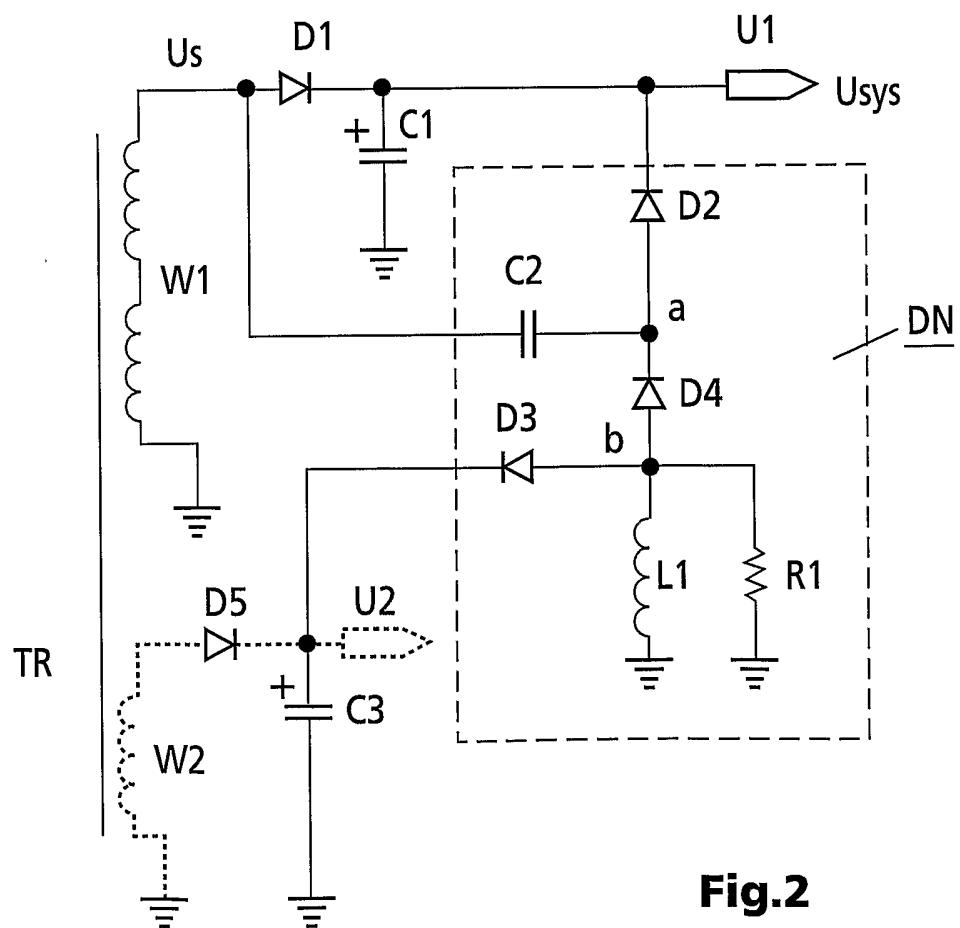


Fig.2

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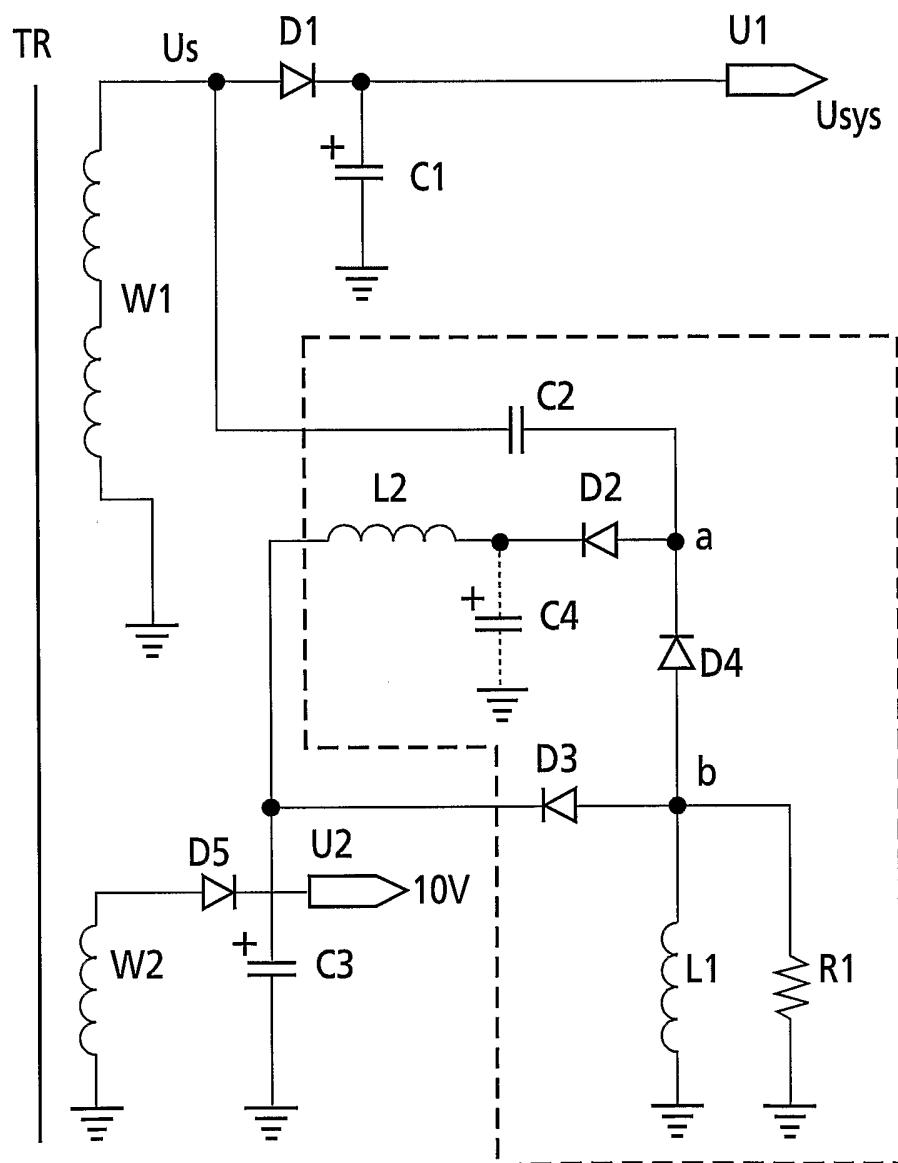


Fig.3

INTERNATIONAL SEARCH REPORT

International Application No
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	EP 0 695 023 A (THOMSON BRANDT GMBH) 31 January 1996 (1996-01-31) column 2, line 29 -column 3, line 26 figure 1	1,2
A	---	3-10
Y	PATENT ABSTRACTS OF JAPAN vol. 1998, no. 14, 31 December 1998 (1998-12-31) & JP 10 248254 A (OOHIRA DENSHI KK), 14 September 1998 (1998-09-14) abstract	1,2
A	US 5 278 748 A (KITAJIMA TOKIMUNE) 11 January 1994 (1994-01-11) column 2, line 60 -column 3, line 4 column 4, line 57 -column 4, line 67 figure 3	1
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 Further documents are listed in the continuation of box C. Patent family members are listed in annex.

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INTERNATIONAL SEARCH REPORTInternational Application No
PCT/EP 02/07917**C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT**

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	DE 40 29 221 A (THOMSON BRANDT GMBH) 19 March 1992 (1992-03-19) cited in the application column 2, line 34 - line 37 figure 1 -----	1

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

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