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— *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*

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(54) Title: SYSTEMS AND METHODS FOR EXPOSING A RESULT OF A CURRENT PROCESSOR INSTRUCTION UPON EXITING A VIRTUAL MACHINE

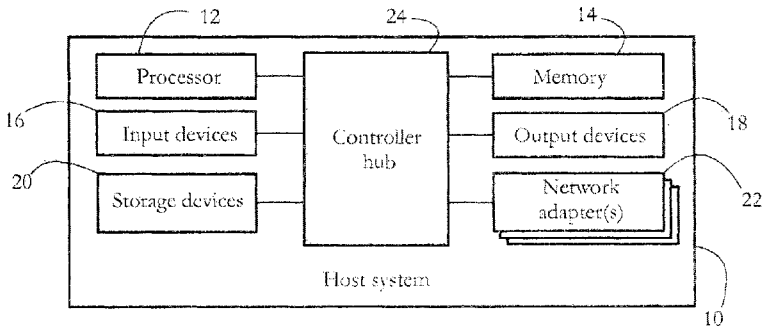


FIG. 1

(57) Abstract: Described systems and methods enable a host system to efficiently perform computer security activities, when operating in a hardware virtualization configuration. A processor is configured to generate a VM suspend event (e.g., a VM exit or a virtualization exception) when a guest instruction executing within a guest VM. performs a memory access violation. In some embodiments, the processor is further configured to delay generating the VM suspend event until the execution stage of the pipeline for the guest instruction is complete, and to save results of the execution stage to a specific location (e.g. a specific processor register readable by security-software) before generating the event.

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A. CLASSIFICATION OF SUBJECT MATTER
INV. G06F21/53 G06F21/55 G06F21/56 G06F9/455 G06F12/08
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B. FIELDS SEARCHED

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G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2007/106986 A1 (WORLEY WILLIAM S JR [US]) 10 May 2007 (2007-05-10) figures 3, 16A, 17B, 19 paragraphs [0047], [0054], [0064] paragraphs [0065], [0067], [0086] -----	1-21
X	US 2011/131388 A1 (CHEN XIAOXIN [US] ET AL) 2 June 2011 (2011-06-02) figures 8, 13, 14 paragraphs [0041], [0059], [0070] paragraphs [0071], [0131] -----	1-21

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[X] Further documents are listed in the continuation of Box C.

[X] See patent family annex.

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Name and mailing address of the ISA/	Authorized officer
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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JONAS PFOH ET AL: "Exploiting the x86 Architecture to Derive Virtual Machine State Information", EMERGING SECURITY INFORMATION SYSTEMS AND TECHNOLOGIES (SECURWARE), 2010 FOURTH INTERNATIONAL CONFERENCE ON, IEEE, PISCATAWAY, NJ, USA, 18 July 2010 (2010-07-18), pages 166-175, XP031799941, ISBN: 978-1-4244-7517-9 figure 1; table 1 page 167 - page 174	1-21
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INTERNATIONAL SEARCH REPORT

Information on patent family members

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