WAFER DICING DEVICE AND METHOD

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Abstract:
Methods and devices for cutting workpieces, which include a laser adapted to at least partially cut a workpiece, are described. The workpiece is a wafer having a plurality of dies each with an integrated circuit. A mechanical cutter follows the laser and engages the workpiece. An embodiment of the mechanical cutter includes a cutting blade adapted to complete the cut through a workpiece. A method includes a two-pass cutting procedure. The first pass is made by a laser, which scribes the workpiece. The second pass is made by the mechanical cutter. In an embodiment, the mechanical cutter follows the scribe created by the laser. In an embodiment, the workpiece is supported by a table. The workpiece moves relative to the laser and the mechanical cutter.
WAFER Dicing DEVICE AND METHOD


TECHNICAL FIELD

[0002] The present invention relates generally to integrated circuit manufacturing equipment, and in particular to apparatus and methods for dicing wafers.

BACKGROUND

[0003] Wafers are fabricated with a plurality of dies each having a plurality of integrated circuit elements therein. A die represents one individual chip that must be separated from adjacent dies before packaging. Conventionally, dies are separated from each other and the rest of the wafer by a saw blade. With the existing dicing technology of using diamond-nickel blades, the backside cutting quality can be improved by performing a two-pass cut. However, this will reduce productivity by around 50% and a wider saw kerf is expected. This is due to machine accuracy for the second pass. The wider saw kerf hinders progress towards a smaller saw street and thus leading to a lower die per wafer count.

[0004] Another area of concern when separating the dies is chipping on the top surface of the dies or fragmentation on the bottom surface of the dies adjacent the saw street. It is believed that one of the possible causes for the top or bottom surface fragmentation detected after the dicing process is the presence of a passivation layer, test structures, and/or metal layers deposited on the saw street during various wafer fabrication processes. These materials are not removed during wafer fabrication due to the higher operational cost and cycle time required for such fabrication steps. The impact of these materials on the cutting quality can be seen in FIG. 1. The reason for this poor cutting quality is believed to be due to the ejection of the blade, which results in overloading of the blade during dicing.

[0005] It is believed that a two-pass cut process using a saw blade for each cut pass will not significantly improve topside chipping as compared to a single-pass cutting due to the presence of test structures and metal layers on the wafer. The cutting quality of a two-pass cut can be seen in FIG. 2. A comparison on the cutting quality obtained from a single-pass cut and a two-pass cut is shown in Table 1.

<table>
<thead>
<tr>
<th>Table 1: Cutting Quality Comparison</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
</tr>
<tr>
<td>1-Pass Cut</td>
</tr>
<tr>
<td>Width (max)</td>
</tr>
<tr>
<td>Back Side</td>
</tr>
<tr>
<td>Chipping</td>
</tr>
<tr>
<td>Back Side</td>
</tr>
<tr>
<td>Cracking</td>
</tr>
<tr>
<td>2-Pass Cut</td>
</tr>
<tr>
<td>52.6 µm</td>
</tr>
<tr>
<td>105.2 µm</td>
</tr>
<tr>
<td>37.4 µm</td>
</tr>
<tr>
<td>55.9 µm</td>
</tr>
<tr>
<td>43.9 µm</td>
</tr>
<tr>
<td>19.6 µm</td>
</tr>
</tbody>
</table>

[0006] The numbers associated with the back side chipping and back side cracking represent the size, e.g., length or width, of such defects in microns. As shown in Table 1, the backside chipping and cracking improves approximately 40-50% using a two-pass cutting process. The top side chipping, for example, in its width, does not improve significantly in a two-pass cutting process.

[0007] While saw cutting of wafers is the conventional industry standard, there remains drawbacks with such cutting. Saw blades wear over time. This results in inconsistent cutting quality from when the blade is new and subsequent cutting operations. Consequently, the operator must predict when the blade has reached the end of its useful life. This can not be predicted accurately. Accordingly, the saw blades may be changed before the end of their useful lives resulting in higher equipment costs than necessary due to premature saw blade replacement. Moreover, saw blades introduce mechanical stresses in the workpiece while sawing, especially at the surfaces of the workpiece. Due to these stresses saw blades may not be used to cut very thin workpieces, such as ultrathin semiconductor wafers.

[0008] A recent development in wafer dicing is laser cutting. However, laser dicing of semiconductor wafers has failed to meet the rigid demands of industry. One significant drawback of using lasers is the collateral thermal damage to the chips caused by the laser. New lasers have been proposed to overcome the drawbacks of conventional lasers in the area of semiconductor dicing. An example of such a system is being promoted by Synova S.A. of Lausanne, Switzerland, and described in WO 99/56907, titled “MATERIAL SHAPING DEVICE WITH A LASER BEAM WHICH IS INJECTED INTO A STREAM OF LIQUID”, herein incorporated by reference. WO 99/56907 describes a method and device for shaping material of workpieces using a laser beam which is injected into a stream of liquid. The liquid, which is to be formed into a stream, is fed to the nozzle channel opening such that the liquid does not swirl, especially without the flow components which are tangential to the nozzle channel axis. The laser irradiation is focused on a channel entry plane and the liquid is fed to the channel opening in such a way that a liquid retention space is avoided in a beam focusing ball and in the immediate surroundings thereof. Another laser cutting arrangement is described in U.S. Pat. No. 5,902,499, herein incorporated by reference. However, these laser cutting arrangements require a reduced speed when used to cut wafers. The feedspeed of the wafer is reduced to 40 mm/s and 20 mm/s for wafers with 305 µm and 470 µm thickness, respectively. This is due to the greater laser pulse energy required. Therefore, with this method, laser cuts can be achieved at the expense of productivity.

[0009] For the reasons stated above, for other reasons stated below, and for other reasons which will become apparent to those skilled in the art upon reading and understanding the present specification, there is a need in the art for improved integrated circuit manufacturing equipment and methods of manufacturing integrated circuits.

SUMMARY

[0010] The present invention is directed to methods and devices for cutting workpieces, which include a laser adapted to at least partially cut a workpiece. In an embodiment, the workpiece is a wafer having a plurality dies each with an integrated circuit. An embodiment of the present invention further includes a mechanical cutter following the laser and engaging the workpiece. An embodiment of the
mechanical cutter includes a cutting blade adapted to complete a cut through a workpiece. An embodiment of the cutting blade includes a nickel-diamond cutting surface on a circular blade. An embodiment of the laser includes a liquid guided laser beam. An embodiment of the laser includes a yttrium-aluminum-garnet (YAG) laser.

[0011] In an embodiment, a process according to the present invention includes a two-pass cutting procedure. The first pass is made by a laser, which scribes the workpiece. The second pass is made by a mechanical cutter. In an embodiment, the mechanical cutter follows the scribe created by the laser. In an embodiment, the workpiece is supported by a table. The workpiece moves relative to the laser and the mechanical cutter. In an embodiment, the relative movement of workpiece is at a speed of about 120 mm/sec.

[0012] These and other embodiments, aspects, advantages, and features of the present invention will be set forth in part in the description which follows, and in part will become apparent to those skilled in the art by reference to the following description of the invention and referenced drawings or by practice of the invention. The aspects, advantages, and features of the invention are realized and attained by means of the instrumentalities, procedures, and combinations particularly pointed out in the appended claims.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] FIGS. 1A and 1B are views of backsides of wafers that are cut using conventional devices and techniques.

[0014] FIGS. 2A and 2B are views of backsides of wafers that are cut using conventional devices and techniques in a two pass cut.

[0015] FIG. 3 is a view of a wafer cutting apparatus according to the teachings of the present invention.

[0016] FIG. 4 is a view of a wafer cutting apparatus according to the teachings of an embodiment of the present invention.

[0017] FIG. 5 is a view of a wafer cutting apparatus according to the teachings of an embodiment of the present invention.

[0018] FIG. 6 is diagrammatic view of a system according to the teachings of the present invention.

[0019] FIG. 7 is diagrammatic view of a system according to the teachings of the present invention.

[0020] FIG. 8 is an enlarged, partial view of a wafer support and laser according to an embodiment of the present invention.

[0021] FIG. 9 is a view of a wafer including a plurality of dies separated by saw streets.

DETAILED DESCRIPTION OF THE EMBODIMENTS

[0022] In the following detailed description of the embodiments, reference is made to the accompanying drawings which form a part hereof, and in which is shown by way of illustration specific embodiments in which the inventions may be practiced. These embodiments are described in sufficient detail to enable those skilled in the art to practice the invention, and it is to be understood that other embodiments may be utilized and that process, electrical or mechanical changes may be made without departing from the scope of the present invention. The terms wafer and substrate used in the following description include any base semiconductor structure. Both are to be understood as including silicon-on-sapphire (SOS) technology, silicon-on-insulator (SOI) technology, thin film transistor (TFT) technology, doped and undoped semiconductors, epitaxial layers of a silicon supported by a base semiconductor structure, as well as other semiconductor structures well known to one skilled in the art. The following detailed description is, therefore, not to be taken in a limiting sense, and the scope of the present invention is defined only by the appended claims and their equivalents.

[0023] The present description uses a reference number convention of the first digit corresponding to the figure in which the number references and the last two digits corresponding to like elements throughout the description. For example, the laser has a reference number of X10, where X is the number of figure on which the reference number refers.

[0024] FIG. 3 shows a side view of a workpiece cutting apparatus 300 according to an embodiment of the present invention. An embodiment of the present invention is adapted to cut and/or dice wafers 320 such as wafers, semiconductor substrates and the like. Apparatus 300 includes a laser 310 producing a laser beam 315 that scribes a wafer 320. As used herein, the term “scribe” is defined as partially cutting into a workpiece and not cutting entirely through the workpiece. The apparatus further includes a mechanical cutter 330, e.g., a saw blade, for further cutting into the wafer 320. In an embodiment, saw blade 330 is a circular saw blade having an outer cutting surface 332. In an embodiment, saw blade 330 includes a diamond cutting surface. In an embodiment, saw blade 330 includes a diamond-nickel cutting surface. In an embodiment, the laser 315 and saw blade 330 are fixed together at a fixed distance apart. In an embodiment, the laser 315 and the saw blade 330 move relative to the wafer 320 in the direction of arrow 340 to respectively scribe and cut the wafer 320.

[0025] In an embodiment, the workpiece cutting apparatus 300 further includes a workpiece support 350. The workpiece support 350 is adapted to rigidly fix the workpiece such that its position is readily determined and it will not move during scribing or cutting. An embodiment of the support 350 includes table 352 and an adhesive layer 354 on the top surface of the table. The adhesive layer 354 fixes the wafer 320 to the table 352. In an embodiment, the adhesive layer 354 is a tape.

[0026] The wafer 320, in an embodiment, includes a substrate layer 323 on which is formed at least one active circuit 324 for each die. In an embodiment, a plurality of active circuits 324 are formed on the substrate layer 323. The active circuits 324 are fabricated according to techniques known to those of skill in the art of semiconductor fabrication. Intermediate the dies are saw streets 325 whereat the laser beam 315 and mechanical cutter 330 can cut into the wafer 320 to dice the wafer into separate dies without damaging the active circuits 324. In an embodiment, the layer 323 is divided into a upper layer 320A and a lower
In an embodiment, integrated circuits are formed within the upper layer 320A according to known fabrication techniques. The lower layer 320B is a substrate layer on which integrated circuit layers are fabricated. In an embodiment, lower layer 320B also includes integrated circuit elements.

[0027] In a method of operation, the workpiece 320 is diced or separated by first producing a laser beam 315 which ablates an upper portion or layer 320A of the workpiece 320. In an embodiment, laser 320 ablates only a portion of the upper layer 320A. In an embodiment, the laser beam 315 and workpiece 320 move relative to each other such that a continuous channel 322 is formed in the workpiece by the laser beam 315. In an embodiment, the laser beam ablates a top layer 320A of the workpiece. In an embodiment, the laser beam ablates completely through the workpiece 320. In an embodiment where the workpiece 320 is a semiconductor wafer, then the top layer 320A includes a metalization layer. In an embodiment, the top layer 320A includes a passivation layer. In an embodiment, the passivation layer includes a glass. In an embodiment, the passivation layer is an insulator. In an embodiment, the passivation layer includes a polyimide. In an embodiment, the polyimide is of the formula PAlx. In an embodiment, the polyimide is poly-C. In an embodiment, the passivation layer includes two sub-layers. In an embodiment, the top layer 320A includes test structures such as contacts and circuit elements. After the laser beam 315 ablates an upper portion of the workpiece 320, the mechanical cutter 330 Completes the cut through the remaining unablated lower portion or bottom layer 320B of the workpiece 320. In an embodiment, the cutter 330 follows the scribe or channel formed by the laser beam 315. In an embodiment, the upper layer 320A ablated by the laser is the passivation layer only and not levels of the wafer beneath the passivation layer. In an embodiment, the laser 310 and mechanical cutter 330 are supported on separate supports (not shown in FIG. 3). In an embodiment, laser 310 and cutter 330 are movable relative to each other. In an embodiment, the laser 310 scribes the entire wafer 320 before the mechanical cutter 330 begins completes the cut through the wafer 320. In an embodiment, the laser 310 and mechanical cutter 330 are fixed to the same support. In an embodiment, the mechanical cutter 330 follows directly after the laser 310 at a fixed distance.

[0028] FIG. 4 shows an embodiment of a workpiece cutting apparatus 400 according to the teachings of the present invention. Workpiece cutting apparatus 400 includes a laser 410 and a mechanical cutter 430 each fixed to a housing 460. The laser 410 produces a laser beam 415 for cutting a workpiece 420 such as a wafer or semiconductor substrate. In an embodiment, the mechanical cutter 430 includes a circular saw blade 432. In an embodiment, the housing 460 is fixed and a workpiece support unit 450 is positioned adjacent the housing. In an embodiment, the workpiece support unit 450 includes a table 452 on which is positioned an attachment device 454 that fixes a workpiece 420 to the table. Workpiece support unit 450 further includes an actuator 455 that is connected to the table 452. Actuator 455 moves the table relative to the fixed housing 450, and hence the laser 410 and mechanical cutter 430, during a cutting operation. The actuator 455, in an embodiment, includes stepper motors that precisely control the position of table 452. Actuator 455 may include pneumatic or hydraulic devices for precisely positioning the table 452. The actuator 455 is capable of moving the table 452 and workpiece 420 in the direction indicated by arrow 456. Accordingly, laser beam 415 produced by laser 410 ablates material from the workpiece 420 before the mechanical cutter 430 cuts the workpiece 420. In an embodiment, the actuator 455 can rotate the table 452 such that both the X and Y directions of the workpiece 420 are cut by the laser 410 and mechanical cutter 430.

[0029] Housing 460 provides a support for the mechanical cutter 430 and the laser 410. The housing 460, in an embodiment, holds the motor (not shown) driving a rotor 433 to which the mechanical cutting blade, e.g., the circular saw blade 432, is fixed. Laser 410 is mounted on the front (right in FIG. 4) face of the housing 460. The front face is the lead, initial surface of the housing that the workpiece passes when it is moved the direction 456. In an embodiment, housing 460 includes a cooling unit 470. Cooling unit 470 includes a coolant source (not shown) that is connected to housing 460 and fluidly connected to a slide bar nozzle assembly 471. Slide bar nozzle assembly 471 is positioned intermediate the laser 410 and mechanical cutter 430. Thus, coolant exits the slide bar nozzle assembly 470 intermediate the laser 410 and cutter 430 to reduce the effects of temperature on the workpiece during a cutting operation. It will be understood that the housing 460 is adaptable for use with embodiments of the present invention described herein.

[0030] Controller 480 is electrically connected to the actuator 455, laser 410 and cutter 430. In an embodiment, controller 480 is further connected to cooling unit 470. Accordingly, controller 480 controls and coordinates operation of each of these elements. Controller 480, in an embodiment, is a computer having a processor and memory. As known to one of ordinary skill in the art, the memory stores data and instructions that are used by the processor to produce control signals for the devices connected to the controller. The memory includes any machine readable data storage device. Examples of the memory include solid state memory chips such as RAM, DRAM SRAM, SDRAM, and other memory chips known in the art. The memory further includes magnetic and optical storage devices, such as disks, tapes, CD-ROM, and other storage devices known on the art.

[0031] In an embodiment, the controller 480 provides signals to the laser 410 that regulate the timing, power and refresh frequency of the laser 410. In an embodiment, controller 480 provides signals controlling movement of the laser housing 450 and, hence, movement of the laser and housing. In an embodiment, the controller 480 provides signals to the mechanical cutter 430 that regulate operation of cutter 430, such as the speed of the cutting blade. Controller 480 further provides signals to the actuator 455 to regulate movement of the table 452 and, hence, the position of the workpiece 420 relative to the laser beam 415. Accordingly, the controller 480 governs operation of the laser 410 in conjunction with movement of the workpiece 420 such that the minimal energy needed to ablate the workpiece material is applied at the correct location of the workpiece. As a result, the potential for collateral damage to the workpiece is reduced.

[0032] FIG. 5 shows an end view of a workpiece cutting apparatus 500 according to an embodiment of the present invention. Apparatus 500 includes a laser 510 producing a
laser beam 515 and a mechanical cutter 530. Laser 510 produces a laser beam 515 that scribes the workpiece 520 in a saw street 521 intermediate active circuits 524 to create a trench 522A in an upper portion of the wafer workpiece. It is understood that it is within the scope of the present invention to position the active circuits with the wafer workpiece 520. In an embodiment, the laser beam 515 cuts into the top layer on a wafer workpiece. In an embodiment, the laser beam 515 cuts through the top layer on a wafer workpiece. In an embodiment, the top layer is a polymer. Mechanical cutter 530 cuts through the remaining portion of the workpiece in previously formed trench. The mechanical cutter 530, in an embodiment, cuts into a portion of the support 554 fixing the workpiece to the table 550. In an embodiment, the mechanical cutter 530 is not aligned with the laser 510. Accordingly, the laser 510 and mechanical cutter 530 maybe positioned at separate workstations. The laser 510 and mechanical cutter 530 may be positioned in the same workstation but supported on independent arms controlled by a controller.

[0035] The liquid source 716, in an embodiment, provides a liquid stream that acts as a guide directing the laser beam to the workpiece 720. In an embodiment, the liquid stream has a diameter equal to the laser beam diameter. In an embodiment, the liquid stream diameter is about 50 micrometers to about 150 micrometers. In an embodiment, the liquid is water. The water, when guiding a laser beam to a wafer workpiece, is filtered to a high degree such that particles that can damage the wafer workpiece are not present. In an embodiment, the water is filtered to remove particles of at 0.2 microns and greater. Moreover, the liquid should be electrically neutral. Accordingly, the liquid is deionized.

[0036] FIG. 8 shows an embodiment of a workpiece support table 850 according to the teachings of the present invention. Table 850 includes reliefs 851 in the form of channels recessed into the table below a workpiece support surface 853. Workpiece support surface 853 is defined by coplanar top surfaces of columns 857. In an embodiment, the table includes an attachment layer (not shown) for holding the workpiece 820 on the top surface of the columns 857. In an embodiment, the workpiece 820 is a wafer or semiconductor substrate. In an embodiment, workpiece 820 includes saw streets 821 extending in at least one direction. The saw streets 821 are aligned with reliefs 851 when the workpiece is fixed to the table 850. In an embodiment, the laser 810 scribes the workpiece 820. In use, the laser 810 produces a laser beam 815 which cuts the workpiece 820 along saw streets in the workpiece that are aligned with the reliefs 851. In an embodiment, the laser 810 is a liquid guided laser and the reliefs 851 receive the liquid and channel it away from the workpiece. In an embodiment, the reliefs 851 provide coolant to the workpiece to reduce the effects of heat produced by the laser beam 815 on the workpiece 820. A wafer workpiece 820 is sensitive to temperature effects which could damage circuitry on the wafer workpiece. As known to one of ordinary skill each wafer has a limited thermal budget, which if exceeded causes defects in the circuitry. Moreover, elements in the wafer workpiece may have low breakdown temperatures. For example, copper and aluminum have relative low melting points compared to other metals. A laser may heat such elements to such an extent that they undesirably reflow. Such reflow may increase resistance and open interconnect lines. Moreover, excess heat may result in undesirable diffusions from one layer to another layer in the circuitry formed on the wafer workpiece.

[0037] The lasers 310, 410, 510, 610, 710, or 810 described herein include any laser suitable for scoring and/or cutting through a workpiece. In an embodiment, the laser is adapted for cutting wafer structures fabricated according to known techniques. In an embodiment, the laser is a solid state laser. In an embodiment, the laser is a yttrium-aluminum-garnet (YAG) laser. In an embodiment, the laser is a neodymium-YAG laser. The laser wavelength, in an embodiment, is about 1 micrometer. In an embodiment, the laser power is about 300 watts. In an embodiment, the laser power is less than about 300 watts. In an embodiment, the laser power is greater than about 100 watts. In an embodiment, the laser refresh rate is about 3,000 Hz. In an embodiment, the laser refresh rate is less than about 3,000 Hz. In an embodiment, the laser refresh rate is greater than about 1,000 Hz. In an embodiment, the laser is an excimer laser. The laser is desirable for use in scribing or cutting the workpiece as it
does not apply mechanical stress onto the workpiece. In an embodiment, the laser is used to completely cut through, e.g., dice, a wafer. In an embodiment, the laser scribes the wafer. Thereafter, the wafer is divided into individual dies. In an embodiment, a mechanical cutter follows the scribes formed by the laser. The mechanical cutter, during a cutting operation, does not contact or engage the top surface of the wafer as the top surface was removed by the laser. In an embodiment, the wafer is broken into individual dies along the scribes created by the laser.

[0038] FIG. 9 shows a wafer 920 that includes a plurality of dies 926 integrally connected by saw streets 921. In an embodiment, the wafer 920 has a thickness of about 300 micrometers. In an embodiment, the wafer 920 has a thickness of about 305 micrometers. In an embodiment, the wafer 920 has a thickness of about 470 micrometers. In an embodiment, the wafer 920 has a thickness of about 500 micrometers. In an embodiment, the saw streets 921 in one group extend parallel to each other. In an embodiment, saw streets 921 in one group extend perpendicular to saw streets in a second group. The wafer 920 includes a base layer on which is formed a plurality of integrated circuit layers to form integrated circuits. The layers include capacitors, transistor, lines, plugs, fill, insulators, wells, metals, and other structures known in the field of integrated circuits. Each die 926 includes at least one integrated circuit element. The dies 926 are separated from each other according to the teachings of the present invention. Individual dies 926 may represent a complete integrated circuit such as a processor, memory chip, or other integrated circuit. Memory chip dies include DRAM, SRAM, SDRAM, and other chips known in the art. The saw streets 921 typically do not contain circuit elements. In an embodiment, at least one saw street includes contacts. The contacts provide electrical connection between the integrated circuits on the die and circuits outside the die. The present description provides examples of various wafers that can be used in the present invention. Thus, these examples do not limit the present invention unless expressed in the appended claims.

[0039] A process according to the teachings of the present invention includes cutting into a workpiece 320, 420, 520, 620, 720, 820, or 920 using a laser 310, 410, 510, 610, 710, or 810. The laser removes material from a top portion of a workpiece to create a scribe. In an embodiment, the scribe extends along a saw street of a wafer workpiece. In an embodiment, a mechanical cutter 330, 430, 530, or 730 cuts the wafer workpiece at the scribe. Accordingly, the cutter completes the cut through the remaining portion of the wafer beneath the scribe created by the laser. The cutter completes the cut through the wafer workpiece, thereby separating adjacent dies. In an embodiment, the laser completes a scribe throughout the length of the saw street prior to the cutter engaging the wafer workpiece. In an embodiment, both the laser and the cutter act on the wafer workpiece at the same time. In an embodiment, the cutter closely follows the laser such that both the laser and the cutter are simultaneously engaging a same saw street.

[0040] In an embodiment, it is desirable to maintain a processing speed of 120 mm/sec when dicing a wafer. The use of a two pass cutting process according to the teachings of the present invention maintains such a speed. Both the laser and the mechanical cutter can engage, e.g., ablate and cut, a wafer at this speed. The speed need not be reduced as when using conventional techniques for cutting wafers having a thickness of greater than 100 micrometers. The laser can scribe a channel in the wafer. The mechanical cutter follows the channel and completes a cut through the wafer. In an embodiment where the wafer has a top layer on the saw street in the range of about 25 micrometers to about 50 micrometers, the laser creates a channel at least as deep as the top layer. Thus, the channel has a depth in a range of about 25 micrometers to about 50 micrometers in this example. In an embodiment, the channel has a depth deeper than about 25 micrometers to about 50 micrometers. In an embodiment, the feed speed of the wafer is about 120 mm/sec. Accordingly, the feed speed of the wafer is not reduced based on the thickness of the wafer or the top layer of the wafer.

[0041] In an embodiment, the two pass cutting process of the present invention improves backside chipping and backside cracking over a single pass cutting process at least the same as the two pass cut listed in Table 1. In an embodiment, the two pass cut of first scribing with a laser and then cutting with a mechanical cutter results in a faster cutting speed than the two pass mechanical cut process described in conjunction with Table 1.

[0042] In an embodiment, the controller 480, 680, or 780 includes software that generates control instructions for the wafer workpiece dicing equipment. The instructions control the relative movement of the wafer workpiece 320, 420, 520, 620, 720, 820, or 920 as compared to the cutting equipment such as laser 310, 410, 510, 610, 710, or 810 and mechanical cutter 330, 430, 530, or 730. In an embodiment, the speed at which the wafer workpiece moves relative to the laser is about 120 micrometers per second. In an embodiment, the software determines the use of the laser and/or the mechanical cutter based on the thickness and composition of the wafer workpiece. For example, if the wafer workpiece is ultrathin, then only the laser is used to cut through the wafer workpiece. In an embodiment, if the wafer workpiece has no, or a very thin polyamide coating on the saw street, then the mechanical cutter is used without the laser.

[0043] The present invention also cuts through ultrathin wafers. Ultrathin wafers have a thickness through which the laser 310, 410, 510, 610, 710, or 810 will completely cut through of about 100 micrometers. In an embodiment, the wafer thickness is less than 100 micrometers. In an embodiment, the thickness of about 5 microns. In an embodiment, the thickness less than about 5 microns. In an embodiment, the thickness of about 4 microns. In an embodiment, the thickness of less than about 5 microns. In an embodiment, the feed speed of the wafer relative to the laser is about 120 millimeters per second. In an embodiment, the laser has an average output power of about 300 watts. In an embodiment, the laser has a refresh rate of about 3000 Hz. In an embodiment, the laser has a maximum refresh rate of 3000 Hz. Accordingly, the present invention provides for dicing ultrathin wafer workpieces using only the laser.

Conclusion

[0044] The present invention provides improved workpiece cutting structures and processes. The laser scribes a workpiece in a relatively stress-free as compared to mechanical cutters such as saws, scribers, and mechanical
separaters. A mechanical cutter, such as a saw blade, follows the laser in the scribe created by the laser in an embodiment of the invention. The speed at which the laser and cutter act on the workpiece is maintained at about 120 mm/sec. to provide adequate throughput. Accordingly, productivity is not diminished. The use of the laser as the first pass in a two pass cutting procedure reduces damage to the workpiece such as surface chipping and backside fragmentation. Accordingly, fewer wafer workpieces are damaged during dicing.

The present invention also provides a laser devices and method for cutting wafer workpieces without a mechanical cutter. The laser is adapted to cut through very thin workpieces. The workpieces have thickness as described herein. Accordingly, there is no mechanical stress on the very thin workpieces, which can cause a significant amount of cracking and chipping particularly in semiconductor wafers.

Although specific embodiments have been illustrated and described herein, it will be appreciated by those of ordinary skill in the art that any arrangement which is calculated to achieve the same purpose may be substituted for the specific embodiments shown. Many adaptations of the invention will be apparent to those of ordinary skill in the art. For example, other integrated circuit processing equipment may be utilized in conjunction with the invention. Accordingly, this application is intended to cover any adaptations or variations of the invention. It is manifestly intended that this invention be limited only by the following claims and equivalents thereof.

The invention claimed is:

1. A wafer having a top surface and a body, comprising:
   a first state wherein the top surface is uncut;
   a second state wherein the top surface has a laser cut partially into the body; and
   a third state wherein a further cut extends completely through the body of the wafer.
   The wafer of claim 1, wherein the laser cut has a depth from the top surface into the body of about 25 micrometers to about 50 micrometers.
   The wafer of claim 1, wherein the wafer has a thickness of greater than about five millimeters.
   The wafer of claim 1, wherein the body includes a saw street where the laser cut is positioned in the second state.
   The wafer of claim 4, wherein the further cut is positioned in the saw street in the third state.
   The wafer of claim 1, wherein the further cut is adapted to be formed by a cutting blade.

7. The wafer of claim 1, wherein the further cut is adapted to be formed by a nickel-diamond cutting surface.

8. A method for dicing workpieces, comprising:
   scribing a workpiece with a laser; and
   completely cutting through the workpiece along the scribe with a mechanical cutter.

9. The method of claim 8, wherein scribing includes setting the laser to have an average power of about 300 watts and a maximum refresh rate of 3,000 Hz.

10. The method of claim 8, wherein completely cutting includes engaging the wafer at the scribe with a saw.

11. The method of claim 10, wherein engaging the wafer includes sawing the wafer with a nickle-diamond cutting surface.

12. The method of claim 8, wherein scribing includes scribing the wafer with a yttrium-aluminum-garnet (YAG) laser.

13. A method for dicing workpieces, comprising:
   scribing a workpiece with a laser; and
   completely cutting through the workpiece along the scribe with a mechanical cutter, wherein the laser and the mechanical cutter simultaneously contact the workpiece.

14. The method of claim 13, wherein completely cutting includes fixing the mechanical cutter at a set distance behind the laser.

15. The method of claim 14, wherein scribing includes setting the laser to have an average power of about 300 watts and a maximum refresh rate of 3,000 Hz.

16. A method for dicing workpieces, comprising:
   moving a workpiece relative to a laser;
   scribing a workpiece with the laser; and
   completely cutting through the workpiece along the scribe with a mechanical cutter.

17. The method of claim 16, wherein moving includes moving the workpiece at a speed of 120 mm/sec.

18. The method of claim 17, wherein scribing includes setting the laser to have an average power of about 300 watts and a maximum refresh rate of 3,000 Hz.

19. The method of claim 16, wherein moving a workpiece relative to a laser includes holding the laser stationary.

20. The method of claim 16, wherein completely cutting through the workpiece includes cutting the workpiece with a nickel coated blade.

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