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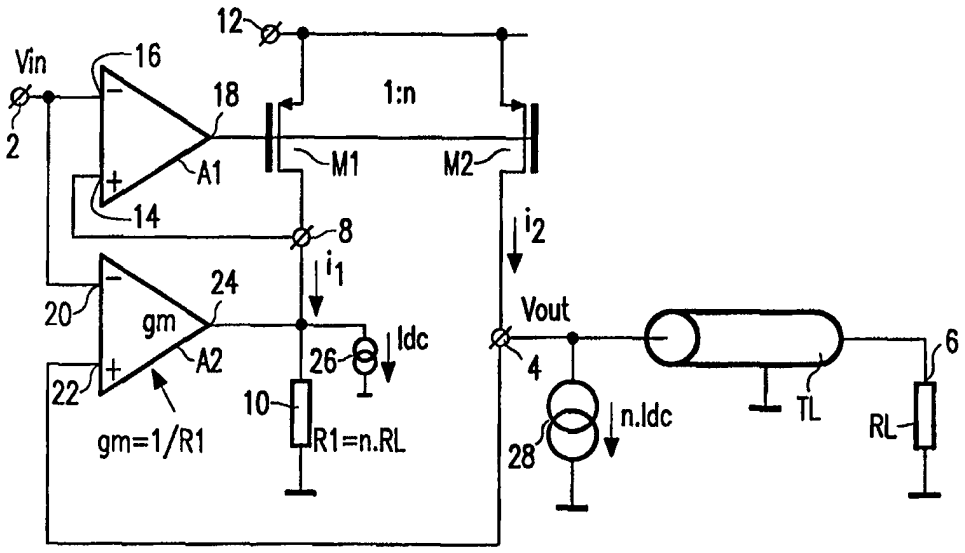

(57) Abstract

A line driver comprising a first transistor (M1), a first amplifier (A1) and a reference resistor (10) for converting an input voltage (Vin) to a first current (i1) through the first transistor (M1). A second current i2=n*i1 flows through a second transistor (M2) which forms a 1:n current mirror with the first transistor (M1). The current (i2) flows to a load (6), if so required via a transmission line (TL). The impedance of the load (6) is equal to the characteristic impedance (RL) of the transmission line (TL). Thus the impedance seen by the line driver is equal to RL. A second transconductance amplifier (A2) counteracts reflected signals in the output signal (Vout) caused by mismatch between the output impedance of the current mirror (M1, M2) and the impedance seen by the line driver.

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INTERNATIONAL SEARCH REPORT

International application No.
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A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H04L 25/02
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EDOC, WPIL, JAPIO

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 9502931 A1 (PHILIPS ELECTRONICS N.V.), 26 January 1995 (26.01.95), page 3, line 7 - page 5, line 6 --	1-10
A	US 5585763 A (M.J. NAVABI ET AL.), 17 December 1996 (17.12.96), column 1, line 8 - line 29, figure 3B --	1-10

☐ Further documents are listed in the continuation of Box C. ☒ See patent family annex.

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Date of the actual completion of the international search	Date of mailing of the international search report
12 August 1998	14 -08- 1998
Name and mailing address of the ISA/ Swedish Patent Office Box 5055, S-102 42 STOCKHOLM Facsimile No. +46 8 666 02 86	Authorized officer Anders Ströbeck Telephone No. +46 8 782 25 00

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Information on patent family members

27/07/98

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9502931 A1	26/01/95	EP 0664937 A	02/08/95
		JP 8501674 T	20/02/96
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US 5585763 A	17/12/96	NONE	