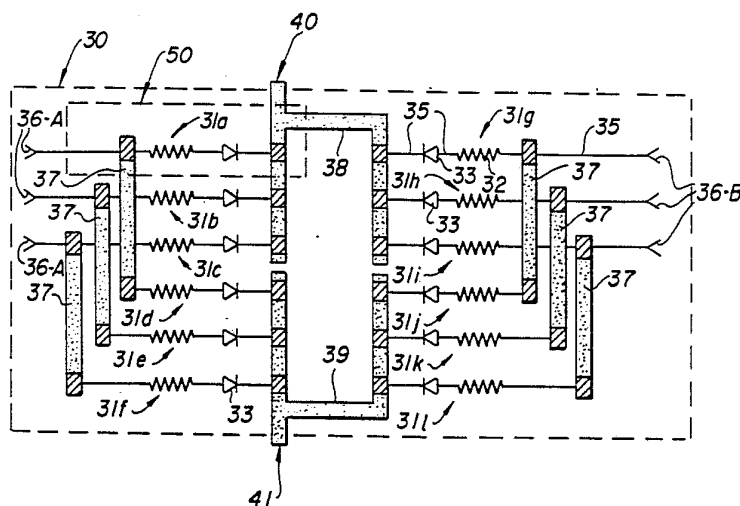




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁵ : B41J 2/05	A1	(11) International Publication Number: WO 91/08899 (43) International Publication Date: 27 June 1991 (27.06.91)
<p>(21) International Application Number: PCT/US90/07141</p> <p>(22) International Filing Date: 6 December 1990 (06.12.90)</p> <p>(30) Priority data: 451,709 18 December 1989 (18.12.89) US</p> <p>(71) Applicant: EASTMAN KODAK COMPANY [US/US]; 343 State Street, Rochester, NY 14650 (US).</p> <p>(72) Inventor: BRAUN, Hilarion ; 2786 Ater Drive, Xenia, OH 45385 (US).</p> <p>(74) Agent: HUSSER, John, D.; 343 State Street, Rochester, NY 14650-2201 (US).</p>	<p>(81) Designated States: AT (European patent), BE (European patent), CH (European patent), DE (European patent), DK (European patent), ES (European patent), FR (European patent), GB (European patent), GR (European patent), IT (European patent), JP, LU (European patent), NL (European patent), SE (European patent).</p> <p>Published <i>With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i></p>	

(54) Title: BUBBLE JET PRINT HEAD HAVING IMPROVED MULTIPLEX ACTUATION CONSTRUCTION



(57) Abstract

The construction includes a support substrate (51); a first circuit portion having branches (31a-31l) that include a resistive heater element (32) and a diode device (33) formed in spaced relation on the substrate (51). A dielectric passivation layer (52) overlies the first circuit portion except at the discrete terminal regions of the first circuit portion branches (31a-31l). A second circuit portion comprising a plurality of multiplex electrode lines (37, 38, 39) overlies the passivation layer (52) and includes connection sections extending through the passivation layer into contact with terminal regions of the first circuit. A second passivation layer (60) overlies the second circuit portion, but not the resistive heater elements.

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BUBBLE JET PRINT HEAD HAVING IMPROVED
MULTIPLEX ACTUATION CONSTRUCTION

Technical Field

The present invention relates to thermal,
5 drop-on-demand, ink jet (herein termed "bubble jet")
printing and, more particularly, to improved print
head constructions for enabling high density printing.

Background Art

Typically, in bubble jet print heads a
10 plurality of electrically resistive heater elements
are deposited on a support substrate that is formed
e.g. of metal or silicon and has a heat control
coating, e.g. SiO_2 . Metal electrodes are formed to
selectively apply voltage across the heater elements
15 and a protective coating is provided over the heater
elements and electrodes. Printing ink is supplied
between the heater elements and orifices of the print
head and heater elements are selectively energized to
a temperature that converts the adjacent ink to steam
20 rapidly, so that a shock wave causes ejection of ink
from the related orifice.

As the development and commercial use of the
bubble jet technology progresses, there is increased
interest in increasing the resolution of those
25 systems. In this context, system resolution can be
thought of as the number of pixel drops printed
within a given print region (e.g. line length). One
way to increase system resolution is to interlace ink
drops, e.g. with multiple passes vis a vis a single
30 array of orifices, or by providing a plurality of
scanning orifice arrays. This approach simplifies
the print head(s) construction, but requires accurate
positioning of the print heads vis a vis the print
media to assure good registration of the drops from
35 separate passes.

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Another way to increase system resolution is to increase the line density of drop ejector subsystems (i.e. orifices and related heater elements) on a single print head. This can be done with one or a plurality of linear orifice arrays (see e.g. U.S. Patent No. 4,734,717). Photofabrication techniques enable construction of such high density orifice plate and heater subsystems; however, a present limit to increasing system resolution is presented by the difficulty in making electrical connections between a large number of heating resistors formed on a tiny chip and the electrical address electronics of the printer control system.

To reduce the problem presented by the large number of addressing leads, several systems have been proposed for multiplexing the address of (i.e. the provision of energizing current through) the resistive heater elements of bubble jet print heads. For example, U.S. Patent 4,695,853 discloses a bubble jet chip construction wherein an x-y electrode matrix is constructed with one matrix electrode portion underlying a pattern of resistor/diodes and the other matrix electrode portion overlying the resistor/diode pattern. The x-y electrode portions are separated by an electrically insulative layer except at the resistor/diode components where they form "vertically" disposed, sandwiching terminals. While this resistor/diode construction allows for multiplexing (and thus reduces leads and terminals necessary for address), it has several problems. First, during multiplexing as described in the '853 patent, diode reverse leakage to the upper electrode can cause electrolytic attack that can destroy the electrode traces. Second, it is extremely difficult to equalize forward resistance in a diode while maintaining a fixed resistance value in the heater resistor element.

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U.S. Patent 4,791,440 discloses another solution to the problem of providing a high resolution print head with a large number of drop-ejection sites. In this approach, one array of electrical connections to the sites is provided on the top side of the chip substrate and another array of site connections is provided on the bottom side of the substrate. A plurality of holes are provided through the substrate material to couple the top and bottom side lead matrices. To further simplify the electrical lead situation, this approach provides also a multiplex address system wherein a plurality of heater arrays are activated at different phases by an array-select voltage pulse, which, when coupled with a particular heat site data pulse, will provide sufficient heater current to eject an ink drop. The '440 patent approach is a difficult one to fabricate, necessitating the forming of multiple holes through the substrate and photofabrication work on both sides of the substrate. In some applications coupling to the printer via the chip bottom surface is not possible. Also, the multiplex system causes unnecessary partial energizations of all heater elements during each active phase of an array. This can cause dissolved gas release from the ink resulting in a bubble which blocks jet activity or ink replenishment.

Disclosure of Invention

A significant purpose of the present invention is to provide an improved construction for a high resolution bubble jet print head which avoids the problems of prior art approaches such as described above. Thus, one advantage of the present invention is to enable relatively simple fabrication of a bubble jet print head having a large number of high density drop ejection sites. Another advantage

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of the present invention is provision of a multiplex print head construction that operates more reliably and efficiently than prior art approaches.

The life of a bubble jet heater array resistor, like that of a light bulb, is a very strong function of the voltage at which it is operated. Thus, it is desirable to operate such elements as close to the bubble formation threshold as possible. They may have, for example, a life of only a few
10 cycles at 25% above threshold, but a life of several million cycles at 10% above threshold. Since there are manufacturing tolerances in the supply voltage, various lead resistances, and the threshold voltage, and the desire is to extract excellent life (by
15 operating close to the bubble formation threshold) all resistance variations should be held to a minimum. Because semiconductor diodes in contrast to ordinary conductors decrease their forward resistance as they increase in temperature, the use of diodes in
20 an ink heating region creates great potential for overall circuit resistance variation, if not thermal runaway. Thus, use of resistor/diodes, as in the '853 patent, is not desirable from the viewpoint of a long device operating life.

25 In one aspect, the present invention provides an improved construction for a drop-on-demand, ink jet printer of the kind having a plurality of orifices, a thin film drop ejector, including heater elements, and manifold means for
30 supplying ink to the heater elements. The improved construction constitutes a drop ejection device having (i) a support substrate; (ii) a first circuit comprising a plurality of circuit branches that each include a resistive heater element and a diode
35 device, formed in spaced relation on the substrate; (iii) a dielectric passivation layer overlying the first circuit except at the discrete terminal

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regions; (iv) a second circuit comprising a plurality of multiplex electrode lines overlying the passivation layer and including connection sections extending through the passivation layer into contact with terminal regions of the first circuit; and (v) a second passivation layer overlying the second circuit portion but not overlying the resistive heater elements.

Brief Description of Drawings

10 FIG. 1 is an exploded perspective view of one ink jet print/cartridge assembly incorporating the present invention;

 FIG. 2 is an enlarged schematic illustration of the circuit structures of the drop ejection device of the FIG. 1 printer assembly;

 FIG. 3 is an enlarged cross-section of a portion of the circuit structure shown in FIG. 2; and

 FIGS. 4A to 4G are schematic plan views illustrating successive stages of fabricating the circuit structure portions shown in FIG. 3.

Modes of Carrying Out the Invention

 FIG. 1 illustrates one bubble jet print/cartridge embodiment of the present invention, which is a modification of the print/cartridge described in U.S. Patent No. 4,734,717. In general, the print/cartridge 10 comprises an ink reservoir portion 11 having a cap 12 with filtering ink supply openings 13 that lead via supply passages 14 to manifold regions 15. A drop ejection chip 16 mounts over the top of the cap and has openings 17 aligned with manifold region 15. An orifice plate 18 having two linear arrays of orifices 18a, 18b is mounted, by a printed adhesive pattern 19, over the top of openings 17 and the resistive heater elements formed in corresponding arrays 20a, 20b on the top surface of the drop ejection chip. As shown generally in FIG. 1, the chip 16 also has group address electrodes

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22a, 22b and group selection electrodes 23, 24. A significant aspect of the present invention is the improved construction and function of the chip 16 and its circuits, which are described in detail subsequently. However, to complete the general description of the print/cartridge it should be explained that ink is supplied, e.g. by capillary action, from the ink reservoir to regions over the resistive heater elements. Current pulses are selectively directed via chip electrodes, through the heater elements, in accord with information signals that gate driver circuits (not shown). The current pulses heat the resistive elements, which vaporize adjacent ink to eject drops of ink from the related orifices.

Referring to FIG. 2, a portion of the chip 16, designated by dotted line 30, is shown in enlarged schematic plan view. In general, the circuit system comprises a first circuit portion, which is formed on a substrate and is indicated by solid lines, second circuit portions (indicated by stippled regions) and a dielectric passivation layer (not shown) intermediate the two circuit portions. More specifically, it can be seen that the first circuit portion comprises a plurality of branch subcircuits 31a-31L that each include a resistive heater element 32 and a diode 33 formed in spaced relation on the substrate and coupled by electrode lines 35 (see subcircuit 31g). Branch electrode lines for subcircuits 31a-31c and 31g-31i extend directly to terminals 36-A, 36-B which are connectible to print pulse drive circuits (not shown).

The second circuit portion comprises six coupling electrode portions 37, which connect the input terminals 36-A, 36-B to respective lines of subcircuits 31c-31d and 31j-31L, and two group selection electrodes 38, 39 which are coupled

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respectively to the opposite ends of subcircuit lines to form subcircuit selection group I (31a-31c and 31g-31i) and group II (31d-31f and 31j-31L).

Selection electrode 38 extends to a group I selection terminal 40 and selection electrode 39 extends to group II selection terminal 41. The cross-hatched areas on the stippled second circuit indicate regions where the electrode extends through the intermediate dielectric passivation layer to make contact with the first circuit portion branches. Otherwise the two circuit portions are electrically isolated from one another by the dielectric passivation layer.

FIG. 3 shown a cross-section of a portion of one actual circuit structure, such as illustrated schematically within the dotted line box 50 of FIG.

2. Thus, substrate 51 can comprise a glass or glazed silicon chip having a heat control top surface (not shown) on which a resistive metal layer 32 (e.g. TaAl or HfB_2) is formed. Electrode leads 35a, 35b are deposited onto the resistive layer except over the ink heating region H so that current will pass from one electrode to another, down through the resistive layer at the heating region. First passivation layer 52, e.g. SiO_2 , is formed over the electrodes and heater elements except at regions 53, 54. At region 53, a coupling portion of electrode 37 extends into contact with address electrode 35a and over the surface of the dielectric passivation layer 52 to couple with branch circuit 31c (not shown in FIG. 3). At region 54 a diode layer, (e.g. comprising silicon and gold) is first deposited and then a coupling portion of group select electrode 38 is deposited to extend over layer 52 to terminal 40 (not shown in FIG. 3). A second passivation, e.g. polyamide, layer 60 is then formed over the regions 53, 54 and other top surfaces of the chip, but not over heating region H.

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Reference to the fabrication sequences of the portion shown in FIG. 3, as illustrated in FIGS. 4A-4G, will assist in understanding the chip construction according to the present invention. As shown in FIG. 4A a layer 32 of resistive material is first deposited on the surface of substrate 51 in regions that include the eventual heat transfer region. Next, as shown in FIG. 4B, metal electrodes 35a and 35b are formed with ends defining the current path through the resistive material at the heating region. A layer(s) of diode material 54 is then deposited onto the top of electrodes 35b, see FIG. 4C, and a dielectric passivation layer 52 (e.g. SiO_2 or Zr) is deposited over the surface as shown in FIG. 4D.

Referring to FIG. 4E, it can be seen that the passivation layer is patterned to reveal portions of electrode 35a and diode 54. At this stage, if Zr is used for formation of layer 52 it can be oxidized to form ZrO_2 . The second circuit portion comprising electrodes 37, 38 are then deposited and patterned in the configuration shown in FIG. 4F, and finally the second passivation layer 60 is deposited and patterned to provide a chip having the topography shown in FIG. 4G. It should be noted that layer 60 can be polyamide or similar material because it is not overlying the heat transfer path, wherein layer 52 provides the protective cover for the resistive heater elements.

Referring again to FIGS. 2 and 3, in operation, enable pulses are sequentially applied to terminals 40 and 41. During enable of electrode 38, the diodes of branch circuits 31a-31c and 31g-31i are forwardly biased and when driver pulses are applied to the terminals 36-A, 36-B in accord with information signals electrodes 35 of those circuits can conduct current through resistive heaters of the

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circuits to heat overlying ink and effect bubble jetting of an ink drop through their corresponding orifice of orifice plate 18. When an enable pulse is operative on terminal 41, electrode 39 forwardly
5 biasing the diodes of branch circuits 31d-31f and 31j-31L so that driver pulses applied to terminals 36-A and 36-B are transmitted to respective ones of those branch circuits (via coupling electrodes 37) to similarly effect drop ejections in accord with
10 information signal gating the driver circuits.

Thus, the circuit construction embodiment described above in accord with the present invention provides the advantages of multiplexing operation without the necessity of patterning two sides of drop
15 ejection chip. Moreover, the circuit constructions of the present invention remove the circuit diode elements from the regions of heat generation. As noted previously, this is particularly important in devices using thin film circuit components which are
20 operated repeatedly at high duty cycles, such as the bubble jet printing devices described above.

Industrial Applicability

The present invention affords industrial advantages by providing a simple ink jet print head
25 construction for reliable multiplex operation to achieve high density printing.

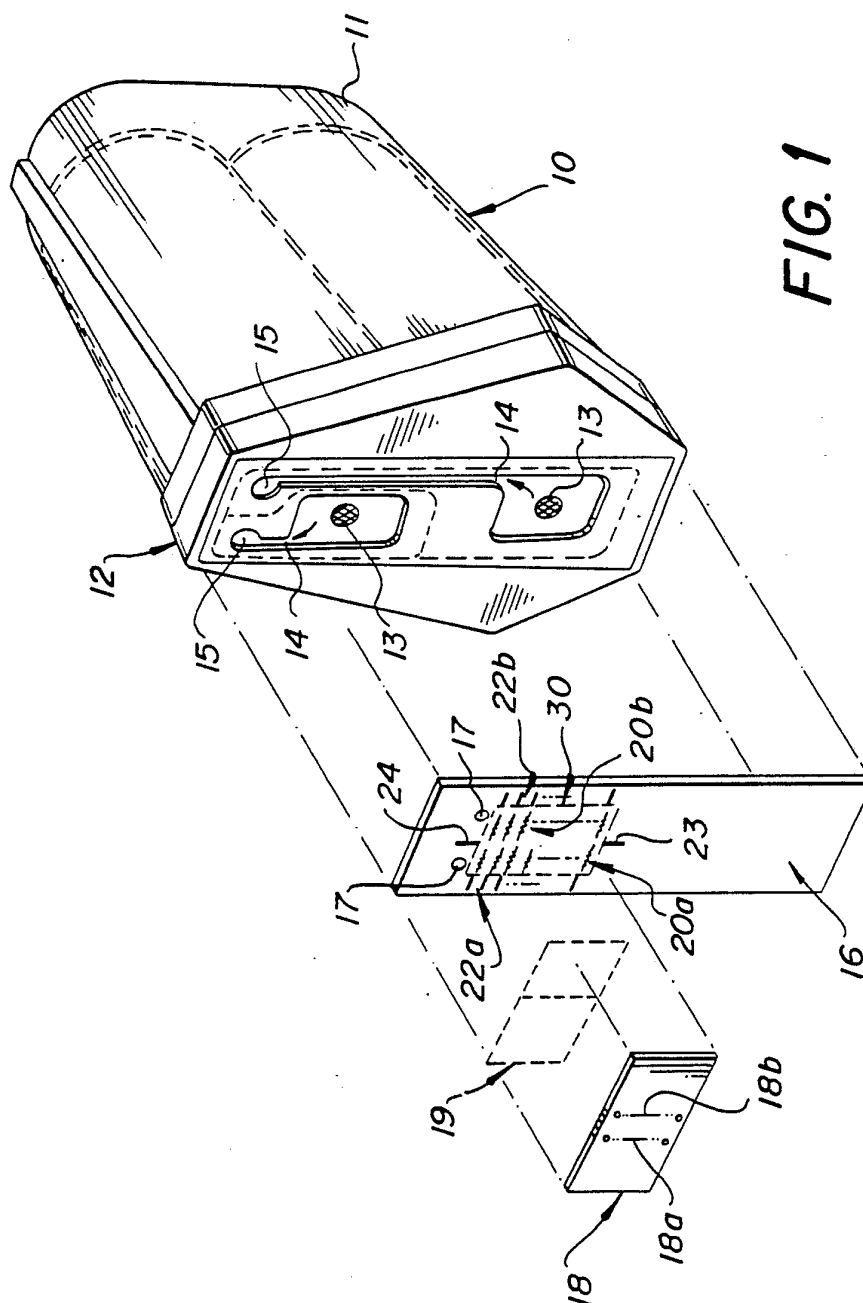
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Claims:

1. In a drop-on-demand ink jet printer of the kind having a plurality of orifices, a thin film drop ejection device including a corresponding plurality of drop ejection heater elements and manifold means for supplying ink to said heater elements, the improvement wherein said drop ejection device comprises:
- (a) a support substrate;
 - 10 (b) a first circuit portion comprising a plurality of circuit branches that each include a resistive heater element and a diode device formed in spaced relation on said substrate and coupled in series by first circuit branch electrode lines to
 - 15 terminal regions;
 - (c) a dielectric passivation layer overlying said first circuit portion except at the discrete terminal regions of said first circuit portion branches;
 - 20 (d) a second circuit portion comprising a plurality of multiplex electrode lines overlying said dielectric passivation layer and including connection sections extending through said passivation layer into contact with terminal regions of said first
 - 25 circuit portion; and
 - (e) a second passivation layer overlying said second circuit portion, but not overlying said resistive heater elements.
2. The invention defined in claim 1
- 30 wherein said first circuit portion comprises at least two of said circuit branches and said second circuit portion comprises:
- (i) an electrode line coupling the print pulse input sides of said circuit branches to a
 - 35 common driver circuit terminal; and
 - (ii) separate electrode lines respectively coupling the diode outputs of said circuit branches to separate enable terminals.



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FIG. 2

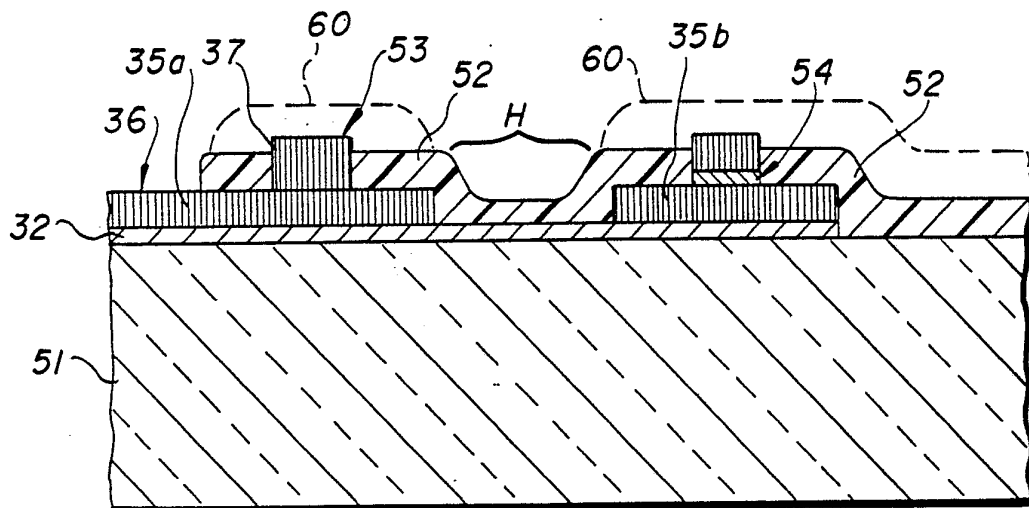
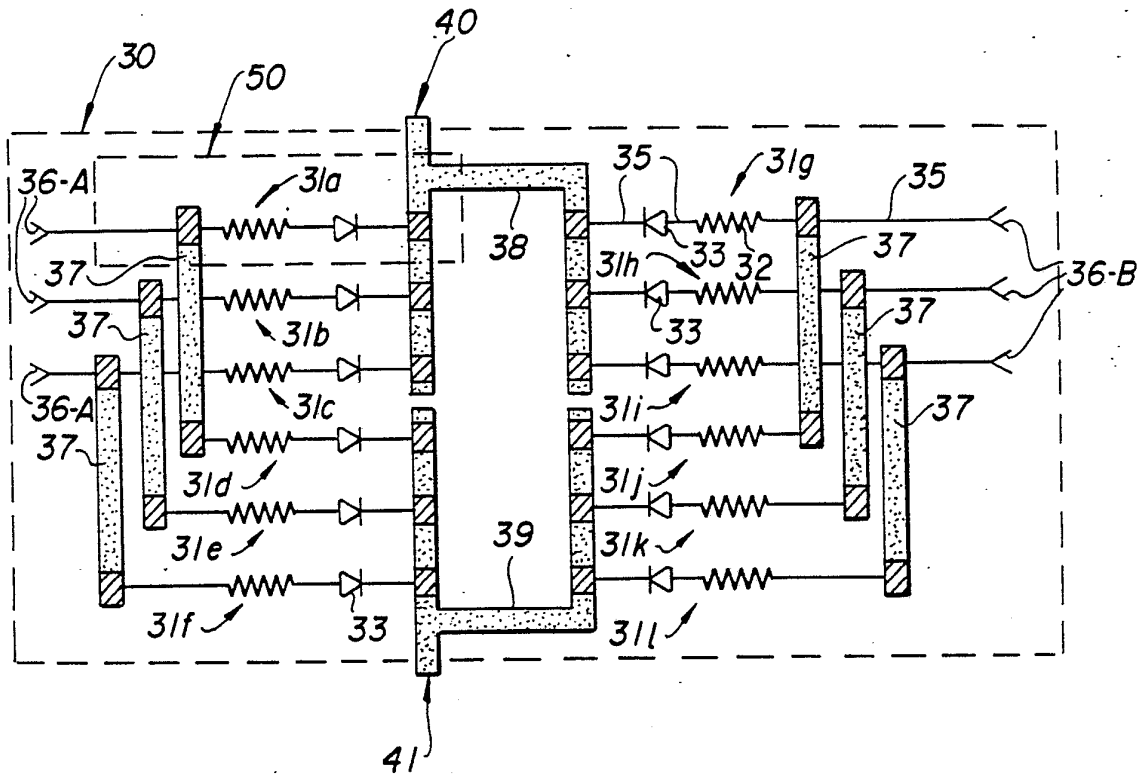


FIG. 3

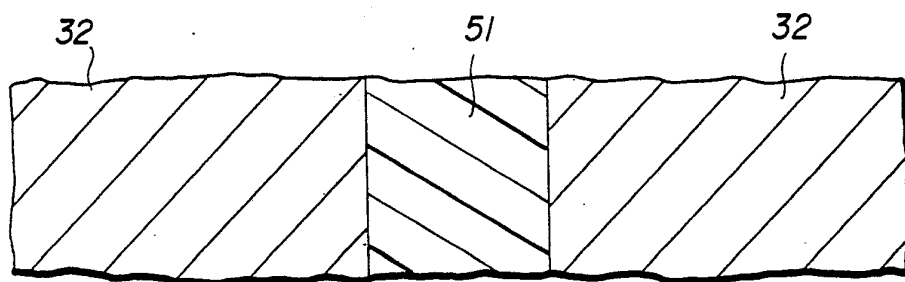


FIG. 4A

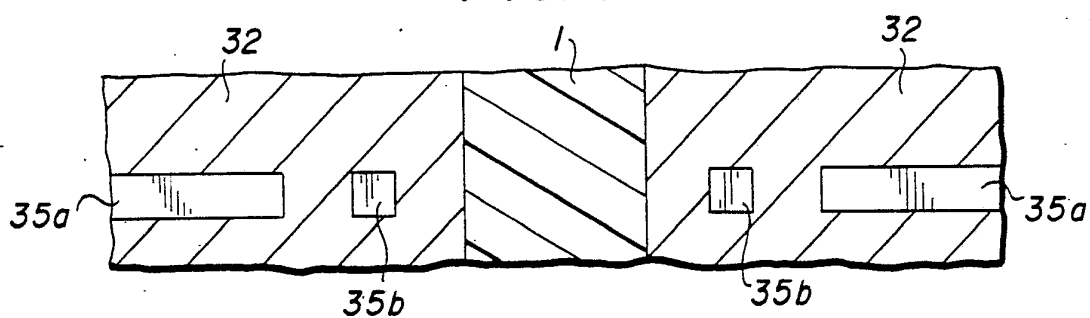


FIG. 4B

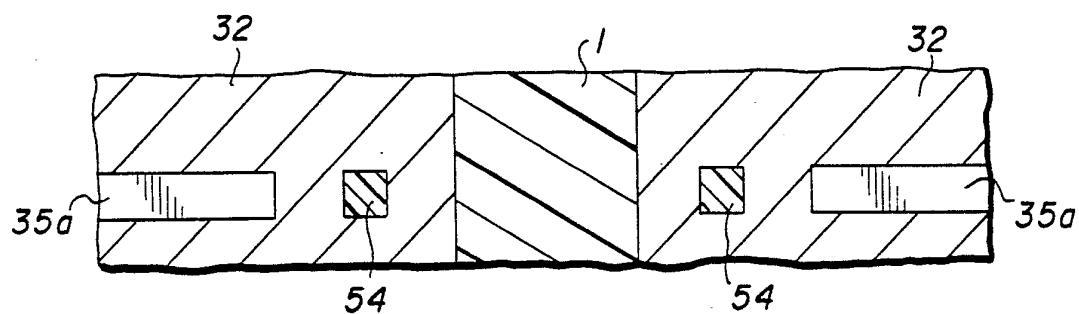


FIG. 4C

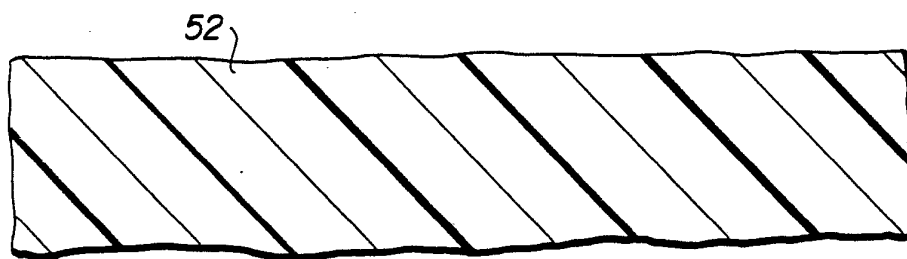


FIG. 4D

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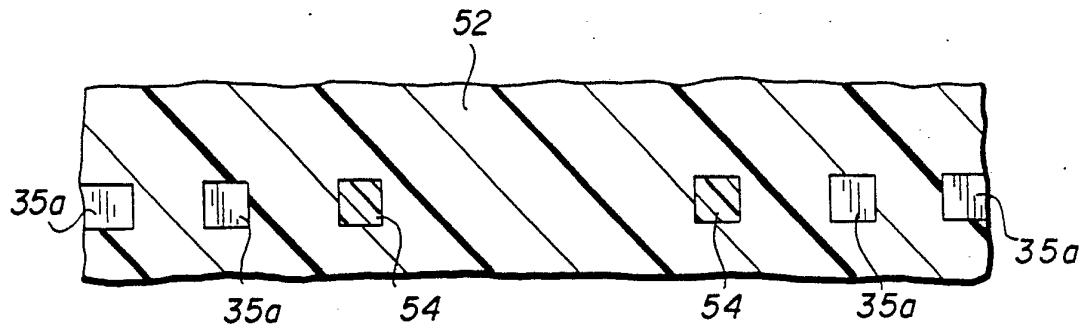


FIG. 4E

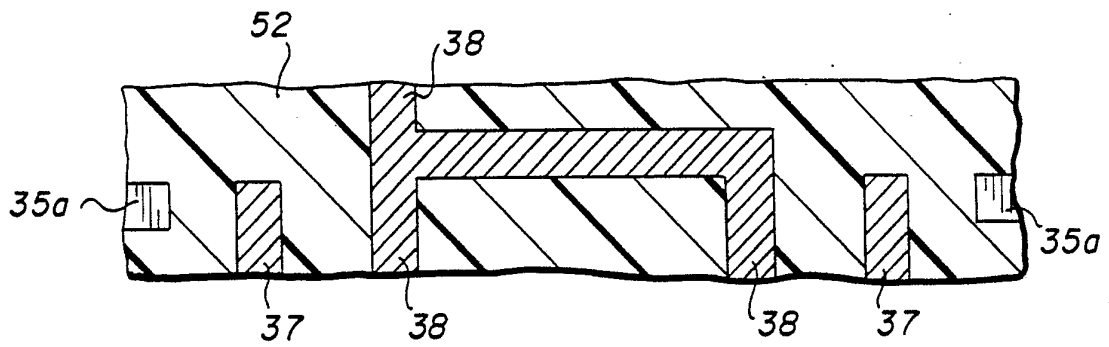


FIG. 4F

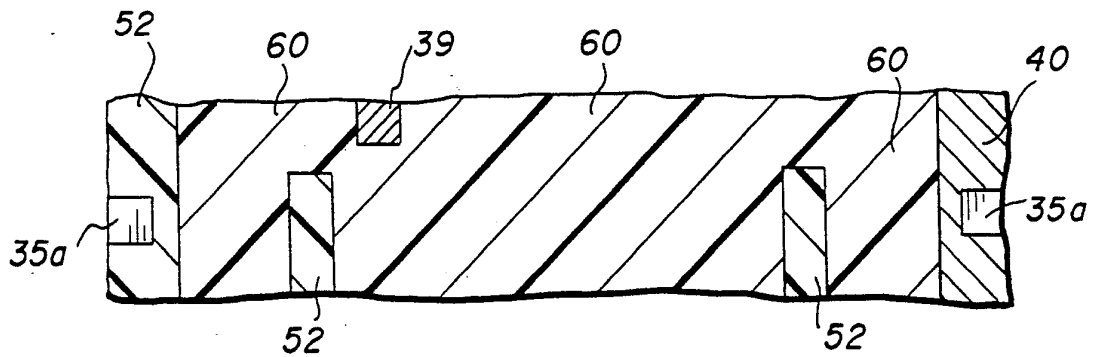
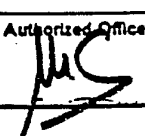


FIG. 4G

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 90/07141

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) ⁶		
According to International Patent Classification (IPC) or to both National Classification and IPC		
IPC ⁵ : B 41 J 2/05		
II. FIELDS SEARCHED		
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Classification System	Classification Symbols	
IPC ⁵	B 41 J, G 01 D	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched ⁸		
III. DOCUMENTS CONSIDERED TO BE RELEVANT ⁹		
Category ⁹	Citation of Document, ¹¹ with Indication, where appropriate, of the relevant passages ¹²	Relevant to Claim No. ¹³
A	US, A, 4 791 440 (ELDRIDGE) 13 December 1988 (13.12.88), see claims. ---	1, 2
P, A	EP, A2, 0 349 959 (CANON KABUSHIKI KAISHA) 10 January 1990 (10.01.90), see totality. ---	1, 2
A	EP, A1, 0 344 809 (CANON KABUSHIKI KAISHA) 6 December 1989 (06.12.89), see totality. ---	1
A	US, A, 4 695 853 (HACKLEMAN) 22 September 1987 (22.09.87), see abstract. ----	1
<div style="display: flex; justify-content: space-between;"> <div style="width: 48%;"> <p>¹⁰ Special categories of cited documents: ¹⁰</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the International filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the International filing date but later than the priority date claimed</p> </div> <div style="width: 48%;"> <p>"T" later document published after the International filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"Z" document member of the same patent family</p> </div> </div>		
IV. CERTIFICATION		
Date of the Actual Completion of the International Search	Date of Mailing of this International Search Report	
04 April 1991 (04.04.91)	24.05.91	
International Searching Authority	Signature of Authorized Officer	
EUROPEAN PATENT OFFICE	 M. SOTELO	

ANHANG
zum internationalen Recherchen-
bericht über die internationale
Patentanmeldung Nr.

ANNEX
to the International Search
Report to the International Patent
Application No.

ANNEXE
au rapport de recherche inter-
national relatif à la demande de brevet
international n°

SA43858

PCT/US 90/07141

In diesem Anhang sind die Mitglieder
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US-A - 4791440	13-12-88	EP-A2- 289347	02-11-88
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