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(54) Title: PARAMETER-STABLE MISREGISTRATION MEASUREMENT AMELIORATION IN SEMICONDUCTOR DEVICES

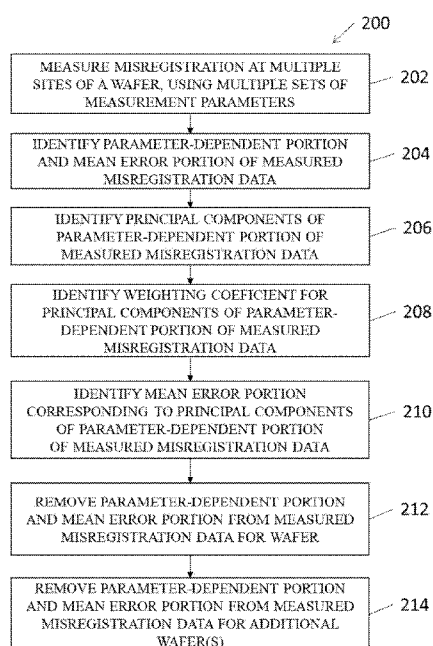


FIG. 2

(57) Abstract: A parameter-stable misregistration measurement amelioration system and method including providing a wafer, including a plurality of multi-layered semiconductor devices formed thereon, selected from a batch wafers intended to be identical, using a misregistration metrology tool to measure misregistration at multiple sites between at least a first layer and a second layer of the wafer, using a plurality of sets of measurement parameters, thereby generating measured misregistration data for each of the sets of measurement parameters, identifying and removing a parameter-dependent portion and a mean error portion from the measured misregistration data for the wafer for each of the sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for the wafer.

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PARAMETER-STABLE MISREGISTRATION MEASUREMENT AMELIORATION
IN SEMICONDUCTOR DEVICES

REFERENCE TO RELATED APPLICATIONS

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[0001] Reference is hereby made to U.S. Provisional Patent Application Serial
No. 62/821,596, filed March 21, 2019 and entitled DYNAMIC ACCURACY
OPTIMIZATION BASED ON OVERLAY ERROR VARIATION WITH
MEASUREMENT CONDITIONS, the disclosure of which is hereby
10 incorporated by reference and priority of which is hereby claimed.

FIELD OF THE INVENTION

[0002] The present invention relates measurement of misregistration in the manufacture
15 of semiconductor devices generally.

BACKGROUND OF THE INVENTION

[0003] Various methods and systems are known for measurement of
20 misregistration in the manufacture of semiconductor devices.

SUMMARY OF THE INVENTION

[0004] The present invention seeks to provide an improved methods and systems
25 for measurement of misregistration in the manufacture of semiconductor devices.

[0005] There is thus provided in accordance with a preferred embodiment of the
present invention a parameter-stable misregistration measurement amelioration
method including providing a wafer, including a plurality of multilayered
30 semiconductor devices formed thereon, selected from a batch wafers intended to
be identical, using a misregistration metrology tool to measure misregistration at
multiple sites between at least a first layer and a second layer of the wafer, using

a plurality of sets of measurement parameters, thereby generating measured misregistration data for each of the sets of measurement parameters, identifying and removing a parameter-dependent portion and a mean error portion from the measured misregistration data for the wafer for each of the sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for the wafer.

[0006] In accordance with a preferred embodiment of the present invention, the sets of measurement parameters comprise at least multiple wavelengths of light used in misregistration measurement.

[0007] Preferably, the identifying the parameter-dependent portion and the mean error portion includes identifying a parameter-dependent portion for the measured misregistration data for each of the sets of measurement parameters, identifying at least one principal component of the parameter-dependent portion of the misregistration data for each of the sets of measurement parameters, identifying a weighting coefficient for the at least one principal component of the parameter-dependent portion of the measured misregistration data for each of the sets of parameters and identifying at least one mean error portion, each of the mean error portions corresponding to each of the at least one principal components of the parameter-dependent portion of the measured misregistration data for each of the sets of measurement parameters.

[0008] In accordance with a preferred embodiment of the present invention the parameter-stable misregistration measurement amelioration method also includes using the parameter-dependent portion and the mean error portion to identify and remove a parameter-dependent portion and a mean error portion from measured misregistration data for at least one additional wafer selected from the batch wafers intended to be identical, for each of the sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for the at least one additional wafer.

[0009] In accordance with a preferred embodiment of the present invention, the misregistration metrology tool is an imaging misregistration metrology tool. Alternatively, in accordance with a preferred embodiment of the present invention, the misregistration metrology tool is a scatterometry misregistration metrology tool.

[0010] Preferably, the at least one principal component of the parameter-dependent portion of the misregistration data for each of the sets of measurement parameters is identified using principal component analysis.

[0011] In accordance with a preferred embodiment of the present invention, the mean error portions are identified using a reference misregistration value. Preferably, the reference misregistration value is generated by using a reference misregistration metrology tool to measure the wafer. Preferably, the reference misregistration tool is an electron beam misregistration metrology tool.

[0012] Alternatively, in accordance with a preferred embodiment of the present invention, the mean error portions are identified using a statistical model. Preferably, the statistical model is compiled from multiple misregistration measurements of the wafer. Preferably, the statistical model includes a modeled portion and an unmodeled portion.

[0013] In accordance with a preferred embodiment of the present invention, the sets of measurement parameters comprise at least one of a focus variability in misregistration measurement, a numerical aperture used in misregistration measurement, an angle of incidence of light used in misregistration measurement and a polarization of light used in misregistration measurement.

[0014] There is also provided in accordance with another preferred embodiment of the present invention a parameter-stable misregistration measurement amelioration system including a misregistration metrology tool operative to measure misregistration at multiple sites between at least a first layer and a second

layer of a wafer, including a plurality of multilayered semiconductor devices formed thereon, selected from a batch of wafers intended to be identical, using a plurality of sets of measurement parameters, thereby generating measured misregistration data for each of the parameters and a misregistration data analyzer operative to identify and remove a parameter-dependent portion and a mean error portion from the measured misregistration data for the wafer for each of the sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for the wafer.

BRIEF DESCRIPTION OF THE DRAWINGS

[0015] The present invention will be understood and appreciated more fully from the following detailed description, taken in conjunction with the drawings in which:

Fig. 1 is a simplified schematic illustration of a parameter-stable misregistration measurement amelioration system; and

Fig. 2 is a simplified flow chart illustrating a parameter-stable misregistration measurement amelioration method useful by the parameter-stable misregistration measurement amelioration system of Fig. 1.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0016] It is appreciated that the system and method described hereinbelow with reference to Figs. 1 & 2 form part of a manufacturing process for semiconductor devices, and the misregistration measured by the system and method described hereinbelow with reference to Figs. 1 & 2 is used to adjust fabrication processes of the semiconductor devices to more closely align various layers of the semiconductor devices being fabricated.

[0017] Reference is now made to Fig. 1, which is a simplified schematic illustration of a parameter-stable misregistration measurement amelioration system (PSMMAS) 100, and to Fig. 2, which is a simplified flow chart illustrating

a parameter-stable misregistration measurement amelioration method (PSMMAM) 200 useful by PSMMAS 100.

[0018] As seen in Fig. 1, PSMMAS 100 includes a misregistration metrology tool 110 and a misregistration data analyzer 120. Misregistration metrology tool 110 can be any suitable misregistration metrology tool, having the capability to measure misregistration using a plurality of sets of measurement parameters, such as an imaging misregistration metrology tool or a scatterometry misregistration metrology tool. Preferably, the parameters include multiple wavelengths of light used in measuring misregistration. A typical imaging misregistration metrology tool forming part of PSMMAS 100 is an Archer™ 700, commercially available from KLA Corporation, of Milpitas, CA. A typical scatterometry misregistration metrology tool forming part of PSMMAS 100 is an ATL100™, commercially available from KLA Corporation, of Milpitas, CA.

[0019] As seen in Fig. 2, at a first step 202, a wafer, including a plurality of multilayered semiconductor devices formed thereon, selected from a batch of wafers intended to be identical, is provided, and misregistration metrology tool 110 measures misregistration, also referred to as overlay, at multiple sites, s , between at least a first layer and a second layer of the wafer, using multiple sets of measurement parameters, λ , preferably including multiple wavelengths of light, thereby generating measured misregistration data, $OVL(\lambda, s)$, for each site and for each of the sets of parameters. It is appreciated that each of the wafers in the batch of wafers intended to be identical undergo the same fabrication steps and include semiconductor devices intended to be identical to corresponding semiconductor devices on all other wafers in the batch of wafers intended to be identical.

[0020] Typically, the wafer measured at step 202 includes features having some number, n , of deformations included in each of the measured sites. Thus, measured misregistration data, $OVL(\lambda, s)$, may include components from both a misregistration between the first and second layers $OVLo(s)$, as well as

components from each deformation eigenvector $\epsilon_k(\lambda, s)$, as described in equation 1:

$$OVL(\lambda, s) = OVL_0(s) + \sum_{k=1}^n \alpha_k(s) \epsilon_k(\lambda, s) \quad (\text{Eq. 1})$$

where k is an index indicating a deformation and $\alpha_k(s)$ is a weighting coefficient for each deformation eigenvector $\epsilon_k(\lambda, s)$. It is noted that unlike the misregistration of the wafer $OVL_0(s)$, the terms from each deformation eigenvector $\epsilon_k(\lambda, s)$ are dependent on a parameter, such as wavelength of light, used in the misregistration measurement. It is further noted that each deformation eigenvector $\epsilon_k(\lambda, s)$ includes terms from both a parameter-dependent portion $\epsilon_k(\lambda, s)$ and a mean error portion $\mu_k(s)$, as seen in equation 2:

$$\epsilon_k(\lambda, s) = \epsilon_k(\lambda, s) + \mu_k(s) \quad (\text{Eq. 2})$$

[0021] It is noted that both the parameter-dependent portion $\epsilon_k(\lambda, s)$ and the mean error portion $\mu_k(s)$ result from the same k^{th} deformation. Therefore, both the parameter-dependent portion $\epsilon_k(\lambda, s)$ and the mean error portion $\mu_k(s)$ are proportional to an amplitude of the k^{th} deformation, and thus the parameter-dependent portion $\epsilon_k(\lambda, s)$ and the mean error portion $\mu_k(s)$ are mathematically related to each other.

[0022] In order to solve equations 1 and 2, and thus identify misregistration between the first and second layers, $OVL_0(s)$, PSMMAM 200 proceeds to solve additional equations, as described hereinbelow with further reference to Figs. 2A & 2B.

[0023] At a next step 204, as seen in equation 3, misregistration data analyzer 120 identifies a parameter-dependent portion $OVL_\epsilon(\lambda, s)$ for each site, s , and for each parameter set, λ , of measured misregistration data $OVL(\lambda, s)$ and a mean error portion $OVL_\mu(s)$ for each site s of measured misregistration data $OVL(\lambda, s)$ generated at step 202.

$$OVL(\lambda, s) = OVL_\epsilon(\lambda, s) + OVL_\mu(s) \quad (\text{Eq. 3})$$

[0024] It is noted that in equation 3, mean error portion $OVL_{\mu}(s)$ includes both the misregistration of the wafer $OVL_o(s)$ and a mean error portion associated with the measured misregistration data $OVL(\lambda, s)$.

5 [0025] At a next step 206, misregistration data analyzer 120 uses principal component analysis (PCA) for a set of parameter-dependent portions $\{OVL_e(\lambda, s)\}$ of measured misregistration data $OVL(\lambda, s)$ to identify a set of principal components $\{\varepsilon_k(\lambda, s)\}$ corresponding to the n deformations included in each of the sites, s , measured at step 202.

10

[0026] At a next step 208, misregistration data analyzer 120 identifies a suitable weighting coefficient $\alpha_k(s)$ for equation 1 by identifying a value of weighting coefficient $\alpha_k(s)$ that minimizes a metric M_1 , as defined in equation 4:

$$M_1 = \sum_{\lambda} [OVL_e(\lambda, s) - \sum_{k=1}^n \alpha_k(s) \varepsilon_k(\lambda, s)]^2 \quad (\text{Eq. 4})$$

15

[0027] It is noted that for an orthonormal set of principal components $\{\varepsilon_k(\lambda, s)\}$, equation 4 represents the projections of $\{OVL_e(\lambda, s)\}$ onto $\{\varepsilon_k(\lambda, s)\}$.

[0028] At a next step 210, misregistration data analyzer 120 identifies mean error portions μ_k corresponding to each one of the principle components $\varepsilon_k(\lambda, s)$ identified at step 206 for each measured site, s . In a preferred embodiment of the present invention, mean error portions $\mu_k(s)$ are identified using equation 5:

20

$$OVL_R(s) = OVL_{\mu}(s) - \sum_k \alpha_k(s) \mu_k(s) \quad (\text{Eq. 5})$$

25

where $OVL_R(s)$ is a reference misregistration value for each site of the wafer measured at step 202, and PSMMAM 200 identifies mean error portions $\mu_k(s)$ which result in the best matching between the left-hand-side and the right-hand-side of equation 5 for all sites, s , measured at step 202.

30

[0029] Preferably, reference misregistration $OVL_R(s)$ is generated by using a reference misregistration metrology tool to measure misregistration of the wafer measured at step 202. A typical reference misregistration metrology tool is an electron beam misregistration metrology tool, such as an eDR7xxx™,

commercially available from KLA Corporation of Milpitas, CA, USA. Other suitable reference misregistration metrology tools include, inter alia, optical tools, SEM tools, TEM tools and AFM tools.

5 [0030] In an alternative embodiment of the present invention, a statistical model is compiled from multiple, preferably at least 200, misregistration measurements of the wafer measured at step 202. Typically, each of the misregistration measurements includes a modeled portion, corresponding to actual device misregistration, and an unmodeled portion, corresponding to deformations. Mean error portions $\mu_k(s)$ are identified by identifying values of mean error portions $\mu_k(s)$ that minimize a metric M_2 , as defined in equation 6:

$$M_2 = \sum_s [OVL_{\mu|U}(s) - \sum_k \alpha_{k|U}(s) \mu_k(s)]^2 \quad (\text{Eq. 6})$$

where $OVL_{\mu|U}(s)$ is the unmodeled portion of the mean error portion of the misregistration of each site included in the statistical model and $\alpha_{k|U}(s)$ is the unmodeled portion of the weighting coefficients $\alpha_k(s)$.

[0031] At a next step 212, misregistration data analyzer 120 removes the parameter-dependent portion $\epsilon_k(\lambda, s)$ and the mean error portion $\mu_k(s)$ from the measured misregistration data $OVL(\lambda, s)$ generated at step 202 for the wafer for each of the parameters used in misregistration measurements, thereby generating ameliorated parameter-stable ameliorated misregistration data $OVL_0(s)$ for the wafer.

25 [0032] In a preferred embodiment of the present invention, parameter-stable ameliorated misregistration data $OVL_0(s)$ is used to adjust at least one tool used in the fabrication of the batch of wafers intended to be identical from which the wafer measured in step 202 was selected.

30 [0033] Preferably, at a next step 214, at least one additional wafer, including a plurality of multilayered semiconductor devices, selected from the batch of wafers intended to be identical from which a wafer was provided at step 202, is provided. As part of step 214, misregistration metrology tool 110 measures misregistration

at multiple sites between at least a first layer and a second layer of the wafer, using a plurality of sets of measurement parameters sets, thereby generating measured misregistration data for each of the parameter sets. Preferably, the parameter sets include multiple wavelengths of light.

5

[0034] Then, misregistration data analyzer 120 uses parameter-dependent portion $OVL_e(\lambda, s)$ for the at least one additional measured at step 214 to define the weighting coefficients $\alpha_k(s)$ for the at least one additional wafer. Once weighting coefficients $\alpha_k(s)$ for the at least one additional wafer are known, misregistration data analyzer 120 uses the one or more deformation eigenvectors $\epsilon_k(\lambda, s)$ identified at step 210 to identify and remove the parameter-dependent portion $\epsilon_k(\lambda, s)$ and the mean error portion $\mu_k(s)$ from the measured misregistration data $OVL(\lambda, s)$ generated at step 214 for the at least one additional wafer for each of the parameter sets, thereby generating ameliorated parameter-stable ameliorated misregistration data $OVL_o(s)$ for the one or more additional wafers. It is appreciated that the one or more deformation eigenvectors $\epsilon_k(\lambda, s)$ identified at step 210 include a parameter-dependent portion $\epsilon_k(\lambda, s)$, and a mean error portion $\mu_k(s)$, as described hereinabove with reference to Eq. 2.

[0035] In an alternative embodiment of the present invention, at step 202, the sets of measurement parameters used by misregistration metrology tool 110 include at least one of a focus variability in misregistration measurement, a numerical aperture used in misregistration measurement, an angle of incidence of light used in misregistration measurement and a polarization of light used in misregistration measurement. In such an embodiment, variations in misregistration measurement data as a function of the varied at least one misregistration measurement parameter are preferably analyzed in a similar manner to the analysis described hereinabove with reference to Fig. 2, thereby generating ameliorated parameter-stable ameliorated misregistration data.

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[0036] It will be appreciated by persons skilled in the art that the present invention is not limited to what has been particularly shown and described hereinabove. The

scope of the present invention includes both combinations and subcombinations of various features described hereinabove as well as modifications thereof, all of which are not in the prior art.

CLAIMS

1. A parameter-stable misregistration measurement amelioration method comprising:
 - 5 providing a wafer, comprising a plurality of multilayered semiconductor devices formed thereon, selected from a batch wafers intended to be identical;
 - using a misregistration metrology tool to measure misregistration at multiple sites between at least a first layer and a second layer of said wafer,
 - 10 using a plurality of sets of measurement parameters, thereby generating measured misregistration data for each of said sets of measurement parameters;
 - identifying and removing a parameter-dependent portion and a mean error portion from said measured misregistration data for said wafer for each of
 - 15 said sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for said wafer.
2. The parameter-stable misregistration measurement amelioration method according to claim 1 and wherein said sets of measurement parameters comprise
20 at least multiple wavelengths of light used in misregistration measurement.
3. The parameter-stable misregistration measurement amelioration method according to claim 1 or claim 2 and wherein said identifying said parameter-dependent portion and said mean error portion comprises:
 - 25 identifying a parameter-dependent portion for said measured misregistration data for each of said sets of measurement parameters;
 - identifying at least one principal component of said parameter-dependent portion of said misregistration data for each of said sets of measurement parameters;
 - 30 identifying a weighting coefficient for said at least one principal component of said parameter-dependent portion of said measured misregistration data for each of said sets of parameters; and

identifying at least one mean error portion, each of said mean error portions corresponding to each of said at least one principal components of said parameter-dependent portion of said measured misregistration data for each of said sets of measurement parameters.

5

4. The parameter-stable misregistration measurement amelioration method according to any of claims 1 – 3 and also comprising using said parameter-dependent portion and said mean error portion to identify and remove a parameter-dependent portion and a mean error portion from measured misregistration data for at least one additional wafer selected from said batch wafers intended to be identical, for each of said sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for said at least one additional wafer.
- 10 5. The parameter-stable misregistration measurement amelioration method according to any of claims 1 – 4 and wherein said misregistration metrology tool is an imaging misregistration metrology tool.
- 15 6. The parameter-stable misregistration measurement amelioration method according to any of claims 1 – 4 and wherein said misregistration metrology tool is a scatterometry misregistration metrology tool.
- 20 7. The parameter-stable misregistration measurement amelioration method according to any of claims 1 – 6 and wherein said at least one principal component of said parameter-dependent portion of said misregistration data for each of said sets of measurement parameters is identified using principal component analysis.
- 25 8. The parameter-stable misregistration measurement amelioration method according to any of claims 1 – 7 and wherein said mean error portions are identified using a reference misregistration value.
- 30

9. The parameter-stable misregistration measurement amelioration method according to claim 8 and wherein said reference misregistration value is generated by using a reference misregistration metrology tool to measure said wafer.
- 5 10. The parameter-stable misregistration measurement amelioration method according to claim 9 and wherein said reference misregistration tool is an electron beam misregistration metrology tool.
- 10 11. The parameter-stable misregistration measurement amelioration method according to any of claims 1 – 7 and wherein said mean error portions are identified using a statistical model.
12. The parameter-stable misregistration measurement amelioration method according to claim 11 and wherein said statistical model is compiled from multiple misregistration measurements of said wafer.
- 15 13. The parameter-stable misregistration measurement amelioration method according to claim 11 or claim 12 and wherein said statistical model comprises a modeled portion and an unmodeled portion.
- 20 14. The parameter-stable misregistration measurement amelioration method according to any of claims 1 – 13 and wherein said sets of measurement parameters comprise at least one of:
- 25 a focus variability in misregistration measurement;
- a numerical aperture used in misregistration measurement;
- an angle of incidence of light used in misregistration measurement; and
- a polarization of light used in misregistration measurement.
- 30 15. A parameter-stable misregistration measurement amelioration system comprising:
- a misregistration metrology tool operative to measure misregistration at multiple sites between at least a first layer and a second layer of a wafer, comprising a plurality of multilayered semiconductor devices formed

thereon, selected from a batch of wafers intended to be identical, using a plurality of sets of measurement parameters, thereby generating measured misregistration data for each of said parameters; and
a misregistration data analyzer operative to:

5 identify and remove a parameter-dependent portion and a mean error portion from said measured misregistration data for said wafer for each of said sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for said wafer.

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16. The parameter-stable misregistration measurement amelioration system according to claim 15 and wherein said sets of measurement parameters comprise at least multiple wavelengths of light used in misregistration measurement.

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17. The parameter-stable misregistration measurement amelioration system according to claim 15 or claim 16 and wherein said misregistration data analyzer is further operative to:

 identify a parameter-dependent portion for said measured misregistration data for each of said sets of measurement parameters;

20

 identify at least one principal component of said parameter-dependent portion of said misregistration data for each of said sets of measurement parameters;

 identify a weighting coefficient for said at least one principal component of said parameter-dependent portion for said measured misregistration data for each of said sets of measurement parameters; and

25

 identify at least one mean error portion, each of said at least one mean error portion corresponding to each of said at least one principal components of said parameter-dependent portion of said measured misregistration data for each of said sets of measurement parameters.

30

18. The parameter-stable misregistration measurement amelioration system according to any of claims 15 – 17 and wherein said analyzer is further operative to use said

parameter-dependent portion and said mean error portion to identify and remove said parameter-dependent portion and a mean error portion from said measured misregistration data for at least one additional wafer selected from said batch of wafers intended to be identical, for each of said sets of measurement parameters, thereby generating ameliorated parameter-stable ameliorated misregistration data for said at least one additional wafer.

19. The parameter-stable misregistration measurement amelioration system according to any of claims 15 – 18 and wherein said misregistration metrology tool is an imaging misregistration metrology tool.

20. The parameter-stable misregistration measurement amelioration system according to any of claims 15 – 18 and wherein said misregistration metrology tool is a scatterometry misregistration metrology tool.

21. The parameter-stable misregistration measurement amelioration system according to any of claims 15 – 20 and wherein said at least one principal component of said parameter-dependent portion of said misregistration data for each of said sets of measurement parameters is identified using principal component analysis.

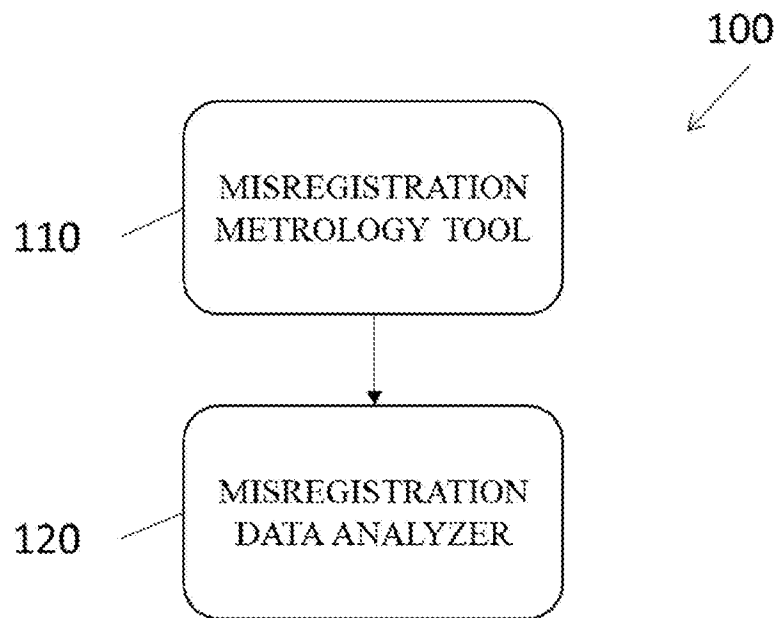
22. The parameter-stable misregistration measurement amelioration system according to any of claims 15 – 21 and wherein said mean error portions are identified using a reference misregistration value.

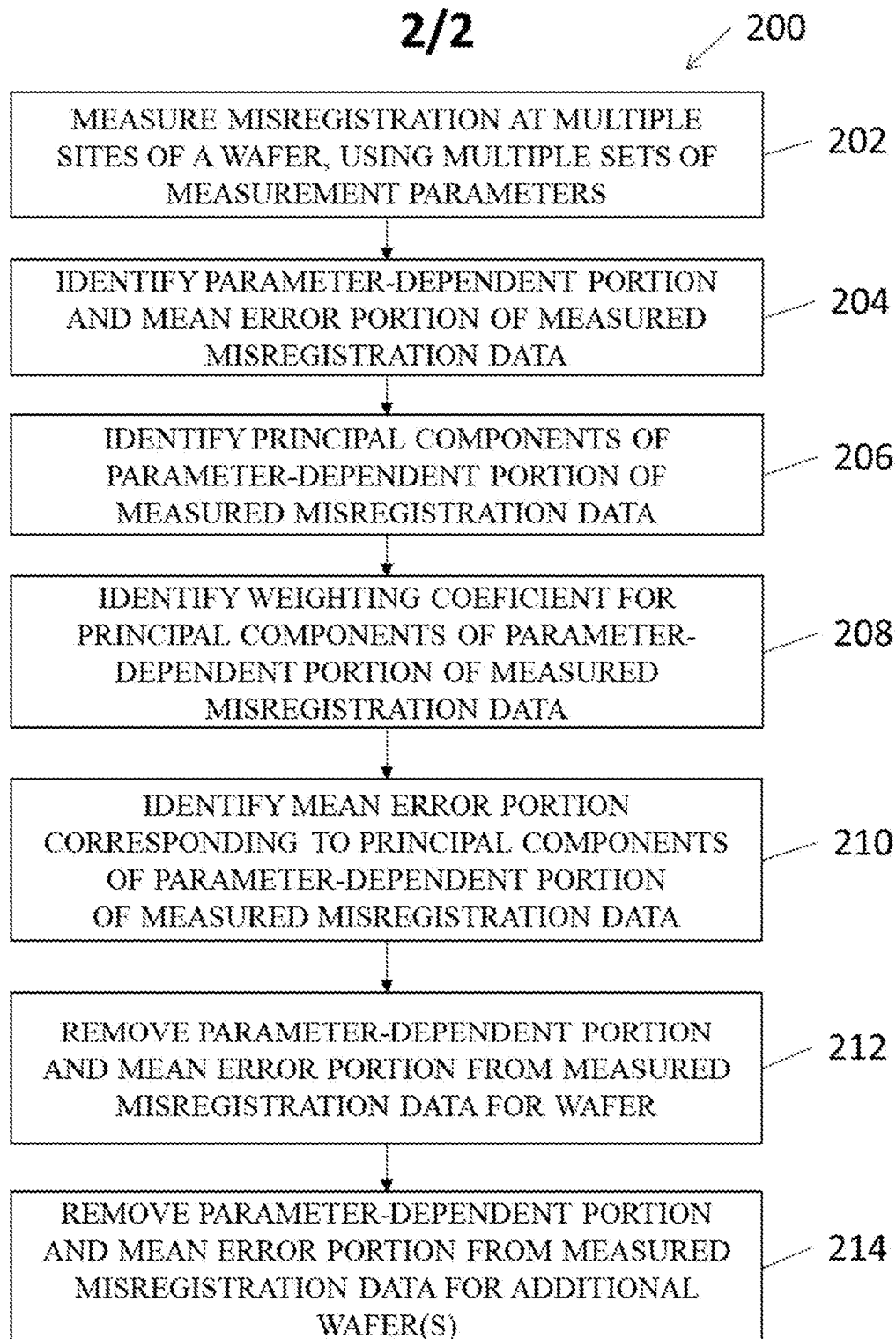
23. The parameter-stable misregistration measurement amelioration system according to claim 22 and wherein said reference misregistration value is generated by using a reference misregistration metrology tool to measure said wafer.

24. The parameter-stable misregistration measurement amelioration system according to claim 23 and wherein said reference misregistration tool is an electron beam misregistration metrology tool.

25. The parameter-stable misregistration measurement amelioration system according to any of claims 15 – 21 and wherein said mean error portions are identified using a statistical model.
- 5 26. The parameter-stable misregistration measurement amelioration system according to claim 25 and wherein said statistical model is compiled from multiple misregistration measurements of said wafer.
- 10 27. The parameter-stable misregistration measurement amelioration system according to claim 25 or claim 26 and wherein said statistical model comprises a modeled portion and an unmodeled portion.
- 15 28. The parameter-stable misregistration measurement amelioration system according to any of claims 15 – 27 and wherein said sets of measurement parameters comprise at least one of:
 a focus variability in misregistration measurement;
 a numerical aperture used in misregistration measurement;
 an angle of incidence of light used in misregistration measurement; and
 a polarization of light used in misregistration measurement.

20

1/2**FIG. 1**

**FIG. 2**

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2019/047797**A. CLASSIFICATION OF SUBJECT MATTER****H01L 21/66(2006.01)i, H01L 21/67(2006.01)i, G01N 21/95(2006.01)i, G01N 21/88(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/66; G01B 11/00; G01B 11/24; G01N 23/225; G06F 15/00; G06F 15/18; H01L 21/027; H01L 21/67; G01N 21/95; G01N 21/88

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: multi-layer, overlay, misregistration, mean error, adjust, parameter

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2009-0063378 A1 (PAVEL IZIKSON) 05 March 2009 See paragraphs [0006], [0033], [0057], [0061]-[0062], [0076], claim 1 and figures 7-8.	1-3, 15-17
A	US 2013-0096876 A1 (NOVA MEASURING INSTRUMENTS LTD.) 18 April 2013 See claim 1 and figures 3-4.	1-3, 15-17
A	KR 10-1281301 B1 (KLA-TENCOR CORPORATION) 03 July 2013 See claims 1-2, 8 and figures 2-3.	1-3, 15-17
A	KR 10-1749440 B1 (HITACHI HIGH-TECHNOLOGIES CORPORATION) 20 June 2017 See claims 1, 6 and figure 16.	1-3, 15-17
A	US 7102749 B2 (NOAH BAREKET) 05 September 2006 See claims 1, 10 and figures 4-6.	1-3, 15-17



Further documents are listed in the continuation of Box C.



See patent family annex.

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19 December 2019 (19.12.2019)

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20 December 2019 (20.12.2019)

Name and mailing address of the ISA/KR

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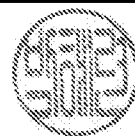


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International application No.
PCT/US2019/047797

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☒ Claims Nos.: 9-10,12,23-24,26
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
Claims 9-10, 12, 23-24, 26 are regarded to be unclear because the claim refer to multiple dependent claim which do not comply with PCT Rule 6.4(a).
3. ☒ Claims Nos.: 4-8,11,13-14,18-22,25,27-28
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

This International Searching Authority found multiple inventions in this international application, as follows:

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fees, this Authority did not invite payment of any additional fees.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest and, where applicable, the payment of a protest fee.
- ☐ The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- ☐ No protest accompanied the payment of additional search fees.

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2019/047797

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2009-0063378 A1	05/03/2009	CN 101939833 A CN 101939833 B EP 2188832 A2 EP 2188832 B1 IL 204014 A JP 2010-538474 A JP 5606313 B2 KR 10-1381304 B1 KR 10-2010-0063095 A US 7873585 B2 WO 2009-029851 A2 WO 2009-029851 A3	05/01/2011 15/03/2017 26/05/2010 27/02/2019 29/08/2013 09/12/2010 15/10/2014 04/04/2014 10/06/2010 18/01/2011 05/03/2009 30/04/2009
US 2013-0096876 A1	18/04/2013	AT 482387 T EP 2165178 A1 EP 2165178 B1 IL 202492 A JP 2010-533376 A JP 5462158 B2 KR 10-1602965 B1 KR 10-2010-0045952 A TW 200912293 A TW I416096 B US 2010-0141948 A1 US 2014-0142869 A1 US 8289515 B2 US 8643842 B2 US 8964178 B2 WO 2009-007981 A1	15/10/2010 24/03/2010 22/09/2010 30/06/2010 21/10/2010 02/04/2014 11/03/2016 04/05/2010 16/03/2009 21/11/2013 10/06/2010 22/05/2014 16/10/2012 04/02/2014 24/02/2015 15/01/2009
KR 10-1281301 B1	03/07/2013	EP 2386114 A2 EP 2386114 B1 JP 2012-514871 A JP 2015-039021 A JP 2016-066093 A JP 5635011 B2 JP 5855728 B2 US 2010-0175033 A1 US 8214771 B2 WO 2010-080732 A2 WO 2010-080732 A3	16/11/2011 31/10/2018 28/06/2012 26/02/2015 28/04/2016 03/12/2014 09/02/2016 08/07/2010 03/07/2012 15/07/2010 07/10/2010
KR 10-1749440 B1	20/06/2017	CN 105074896 A CN 105074896 B JP 6106743 B2 TW 201504622 A TW 201842329 A TW 201842347 A TW I644099 B	18/11/2015 27/04/2018 05/04/2017 01/02/2015 01/12/2018 01/12/2018 11/12/2018

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2019/047797

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 7102749 B2	05/09/2006	TW I654438 B	21/03/2019
		US 10445875 B2	15/10/2019
		US 2016-0005157 A1	07/01/2016
		US 2018-0247400 A1	30/08/2018
		US 9990708 B2	05/06/2018
		WO 2014-129307 A1	02/02/2017
		WO 2014-129307 A1	28/08/2014
		JP 2004-501516 A	15/01/2004
		JP 4789393 B2	12/10/2011
		US 2003-0206303 A1	06/11/2003
		US 2005-0174574 A1	11/08/2005
		US 6462818 B1	08/10/2002
		US 6580505 B1	17/06/2003
		US 6894783 B2	17/05/2005
		WO 01-98761 A1	27/12/2001