

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
19 March 2009 (19.03.2009)

PCT

(10) International Publication Number  
**WO 2009/034450 A2**

(51) International Patent Classification:  
**H04L 12/26** (2006.01) **H04L 12/40** (2006.01)

(21) International Application Number:  
PCT/IB2008/002365

(22) International Filing Date:  
11 September 2008 (11.09.2008)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
60/993,622 13 September 2007 (13.09.2007) US

(71) Applicant (for all designated States except US): **ACCE-  
DIAN NETWORKS INC.** [CA/CA]; 1255 Trans-Canada  
Hwy, Suite 120, Dorval, Québec H9P 2V4 (CA).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **ROBITAILLE,**  
**Claude** [CA/CA]; 24 rue Maude, St-placide, Québec  
J0V 2B0 (CA). **ROCHON, Steve** [CA/CA]; 5680 rue  
Bertrand, Brossard, Québec J4Z 3E1 (CA).

(81) Designated States (unless otherwise indicated, for every  
kind of national protection available): AE, AG, AL, AM,  
AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA,  
CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE,  
EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID,  
IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK,  
LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW,  
MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT,  
RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ,  
TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM,  
ZW.

(84) Designated States (unless otherwise indicated, for every  
kind of regional protection available): ARIPO (BW, GH,  
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,  
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),  
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,  
FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL,  
NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG,  
CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— without international search report and to be republished  
upon receipt of that report

(54) Title: SYSTEM FOR TESTING ETHERNET PATHS AND LINKS WITHOUT IMPACTING NON-TEST TRAFFIC

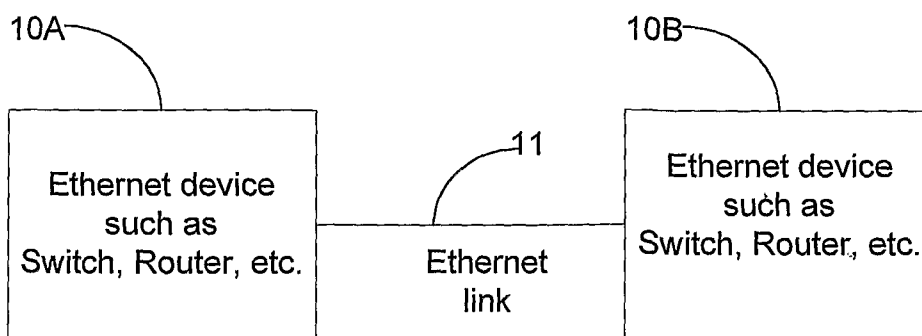


FIG. 1

(57) Abstract: A system for testing Ethernet paths or links without adversely impacting non-test traffic. The system includes a test traffic generator that includes a scheduler that determines when a new test packet is generated. The test traffic generator includes a packet creator that builds a test packet and a transmitter for transmitting the test packet via the Ethernet path or link. The packet creator sends the test packet to the transmitter. The traffic generator includes a transmit credit block coupled to the transmitter or to the scheduler. The transmit credit block stores an amount of credits representing a number of bytes that are available to transmit and decrements the amount each time a non-test packet is communicated via the Ethernet path or link.



WO 2009/034450 A2

**SYSTEM FOR TESTING ETHERNET PATHS AND LINKS**  
**WITHOUT IMPACTING NON-TEST TRAFFIC**

**CROSS-REFERENCE TO RELATED APPLICATION**

This application claims the benefit of U.S. Provisional Application No.  
5 60/993,622, filed September 13, 2007.

**FIELD OF THE INVENTION**

This invention relates generally to testing of Ethernet systems, and, more particularly, to systems for testing Ethernet paths and links without impacting non-test  
10 traffic.

**BACKGROUND OF THE INVENTION**

As depicted in FIG. 1, an Ethernet link 11 is a connection between two adjacent Ethernet devices 10A or 10B, while an Ethernet path 13, as depicted in FIG.  
15 2, is a connection between two Ethernet devices 10A or 10B via a network 12 composed of multiples links, which may or may not be using Ethernet. The Ethernet device 10A, 10B, may be a network element device, such as a router, a switch, etc. or could be a simpler device, such as a personal computer (PC) or other terminal device. In case of a network element device, the Ethernet traffic may originate from or be  
20 destined for the device itself or from other devices behind it. The Ethernet link 11 and the Ethernet path 13 can be called an Ethernet circuit. The testing of an Ethernet circuit includes transmitting test traffic from one location toward an Ethernet device 10A or 10B. The test location may be anywhere on the Ethernet circuit. Test traffic reception processing, which may include validation, counting, bandwidth  
25 determination, etc., is not necessary, albeit useful, for the purpose of this invention.

The test traffic uses specified attributes (e.g., Ethernet ethertype, data payload, priority parameters, IP or MAC addresses, packet size, etc.) and specified transmission characteristics (such as bandwidth, burst size, test duration, etc.). The goal of the testing is to verify the ability of the Ethernet link 11 or path 13 to  
30 successfully convey Ethernet frames from one device 10A or 10B to another Ethernet device 10B or 10A under the specified conditions (packet attributes and transmissions characteristics). The traffic attributes or transmission characteristics used for a given

test session may be set to any desired value as necessary but the operation of this invention does not depend on any setting. The Ethernet link 11 or path 13 is said to be in-use or in-service when traffic other than the test traffic is present on the said Ethernet link 11 or path 13 at the location where test traffic is transmitted.

5

### SUMMARY OF THE INVENTION

Aspects disclosed herein relate to the testing of an Ethernet link 11 or Ethernet path 13, while it is being used by non-test traffic. In a typical test scenario, not using this invention, the non-test traffic is interrupted so that the validity of the test is not impaired. As a result, such a test is only performed at the installation time of the Ethernet link 11 or path 13 or when the Ethernet circuit is not in use or during non-normal business hours (night, week-ends, etc.). It is, however, desirable to be able to perform a test while non-test traffic is present, because problems (to be detected by the test) usually occur at the time of normal operation of the Ethernet circuit. But the test should be conducted in such a way that the normal non-test traffic is not affected. This invention relates to the methods and techniques used to perform a test while not impairing or impacting the normal operation of the non-test traffic. Impact or impairment occurs when non-test traffic is either excessively delayed or prevented from being delivered from one Ethernet device 10A or 10B to the other Ethernet device 10B or 10A due to the presence of test traffic. It is possible to execute a test, with non-test traffic present, without the use of this invention however some of the non-test traffic may either be dropped or excessively delayed.

25

### BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be better understood from the following description of preferred embodiments together with reference to the accompanying drawings, in which:

FIG. 1 is a function block diagram of two Ethernet devices connected via an Ethernet link;

30

FIG. 2 is a functional block diagram of an Ethernet path between two Ethernet devices;

FIG. 3 is a functional block diagram of a test traffic generator in an Ethernet network comprising Ethernet links, an Ethernet path, and Ethernet devices;

FIG. 4 is a functional block diagram of a transmit generator and its functional blocks including a transmit block coupled to a transmit credit block; and

5        FIG. 5 is a functional block diagram of a transmit generator and its functional blocks like those shown in FIG. 4 except that the transmit credit block is responsive to a scheduler.

### DETAILED DESCRIPTION

10        Although the invention will be described in connection with certain preferred embodiments, it will be understood that the invention is not limited to those particular embodiments. On the contrary, the invention is intended to cover all alternatives, modifications, and equivalent arrangements as may be included within the spirit and scope of the invention as defined by the appended claims.

15        An embodiment of the invention includes the test traffic generator 14A, 14B, or 14C of FIGS. 3-5, which dynamically adapts its transmission characteristics in such a way as to not impair or impact the non-test traffic. Non-test traffic transmission characteristics (bandwidth, burst size, duration, etc.) may be bound by a bandwidth profile of some sort or may only be limited by the physical characteristics of the  
20    Ethernet medium (link speed, etc.).

      The dynamic test traffic generator 14A may be embedded in an Ethernet device 10A or 10B, or it may be located adjacent to the Ethernet link 11 as a generator 14B or it may be located in-line of the Ethernet link 11 as a generator 14C. In the case of generator 14C, there are at least two Ethernet ports that carry non-test traffic.  
25    Generator 14C must therefore forward non-test traffic between its ports. This function does not relate to the operation of this invention. In all cases, the information about the amount of non-test traffic flowing in the same direction as the test traffic is made available to the dynamic test traffic generator 14A, 14B, 14C. As an example, if the test traffic generator 14B sends test traffic toward the Ethernet  
30    device 10B, the information about the amount of non-test traffic flowing from Ethernet device 10A toward the Ethernet device 10B is made available to the generator 14B. The Ethernet path 13 and the network 12 may or may not be present.

If the network 12 and the Ethernet path 13 are not present, the two Ethernet links 11 are connected together and form a single Ethernet link (as in FIG. 1).

The transmit generator 20 of FIG. 4 includes multiple functional building blocks, such as a scheduler 21, a packet creator 22, a transmit block 23 and a transmit credit block 24. Other functional building blocks not related to this invention may be included but are not illustrated. The scheduler 21 determines when a new test packet needs to be generated. It instructs the packet creator 22, which builds a valid test packet and sends it to the transmit block 23. The function of the transmit block 23 depends on where the traffic generator 20 is located. In the case of the traffic generator 14A, embedded in Ethernet device 10A, the function of the transmit block 23 is to sending the generated traffic to other functions within Ethernet device 10A. The exact scope and operation of these other functions do not relate to this invention. In the case of the traffic generator 14C, the function of the transmit block 23 is to send the generated traffic to other functions within the device. These other functions do not relate to the operation of this invention. In the case of the traffic generator 14B, the transmit block 23 includes an Ethernet port and is responsible for sending the Ethernet test packet on the Ethernet link 25 attached to it. Two different preferred embodiments are illustrated in FIGS. 4 and 5.

In the case of FIG. 4, the transmit block 23 queries the transmit credit block 24 and transmits a test packet if sufficient credits are available. Credits represent the number of bytes to transmit. Headers, preamble and other Ethernet frame fields, such as the inter-frame gap, may be included in the credit count depending on the mode of operation of the bandwidth associated with the test. As an example, a layer 2 test includes at least Ethernet headers but not the inter-packet gap. If a transmission occurs, the appropriate amount of credit is debited from the transmit credit block 24. If there is not enough transmit credit available, the transmission is delayed until enough credit is available. The information about the amount of non-test traffic is provided to the transmit credit block 24 via line 26. The method by which this information is made available to block 24 is not relevant to the operation of this invention. Credits are removed from the transmit credit block 24 for each non-test packet. This effectively reduces the amount of credit available to allow transmission of test traffic. The credit total may become negative, due to excessive non-test traffic. This will further delay the transmission of test traffic because more credits will need

to be added before enough credit is available to start the transmission of a test packet. Transmission credits are added to the transmit credit block 24 using methods and techniques that are not pertinent to this invention.

In the case of FIG. 5, the scheduler 21 queries the transmit credit block 24 and  
5 only sends a request to the packet creator 22 if sufficient credits are available. Credits represent the number of bytes to transmit. Headers, preamble and other Ethernet frame fields, such as the inter-packet gap, may be included in the credit count depending on the mode of operation of the bandwidth associated with the test. As an example, a layer 2 test would include at least Ethernet headers but not the inter-frame  
10 gap. If a request occurs, the appropriate amount of credit is debited from the transmit credit block 24. If not enough transmit credit is available, the request is delayed until enough credit is available. The information about the amount of non-test traffic is provided to the transmit credit block 24 via the line 26. The method by which this information is made available to block 24 is not relevant to the operation of this  
15 invention. Credits are removed from the transmit credit block 24 for each non-test packet. This effectively reduces the amount of credit available to allow transmission of test traffic. The credit total may become negative, due to excessive non-test traffic. This will further delay the transmission of test traffic because more credits will need to be added before enough credit is available to start the transmission of a test packet.  
20 Transmission credits are added to the transmit credit block 24 using methods and techniques that are not pertinent to this invention.

While particular embodiments and applications of the present invention have been illustrated and described, it is to be understood that the invention is not limited to the precise construction and compositions disclosed herein and that various  
25 modifications, changes, and variations may be apparent from the foregoing descriptions without departing from the spirit and scope of the invention as defined in the appended claims.

**What is claimed is:**

1. A system for testing Ethernet paths or links without adversely impacting non-test traffic, comprising:

a test traffic generator that includes a scheduler that determines when a new  
5 test packet is generated, a packet creator that builds a test packet, a transmitter for  
transmitting the test packet via the Ethernet path or link, wherein the packet creator  
sends the test packet to the transmitter, and a transmit credit block coupled to the  
transmitter or to the scheduler, the transmit credit block storing an amount of credits  
representing a number of bytes that are available to transmit and decrementing the  
10 amount each time a non-test packet is communicated via the Ethernet path or link.

2. The system of claim 1, wherein the transmit credit block is coupled to  
the transmitter, the transmitter configured to query the transmit credit block to  
determine whether sufficient credits are available from the transmit credit block to  
transmit the test packet.

15 3. The system of claim 1, wherein the transmit credit block is coupled to  
the scheduler, the scheduler configured to query the transmit credit block and to send  
a request to the packet creator to build the test packet responsive to sufficient credits  
being available from the transmit credit block to transmit the test packet.

4. The system of claim 1, wherein the credits include at least one field  
20 from an Ethernet frame.

5. The system of claim 4, wherein the at least one field is a preamble field  
of the Ethernet frame.

6. The system of claim 4, wherein the at least one field is an inter-frame  
gap of the Ethernet frame.

25 7. The system of claim 1, further comprising an Ethernet device, the  
transmit generator being incorporated into the Ethernet device.

8. The system of claim 1, wherein the transmit generator includes an  
Ethernet port, and wherein the transmit generator is connected to the Ethernet link via  
the Ethernet port.

30 9. The system of claim 1, wherein the transmit generator includes at least  
two Ethernet ports for in-line connection of the transmit generator to the Ethernet link.

1/2

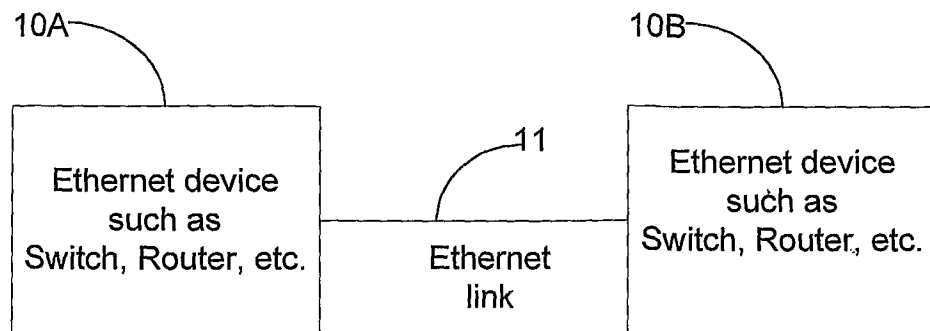


FIG. 1

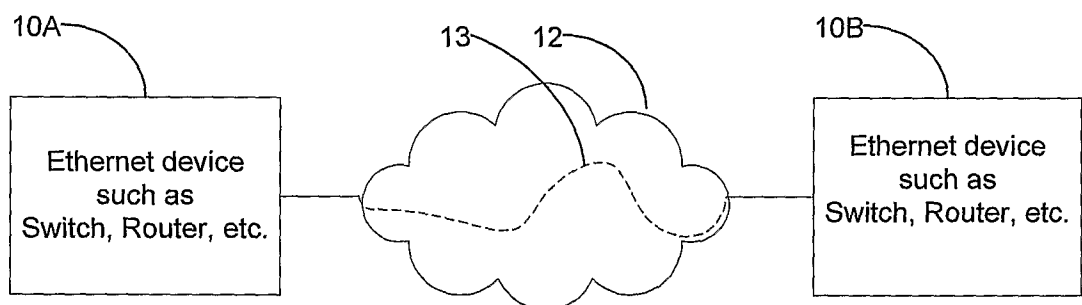


FIG. 2

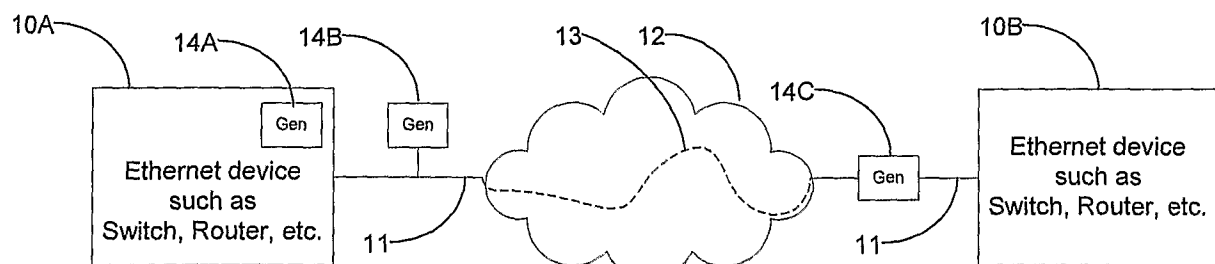


FIG. 3

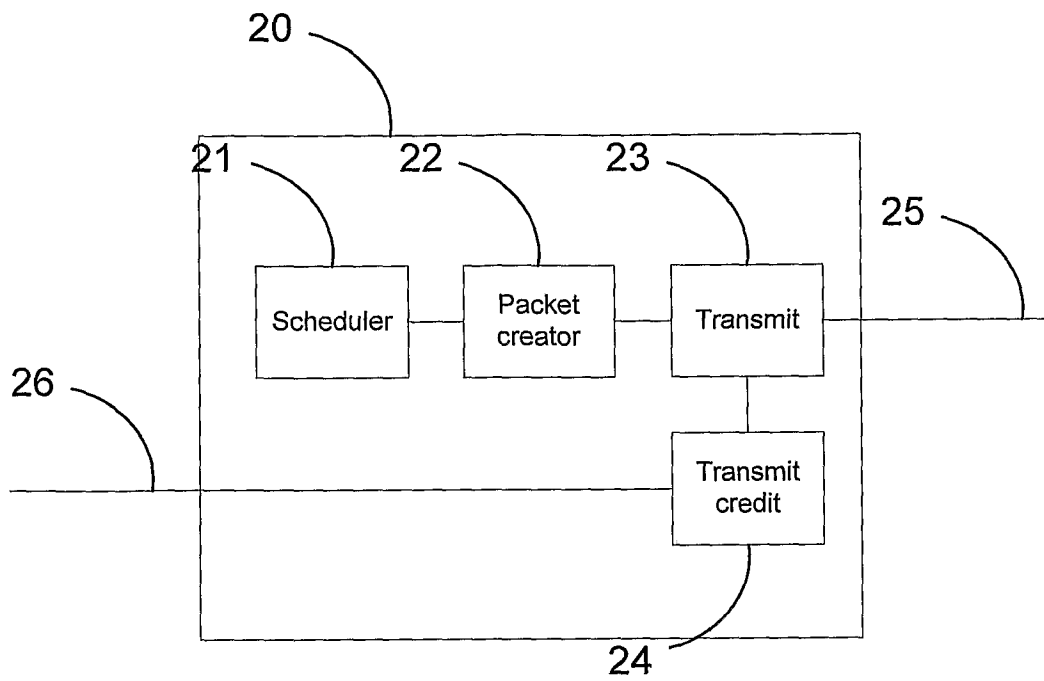


FIG. 4

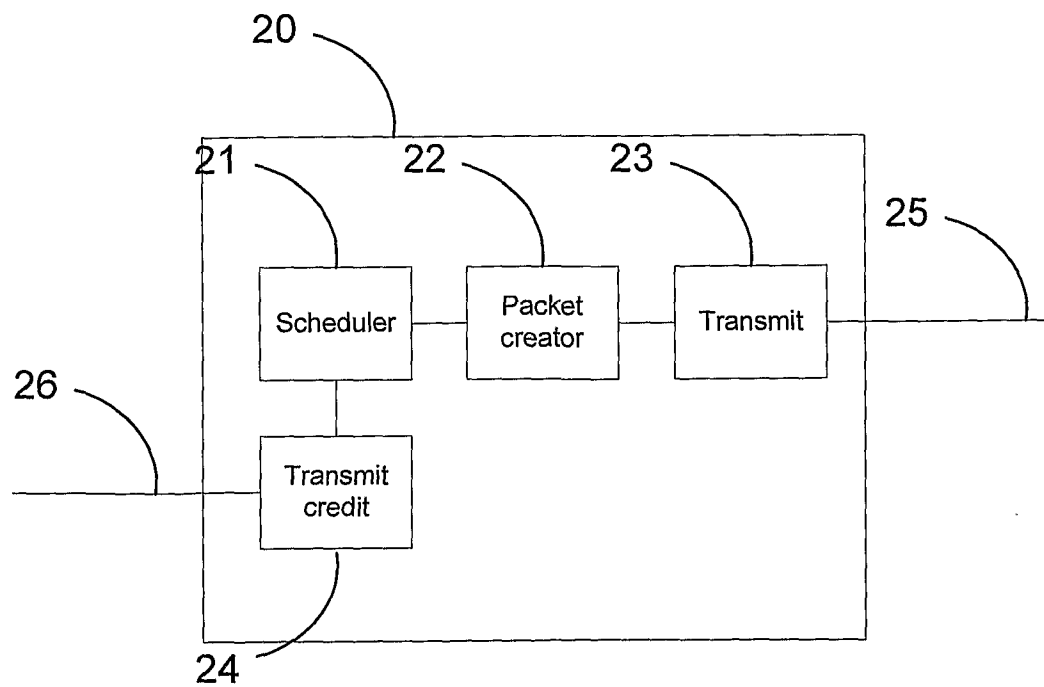


FIG. 5