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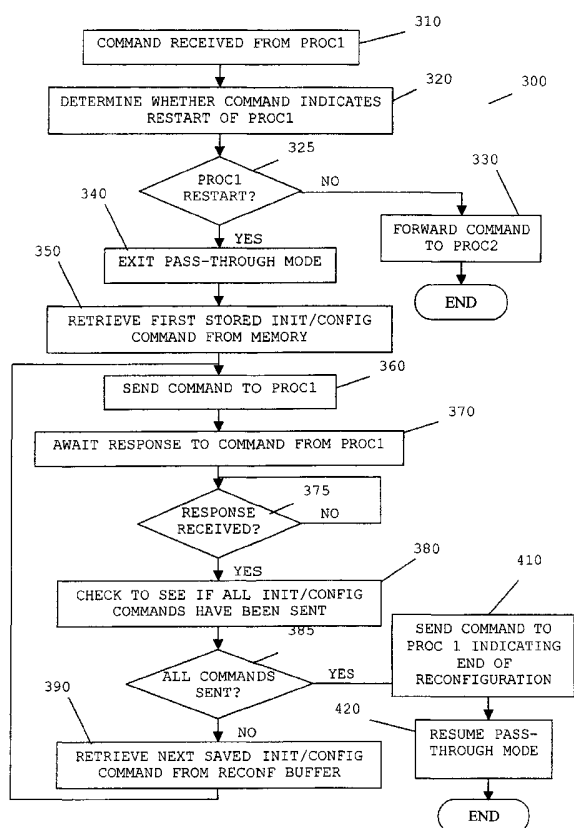
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(54) Title: PROCESSOR RE-START CONTROL



(57) Abstract: A method (300) of handling a re-start of a first processor (110) in a dual-processor system where the system includes a first processor (110) performing a first set of functions operably coupled to a second processor (120) performing a second set of functions. The method includes the steps of storing (240) one or more initialisation and/or configuration commands in a memory element, wherein the commands are sent by the second processor (120) to the first processor (110) to set an operation of said first processor. A message (310) from the first processor (110) indicates a re-start operation of the first processor, in response to which the one or more initialisation or configuration commands are retrieved (350) from the memory element (140). The first processor (110) is then re-started with preferably the most-recent initialisation or configuration setting so that the operational state of the first processor (110), following re-start, is as expected by the second processor (120).



European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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A	US 3 795 800 A (NIMMO J ET AL) 5 March 1974 (1974-03-05) abstract	1-15

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☒ Patent family members are listed in annex.

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Information on patent family members

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