Innovation, Sciences et Développement économique Canada
Office de la Propriété Intellectuelle du Canada

Innovation, Science and Economic Development Canada
Canadian Intellectual Property Office

CA 3105446 C 2023/04/11

(11)(21) 3 105 446

(12) BREVET CANADIEN
CANADIAN PATENT

(13) C

(86) Date de dépôt PCT/PCT Filing Date: 2019/07/02
(87) Date publication PCT/PCT Publication Date: 2020/01/09
(45) Date de délivrance/Issue Date: 2023/04/11
(85) Entrée phase nationale/National Entry: 2020/12/30
(86) N° demande PCT/PCT Application No.: RU 2019/050102
(87) N° publication PCT/PCT Publication No.: 2020/009619
(30) Priorités/Priorities: 2018/07/02 (US62/693,441); 2018/08/31 (US62/725,845); 2018/09/16 (US62/731,967); 2018/09/17 (US62/731,972); 2018/09/24 (US62/735,722); 2018/11/08 (US62/757,732); 2019/01/17 (US62/793,866)

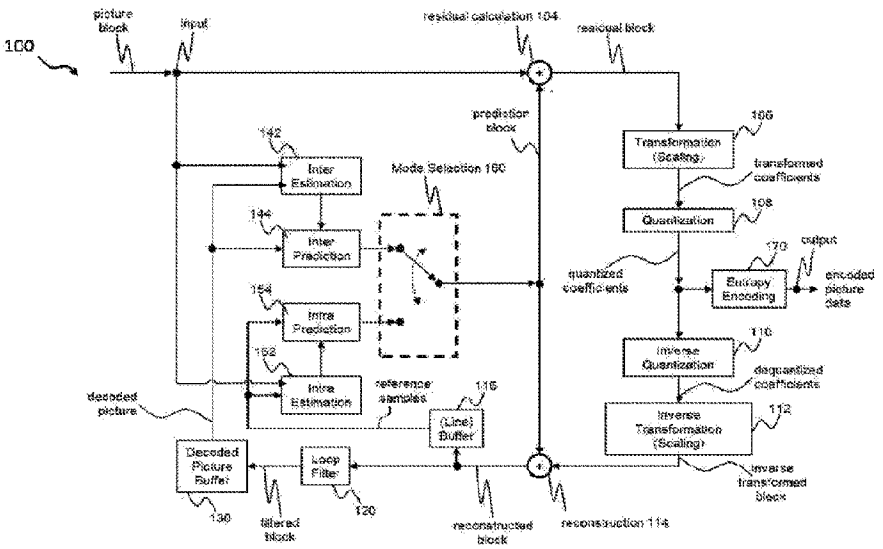
(51) Cl.Int./Int.Cl. *H04N 19/80* (2014.01), *H04N 19/129* (2014.01), *H04N 19/60* (2014.01)

(72) Inventeurs/Inventors:
STEPIN, VICTOR ALEXEEVICH, CN;
IKONIN, SERGEY YURIEVICH, CN;
CHERNYAK, ROMAN IGOREVICH, CN;
CHEN, JIANLE, US;
KURYSHEV, DMITRY, CN

(73) Propriétaire/Owner:
HUAWEI TECHNOLOGIES CO., LTD., CN

(74) Agent: SMART & BIGGAR LP

(54) Titre : APPAREIL ET PROCEDE DE FILTRAGE EN CODAGE VIDEO
(54) Title: APPARATUS AND METHOD FOR FILTERING IN VIDEO CODING



(57) Abrégé/Abstract:
The invention relates to a filter for video coding, wherein the filter is configured for processing a block for generation of a filtered block, and wherein the block comprises a plurality of pixels. The filter includes one or more processor configured to: scan a current pixel of the block and its neighboring pixels of the current pixel according to a predefined scan template; obtain spectrum components by performing transform for the current pixel and its neighboring pixels; obtain filtered spectrum components based on a filtering parameter and the spectrum components; obtain filtered pixels by performing inverse transform for the filtered spectrum components; and generate a filtered block based on the filtered pixels. The filter is provided allowing improving the efficiency for video coding.

ABSTRACT

The invention relates to a filter for video coding, wherein the filter is configured for processing a block for generation of a filtered block, and wherein the block comprises a plurality of pixels. The filter includes one or more processor configured to: scan a current pixel of the block and its neighboring pixels of the current pixel according to a predefined scan template; obtain spectrum components by performing transform for the current pixel and its neighboring pixels; obtain filtered spectrum components based on a filtering parameter and the spectrum components; obtain filtered pixels by performing inverse transform for the filtered spectrum components; and generate a filtered block based on the filtered pixels. The filter is provided allowing improving the efficiency for video coding.

Date Reçue/Date Received 2021-01-18

Apparatus and Method for Filtering in Video Coding

TECHNICAL FIELD

[0001] Generally, the present invention relates to the field of video coding. More specifically, the present invention relates to a filter for video coding, method for filtering reconstructed video frames, and method for filtering video blocks as well as an encoding apparatus and a decoding apparatus comprising such the filter for video coding.

BACKGROUND

[0002] Digital video has been widely used since the introduction of DVD-discs. Before transmission the video is encoded and transmitted using a transmission medium. The viewer receives the video and uses a viewing device to decode and display the video. Over the years the quality of video has improved, for example, because of higher resolutions, color depths and frame rates. This has lead into larger data streams that are nowadays commonly transported over internet and mobile communication networks.

[0003] Higher resolution videos, however, typically require more bandwidth as they have more information. In order to reduce bandwidth requirements video coding standards involving compression of the video have been introduced. When the video is encoded the bandwidth requirements (or corresponding memory requirements in case of storage) are reduced. Often this reduction comes at the cost of quality. Thus, the video coding standards try to find a balance between bandwidth requirements and quality.

[0004] As there is a continuous need for improving quality and reducing bandwidth requirements, solutions that maintain the quality with reduced bandwidth requirements or improve the quality while maintaining the bandwidth requirement are continuously searched. Furthermore, sometimes compromises may be acceptable. For example, it may be acceptable to increase the bandwidth requirements if the quality improvement is significant.

[0005] The High Efficiency Video Coding (HEVC) is an example of a video coding standard that is commonly known to persons skilled in the art. In HEVC, to split a coding unit (CU) into prediction units (PU) or transform units (TUs). The Versatile Video Coding (VVC) next generation standard is the most recent joint video project of the ITU-T Video Coding

Date Reçue/Date Received 2021-01-18

Experts Group (VCEG) and the ISO/IEC Moving Picture Experts Group (MPEG) standardization organizations, working together in a partnership known as the Joint Video Exploration Team (JVET). VVC is also referred to as ITU-T H.266/Next Generation Video Coding (NGVC) standard. In VVC, it removes the concepts of multiple partition types, i.e. it removes the separation of the CU, PU and TU concepts except as needed for CUs that have a size too large for the maximum transform length, and supports more flexibility for CU partition shapes.

[0006] Image filtering is frequently used to emphasize certain features of an image or to enhance the objective or perceptual quality of the filtered image. Image filtering has to deal with various sources of noise. Accordingly, various approaches for quality enhancement have been proposed and are currently in use. For example, in an adaptive Loop filter (ALF) method, each reconstructed frame is divided into a set of small blocks (super-pixels) and each block is filtered by the adaptive loop filter in that each pixel of the filtered reconstructed frame is a weighted sum of several pixels in the connected area of the pixel from the reconstructed frame around the position of the generating filtered pixel. Weighting coefficients (also referred to as filter coefficients) have property of central symmetry and are transmitted from the encoder to the decoder side. Edges often have a big size and therefore the number of transmitted weighting coefficients can become too large for an efficient processing. A large number of weighting coefficients requires a complex rate-distortion optimization (RDO) at the encoder side for decreasing the number of weighting coefficients for transmission. On the decoder side ALF requires implementation of universal multipliers and these multipliers should be reloaded for each 2x2 pixel block.

[0007] Thus, there is a need for an improved filter and method allowing to improve the prediction quality with low complexity and, thus, increase the video coding efficiency.

SUMMARY

[0008] It is an object of the invention to provide an improved filter and method allowing to improve the filtering efficiency with limited complexity and, thus, increase the video coding efficiency.

Date Reçue/Date Received 2021-01-18

[0009] The foregoing and other objects are achieved by the subject matter of the independent claims. Further implementation forms are apparent from the dependent claims, the description and the figures.

[0010] According to a first aspect the invention relates to a filter for video coding, wherein the filter is configured for processing a block for generation of a filtered block, wherein the block comprises a plurality of pixels. The filter includes a memory storage comprising instructions; and one or more processor in communication with the memory. The one or more processors executes the instructions to: load a current pixel and its neighboring pixels to a linear buffer according to a predefined scan template; obtain spectrum components by performing 1D transform for pixels in the linear buffer; obtain filtered spectrum components by multiplying each spectrum component with a gain coefficient, wherein the gain coefficient depends on a corresponding spectrum component and a filtering parameter; obtain filtered pixels by performing inverse 1D transform for filtered spectrum components; and generate the filtered block based on the filtered pixels.

[0011] As an example, the block (or frame) may be a predicted block, and the filtered block is a filtered predicted block. As another example, the block (or frame) may be a reconstructed block, and the filtered block is a filtered reconstructed block.

As an example, the gain coefficient is a function of the corresponding spectrum component and the filtering parameter. The filtering parameter may be derived from a codec quantization parameter (QP).

An another example, a first spectrum component is bypassed without filtering when the gain coefficient $G(i,\sigma)$ for the first spectrum component is equal to one. The first spectrum component corresponds to sum or average value of samples in the linear buffer, and the first spectrum component may correspond to DC.

As other example, wherein the one or more processors executes the instructions to: drop N bits from table values of the LUT, N is an integer. N may be dependent on QP value, or is a fixed value.

As an example, the predefined scan template is defined as set of spatial or raster offsets relative to a position of the current pixel inside the reconstructed block. Offsets point to neighbour pixels are inside the reconstructed block. At least one filtered pixel may be placed to its original position according to the predefined scan template. When all filtered pixels are

Date Reçue/Date Received 2021-01-18

added to an accumulation buffer according to the predefined scan template, and the accumulation buffer could be initialized by zero before the obtaining filtered spectrum components. When final filtered pixels are obtained as accumulated values in the accumulation buffer divided by number of pixels adding to a current position of the accumulation buffer; one or more processor is configured to generate the filtered reconstructed block based on the final filtered pixels.

Optionally, differences between all filtered and corresponding unfiltered pixels are added to an accumulation buffer according to the predefined scan template, and the accumulation buffer could initialized by unfiltered pixels multiplied by maximum number of pixel values to be added in the block. The final filtered pixels are obtained as accumulated values in the accumulation buffer divided by maximum number of pixel values to be added in the block.

[0012] According to a second aspect the invention relates to a corresponding filtering method for processing a block for generation of a filtered block, wherein the block comprises a plurality of pixels. Each pixel is associated with a pixel value. The filtering method comprises the steps of: loading a current pixel and its neighboring pixels to a linear buffer according to a predefined scan template; obtaining spectrum components by performing 1D transform for pixels in the linear buffer; obtaining filtered spectrum components by multiplying each spectrum component with a gain coefficient, wherein the gain coefficient depends on a corresponding spectrum component and a filtering parameter; obtaining filtered pixels by performing inverse 1D transform for filtered spectrum components; and generating the filtered block based on the filtered pixels.

[0013] As an example, the block (or frame) may be a predicted block, and the filtered block is a filtered predicted block. As another example, the block (or frame) may be a reconstructed block, and the filtered block is a filtered reconstructed block.

As an example, the gain coefficient is a function of the corresponding spectrum component and the filtering parameter. The filtering parameter may be derived from a codec quantization parameter (QP).

An another example, a first spectrum component is bypassed without filtering when the gain coefficient $G(i,\sigma)$ for the first spectrum component is equal to one. The first spectrum component may correspond to DC value.

Date Reçue/Date Received 2021-01-18

As other example, filtering of the spectrum components based on a look up table (LUT). LUT generation may be based on an auxiliary function for at least some quantization parameters (QPs).The auxiliary function may be a straight line equation comming over points (i,THR) and (a, 0), where $a > 0$ and a depends on filtering parameter σ or QP value. For example, for the last QP in a set of QPs, a equals to 11; or for the second last QP in the set of QPs, a equals to 9.

As other example, the method further includes: dropping N bits from table values of the LUT, N is an integer. N may be dependent on QP value, or is a fixed value. When N is selected less for lower QP in comparison to higher QP from the set of QPs, for example, for the first QP in the set of QPs, N is equal to 2; or for the rest QPs from the set of QPs, N is equal to 3. Alternatively, when N is selected higher for higher QP in comparison to lower QP from the set of QPs of QPs, for example, for the last QP or for the last two QPs in the set of QPs, N is equal to 4; or for the rest QPs from the set of QPs, N is equal to 3. Alternatively, when N is selected less for lower QP and higher for higher QP in comparison to the rest QP from the set of QPs, for example, for the first QP in the set of QPs, N is equal to 2; for the last QP or for the last two QPs in the set of QPs, N is equal to 4; or, for the rest QPs from the set of QPs, N is equal to 3.

As an example, the predefined scan template is defined as set of spatial or raster offsets relative to a position of the current pixel inside the reconstructed block. Offsets point to neighbour pixels are inside the reconstructed block. At least one filtered pixel may be placed to its original position according to the predefined scan template. When all filtered pixels are added to an accumulation buffer according to the predefined scan template, and the accumulation buffer could be initialized by zero before the obtaining filtered spectrum components. When final filtered pixels are obtained as accumulated values in the accumulation buffer divided by number of pixels adding to a current position of the accumulation buffer; one or more processor is configured to generate the filtered reconstructed block based on the final filtered pixels.

Optionally, differences between all filtered and corresponding unfiltered pixels are added to an accumulation buffer according to the predefined scan template, and the accumulation buffer could initialized by unfiltered pixels multiplied by maximum number of pixel values to be added in the block. The final filtered pixels are obtained as accumulated values in the accumulation buffer divided by maximum number of pixel values to be added in the block.

Date Reçue/Date Received 2021-01-18

[0014] According to a third aspect the invention relates to an encoding apparatus for encoding a current frame from an input video stream, wherein the encoding apparatus comprises a filter according to the first aspect of the invention.

[0015] According to a fourth aspect the invention relates to a decoding apparatus for decoding a current frame from a received bitstream, wherein the decoding apparatus comprises a filter according to the first aspect of the invention.

[0016] According to a fifth aspect the invention relates to a computer program comprising program code for performing the method according to the second aspect when executed on a computer.

[0016a] Another aspect of the present disclosure relates to a method for processing a block, wherein the block comprises a plurality of pixels, wherein the method comprises: scanning a current pixel of the block and its neighboring pixels of the current pixel according to a predefined scan template, wherein the block is a reconstructed block or a predicted block; obtaining spectrum components by performing transform for the current pixel and its neighboring pixels; determining whether a filter is applied to the spectrum components based on conditions; when the filter is applied to spectrum component, obtaining filtered spectrum components based on a filtering parameter and the spectrum components; when the filter is not applied to spectrum component, saving spectrum component without changing; obtaining filtered pixels by performing inverse transform for the filtered spectrum components; and generating a filtered block based on the filtered pixels; wherein the conditions comprises at least one of: the block has non-zero residual signal; a size of the block; an aspect ratio of the block; a prediction mode of the block; a prediction mode of the block in intra; or a prediction mode of the block in inter.

[0016b] Another aspect of the present disclosure relates to a decoder comprising processing circuitry for carrying out a method as disclosed herein.

[0016c] Another aspect of the present disclosure relates to an encoder comprising processing circuitry for carrying out a method as disclosed herein.

[0016d] Another aspect of the present disclosure relates to a decoder comprising a filter for carrying out a method as disclosed herein.

Date Reçue/Date Received 2022-04-22

[0016e] Another aspect of the present disclosure relates to an encoder comprising a filter for carrying out a method as disclosed herein.

[0016f] Another aspect of the present disclosure relates to a decoder, comprising: a memory storage comprising instructions; and one or more processors in communication with the memory, wherein the one or more processors execute the instructions to carry out a method as disclosed herein.

[0016g] Another aspect of the present disclosure relates to an encoder, comprising: a memory storage comprising instructions; and one or more processors in communication with the memory, wherein the one or more processors execute the instructions to carry out a method as disclosed herein.

[0016h] Another aspect of the present disclosure relates to a non-transitory computer-readable medium storing computer-executable instructions that, when executed by a processor of a computing device, cause the computing device to perform the method as disclosed herein.

[0016i] Another aspect of the present disclosure relates to an apparatus for processing a block, wherein the block comprises a plurality of pixels, and wherein the apparatus comprises: means for scanning a current pixel of the block and its neighboring pixels of the current pixel according to a predefined scan template, wherein the block is a reconstructed block or a predicted block; means for obtaining spectrum components by performing transform for the current pixel and its neighboring pixels; means for determining whether a filter is applied to the spectrum components based on conditions; means for obtaining filtered spectrum components based on a filtering parameter and the spectrum components when the filter is applied to spectrum component; means for saving spectrum component without changing when the filter is not applied to spectrum component; means for obtaining filtered pixels by performing inverse transform for the filtered spectrum components; and means for generating a filtered block based on the filtered pixels; wherein the conditions comprises at least one of: the block has non-zero residual signal; a size of the block; an aspect ratio of the block; a prediction mode of the block; a prediction mode of the block in intra; or a prediction mode of the block in inter.

Date Reçue/Date Received 2022-04-22

[0017] Thus, the filter is provided allowing improving the efficiency for video coding. More specifically, the improved filter according to embodiments of the invention estimates filter parameters from the frame itself without filter parameters signaling and, therefore, requires significantly less signaling than conventional filters, which signal weight coefficients for filtering in the image domain.

Date Reçue/Date Received 2022-04-22

BRIEF DESCRIPTION OF THE DRAWINGS

[0018] Further embodiments of the invention will be described with respect to the following figures, wherein:

[0019] Fig. 1A shows a schematic diagram illustrating an encoding apparatus according to an embodiment comprising a filter according to an embodiment;

[0020] Fig. 1B shows a schematic diagram illustrating an encoding apparatus according to an embodiment comprising a filter according to an embodiment;

[0021] Fig. 2A shows a schematic diagram illustrating a decoding apparatus according to an embodiment comprising a filter according to an embodiment;

[0022] Fig. 2B shows a schematic diagram illustrating a decoding apparatus according to an embodiment comprising a filter according to an embodiment;

[0023] Fig. 3A shows a schematic diagram illustrating aspects of a filtering process implemented in a filter according to an embodiment;

[0024] Fig. 3B shows a schematic diagram illustrating aspects of a filtering process implemented in a filter according to an embodiment;

[0025] Fig. 3C shows a schematic diagram illustrating aspects of a filtering process implemented in a filter according to another embodiment;

[0026] FIG. 4A illustrates templates for different pixel position inside square reconstructed block;

[0027] Fig. 4B illustrated equivalent filter shape for one pixel;

[0028] Fig. 4C gives an example of padding;

[0029] Fig. 4D gives another example of padding;

[0030] Fig. 5A shows a flow diagram illustrating steps of a filtering method according to an embodiment;

[0031] Fig. 5B shows a flow diagram illustrating steps of a filtering method according to an embodiment;

[0032] FIG. 6 is an example hardware filter design based on SRAM;

Date Reçue/Date Received 2021-01-18

[0033] FIG. 7 is exemplary hardware design of 2x2 group filtering based on flip-flops;

[0034] FIG. 8 shows an example of combining results of four 2x2 groups filtering with reusing of results of same spatial group final filter;

[0035] FIG. 9 as an example, shows the result of optimizing the LUT;

[0036] FIG. 10 as an example, shows undesirable gap in filter transfer function;

[0037] FIG. 11 as an example, represents same table with table entries being plot one-by-one;

[0038] FIG. 12 as an example, illustrates the method how gap can be eliminated using auxiliary function;

[0039] FIG. 13 illustrates the example of eliminating gap by taken maximum from two values while generating the LUT;

[0040] FIG. 14, as an example, illustrates filter transfer function after applying described above method;

[0041] FIG. 15 illustrates example of filter transfer functions depending on five QPs in the set;

[0042] FIG. 16 illustrates example of filter transfer functions for five QPs in the set based on corresponding tables;

[0043] FIG. 17 illustrates example of filter transfer functions for five QPs in the set based on corresponding tables; and

[0044] FIG. 18 is a schematic diagram illustrating an exemplary structure of an apparatus according to an embodiment.

[0045] In the various figures, identical reference signs will be used for identical or functionally equivalent features.

DETAILED DESCRIPTION OF EMBODIMENTS

[0046] In the following description, reference is made to the accompanying drawings, which form part of the disclosure, and in which are shown, by way of illustration, specific

Date Reçue/Date Received 2021-01-18

aspects in which the present invention may be placed. It is understood that other aspects may be utilized and structural or logical changes may be made without departing from the scope of the present invention. The following detailed description, therefore, is not to be taken in a limiting sense, as the scope of the present invention is defined by the appended claims.

[0047] For instance, it is understood that a disclosure in connection with a described method may also hold true for a corresponding device or system configured to perform the method and vice versa. For example, if a specific method step is described, a corresponding device may include a unit to perform the described method step, even if such unit is not explicitly described or illustrated in the figures. Further, it is understood that the features of the various exemplary aspects described herein may be combined with each other, unless specifically noted otherwise.

[0048] Figure 1A shows an encoding apparatus 100 according to an embodiment comprising a filter 120 according to an embodiment. The encoding apparatus 100 is configured to encode a block of a frame of a video signal comprising a plurality of frames (also referred to as pictures or images herein), wherein each frame is dividable into a plurality of blocks and each block comprises a plurality of pixels. In an embodiment, the blocks could be macro blocks, coding tree units, coding units, prediction units and/or prediction blocks.

[0049] The term "block" in this disclosure is used for any type block or for any depth block, for example, the term "block" is included but not limited to root block, block, sub-block, leaf node, and etc. The blocks to be coded do not necessarily have the same size. One picture may include blocks of different sizes and the block rasters of different pictures of video sequence may also differ.

[0050] In the exemplary embodiment shown in figure 1A, the encoding apparatus 100 is implemented in the form of a hybrid video coding encoder. Usually, the first frame of a video signal is an intra frame, which is encoded using only intra prediction. To this end, the embodiment of the encoding apparatus 100 shown in figure 1A comprises an intra prediction unit 154 for intra prediction. An intra frame can be decoded without information from other frames. The intra prediction unit 154 can perform the intra prediction of a block on the basis of information provided by the intra estimation unit 152.

Date Reçue/Date Received 2021-01-18

[0051] The blocks of subsequent frames following the first intra frame can be coded using inter or intra prediction, as selected by a mode selection unit 160. To this end, the encoding apparatus 100 shown in figure 1A further comprises an inter prediction unit 144. Generally, the inter prediction unit 144 can be configured to perform motion compensation of a block based on motion estimation provided by the inter estimation unit 142.

[0052] Furthermore, the prediction signal in the hybrid encoder embodiment shown in figure 1B obtain from intra or inter prediction is further filtered by filter 145.

[0053] Furthermore, in the hybrid encoder embodiment shown in figures 1A and 1B a residual calculation unit 104 determines the difference between the original block and its prediction, i.e. the residual block defining the prediction error of the intra/inter picture prediction. This residual block is transformed by the transformation unit 106 (for instance using a DCT) and the transformation coefficients are quantized by the quantization unit 108. The output of the quantization unit 108 as well as the coding or side information provided, for instance, by the intra prediction unit 154, the inter prediction unit 144 and the filter 120 are further encoded by an entropy encoding unit 170.

[0054] A hybrid video encoder usually duplicates the decoder processing such that both will generate the same predictions. Thus, in the embodiment shown in figure 1 the inverse quantization unit 110 and the inverse transformation unit perform the inverse operations of the transformation unit 106 and the quantization unit 108 and duplicate the decoded approximation of the residual block. The decoded residual block data is then added to the results of the prediction, i.e. the prediction block, by the reconstruction unit 114. Then, the output of the reconstruction unit 114 can be provided to a line buffer 116 to be used for intra prediction and is further processed by the filter 120, which will be described in more detail below. The final picture is stored in the decoded picture buffer 130 and can be used for the inter prediction of subsequent frames.

[0055] Figure 2A shows a decoding apparatus 200 according to an embodiment comprising a filter 220 according to an embodiment. The decoding apparatus 200 is configured to decode a block of a frame of an encoded video signal. In the embodiment shown in figure 2A the decoding apparatus 200 is implemented as a hybrid decoder. An entropy decoding unit 204 performs entropy decoding of the encoded picture data, which generally can comprise

Date Reçue/Date Received 2021-01-18

prediction errors (i.e. residual blocks), motion data and other side information, which are needed, in particular, for an intra prediction unit 254 and an inter prediction unit 244 as well as other components of the decoding apparatus 200, such as the filter 220. Generally, the intra prediction unit 254 and the inter prediction unit 244 of the decoding apparatus 200 shown in figure 2A are selected by a mode selection unit 260 and function in the same way as the intra prediction unit 154 and the inter prediction unit 144 of the encoding apparatus 100 shown in figure 1, so that identical predictions can be generated by the encoding apparatus 100 and the decoding apparatus 200. A reconstruction unit 214 of the decoding apparatus 200 is configured to reconstruct the block on the basis of the filtered predicted block and the residual block provided by the inverse quantization unit 210 and the inverse transformation unit 212. As in the case of the encoding apparatus 100, the reconstructed block can be provided to a line buffer 216 used for intra prediction and the filtered block/frame can be provided to a decoded picture buffer 230 by the filter 220 for inter prediction.

[0056] As shown in Fig. 2B, the prediction signal in the hybrid decoder embodiment shown in Fig. 2B obtain from intra or inter prediction is further filtered by filter 264.

[0057] As already described above, the filter 120, 220 may be used at a frame level, for example, the filter 120, 220 may be configured to process a reconstructed frame from a decoded reconstructed video stream for generating a filtered reconstructed frame, where the reconstructed frame includes a plurality of blocks. The filter 120, 220 may be also used at a block level immediately after block reconstruction without waiting for a whole frame, for example, the filter 120, 220 may be configured to process a reconstructed block for generating a filtered reconstructed block, where the reconstructed block includes a plurality of pixels.

[0058] The filter 120, 220, 145 or 264 comprises one or more processor (or one or more processing unit). As will be explained in more detail below, the one or more processor (or one or more processing unit) is configured to: load a current pixel and its neighboring pixels to a linear buffer according to a predefined scan template (in other words, scan order, or scan pattern); obtain spectrum components by performing 1D transform for each pixel in the linear buffer; obtain filtered spectrum components by multiplying each spectrum component with a gain coefficient, wherein the gain coefficient depends on a corresponding spectrum component and a filtering parameter; obtain filtered pixels by performing inverse 1D transform for filtered spectrum; and generate a filtered reconstructed block based on the filtered pixels estimated

Date Reçue/Date Received 2021-01-18

on previous processing steps. In an example, the gain coefficient depends on a corresponding spectrum component and a filtering parameter. In another example, the gain coefficient depends on one or more filtering parameters and one or more corresponding spectrum components. In other example, the respective gain coefficient may depend on one or more filtering parameters and the corresponding spectrum component as well as neighboring spectral components to the left and to the right of the spectrum component.

[0059] The disclosure describes in-loop filter for lossy video codec which performs local and/or non-local filtering of reconstructed block from reconstructed frame. According to an example, the reconstructed frame is divided into set of small non-overlapped rectangular blocks (CU blocks). On the next step each reconstructed block (reconstructed CU block) is filtered in frequency domain independently from other reconstructed blocks. The filter can also be applied after transform and reconstruction, and the filtered result is used both for output as well as for spatial and temporal prediction.

[0060] As another example, the disclosure describes prediction filter for lossy video codec which performs local and/or non-local filtering of prediction block of reconstructed frame.

[0061] At the first step of processing all pixels inside reconstructed block can be processed independently from each other. For processing of pixel r(0), neighboring pixels are used. For example, as illustrated on Figure 3A, pixels r(1) to r(7) are used, and pixels r(0) to r(7) form one processing group.

[0062] Fig. 3A or 3B shows a schematic diagram 300, 300' illustrating aspects of a filtering process implemented in a filter according to an embodiment. At step 302, 302', a current pixel and its neighboring pixels from a block are loaded to a linear buffer according to a predefined scan template. As an example, the block may be a predicted block. As another example, the block may be a reconstructed block.

[0063] At step 304, 304', 1D transform is performed for pixel r(0) and its neighboring pixels r(1) to r(7) in the linear buffer to obtain spectrum components R:

$$\boldsymbol{R = 1D_Transform(r)}$$

[0064] As an example, the 1D transform may be a Hadamard transform.

[0065] It should be understood that whether to perform 1D transform on 4 pixels in the row (e.g. pixels A,B,C,D as in example on Fig. 3B) or perform 2D transform to spatially located

Date Reçue/Date Received 2021-01-18

pixels A,B,C,D relates to implementation specific. Applying 2D transform on 4 pixels A,B,C,D located in 2x2 block can lead to same result as applying 1D transform to 4 pixels A,B,C,D being taken as a row. At step 306, 306', filtering is performed in frequency domain based on multiplication of each spectrum component $\boldsymbol{R(i)}$ by a corresponding gain coefficient $\boldsymbol{G(i,\sigma)}$ to obtain a filtered spectrum components $\boldsymbol{F(i)}$:

[0066] The set of gain coefficients for all spectrum components is frequency impulse

$$\boldsymbol{F(i) = R(i) * G(i,\sigma)} \qquad \textbf{(1)}$$

response of the filter.

[0067] In an example, the gain coefficient depends on a corresponding spectrum component and a filtering parameter. In another example, the gain coefficient depends on one or more filtering parameters and one or more the corresponding spectrum components. In other example, the respective gain coefficient may depend on the one or more filtering parameters and the corresponding spectrum component as well as neighboring spectral components to the left and to the right of the spectrum component. If each gain coefficient is a function of spectrum component of the reconstructed block and the filtering parameter, or is a function spectrum component of the predicted block and the filtering parameter, gain coefficient $\boldsymbol{G(i,\sigma)}$ can be described by the following formula as an example:

$$G(i,\sigma) = \frac{R(i)^2}{R(i)^2 + m*\sigma^2} \qquad (2)$$

where (i) is an index of a spectrum component, R(i) is the spectrum component corresponding to (i) index, $G(i,\sigma)$ is the gain coefficient corresponding to R(i) ,σ is the filtering parameter, m is normalization constant equal to number of spectrum components. For example, m is 1, 2, 3, 4 ....Different spectrum components may have a same gain coefficient, or may have different gain coefficients.

[0068] For those transforms that have spectrum components corresponding to average (FFT, DCT, DST etc.) or sum (Hadamard) of input samples of the transform block (usually first component corresponding to DC value) it may be advantageous to have filtering coefficient equal to 1 to avoid changing of average luminance of the filtered block. That means bypassing (no filtering) for first spectrum component corresponding to DC value.

[0069] σ as the filtering parameter, may be deriving from codec quantization parameter (QP) on the encoder and decoder sides, for example, using the following formula:

Date Reçue/Date Received 2021-01-18

$$\sigma = k * 2^{(n*(QP-s))},$$

wherein k, n and s are constants having values as example: $k = 2.64, n = 0.1296, s = 11$. Different spectrum components may have a same filtering parameter, or may have different filtering parameters.

[0070] Parameters k, n and s can be selected in a such way to make sigma dependent on quantization step size that doubles each time the QP value increases by 6 in latest video coding standards. In example with parameters $k = 0.5, n = \frac{1}{6}\ and\ s = 0$ the σ parameters is derived as follows:

$$\sigma = 0.5 * 2^{\left(\frac{QP}{6}\right)}$$

[0071] Quantization scaling matrices are widely used to improve video compression quality. In this method quantization step size derived based on QP is multiplied by scaling factor transmitted in bitstream. For such method σ parameters derivation may be based on actual scaled quantization step size used for certain QP:

$$\sigma = k * \mathbf{Quantization_step_size(QP - s)}$$

[0072] Constants k, n and s may have a fixed values for σ calculation, or may have different values depending on QP, block size and shape, type of prediction (inter/intra) for current block. For example for intra predicted square blocks with size 32x32 or more parameter s may be calculated as $s = 11 + 8 = 19$. As equivalent filter parameter σ has smaller value that leads to softer filter which is more suitable for big square intra-predicted blocks usually corresponding to flat regions. As another example, k may be modified based on bit depth of pixels $k_{mod} = k$*(1 << (bit_depth - 8))

[0073] According to the method 300, 300', gain coefficient for each frequency is derived from spectrum component of the reconstructed pixels or predicted pixels. Therefore, the method 300, 300' do not need transmission of filtering parameters and can be applied for any reconstructed block or predicted block without additional signaling.

[0074] The LUT details are discussed.

[0075] It can be noted that filtering implies multiplication of spectrum component R(i) on scaling coefficient which is always less than 1. It can also be observed that at high values

Date Reçue/Date Received 2021-01-18

of R(i) scaling coefficient is close to 1. Based on these observation spectrum filtering is implemented using lookup table that allows to exclude multiplications and division from filtering operations:

[0076] Spectrum gain coefficient is less 1, so filtering can be implemented based on short look up table (LUT) reading according to the following formulas:

$$F(i,\sigma) = \begin{cases} R(i) \ , \ Abs\big(R(i)\big) \geq THR \\ LUT(R(i),\sigma), \ R(i) > 0 \\ -LUT(-R(i),\sigma), \ R(i) \leq 0 \end{cases}$$

Where $LUT(R(i),\sigma) = \frac{R(i)^3}{R(i)^2 + \mathrm{m} * \sigma^2}$. (i) is an index of a spectrum component, R(i) is the spectrum component corresponding to (i) index, σ is the filtering parameter, and THR is a threshold, *m* is normalization constant equal to number of spectrum components. For example, m is 1, 2, 3, 4 ....

[0077] As an example, THR may be calculated from following formula,

$$\frac{THR^2}{THR^2 + m * \sigma^2} = \mathrm{C}$$

where C is a value close to 1, for example, 0.8 or 0.9. To reduce LUT size threshold THR may be dependent from QP value.

For further reducing LUT size second threshold may be introduced to replace small filtered values by zero. In that case the filtered spectrum component is $F(i,\sigma)$ is further derived as:

$$F(i,\sigma) = \begin{cases} R(i) \ , \ Abs\big(R(i)\big) > THR \\ 0, \quad Abs\big(R(i)\big) < THR2 \\ LUT(R(i),\sigma), \ R(i) > 0 \\ -LUT(-R(i),\sigma), \ R(i) \leq 0 \end{cases}$$

Wherein THR2 defines the threshold below that filtered spectrum component is considered to be zero. The second THR2 can also be defined depending on QP value.

[0078] After filtering in frequency domain, inverse 1D transform is performed for filtered spectrum component at step 308 to obtain filtered pixels $\boldsymbol{f}$:

Date Reçue/Date Received 2021-01-18

$$f = 1D_Inverse_Transform(F)$$

[0079] At step 310, 310', the result of inverse 1D transform is placed to linear buffer of filtered reconstructed pixels or filtered pixels.

[0080] At step 312, 312' (not shown in Fig. 3A or 3B), a filtered block is generated based on the filtered pixels estimated on previous processing steps. As an example, the filtered block may be a filtered predicted block. As another example, the filtered block may be a filtered reconstructed block.

[0081] As shown in Fig. 3A as an embodiment, after filtering step 306, the filtered pixel f(0) is placed to its original position according to the predefined scan template. Other filtered samples f(1) – f(7) are not used. At another embodiment, more than one filtered pixels, for example, all filtered pixels from linear buffer of filtered samples are added to an accumulation buffer according to the predefined scan template used at step 302 of FIG. 3A. The accumulation buffer should be initialized by zero before the filtering step. At the last normalization step, final filtered pixels are obtained as accumulated values in the accumulation buffer divided by number of pixels added to a current position of the accumulation buffer, in other words, number of pixels values added to current position of accumulation buffer on previous processing steps. Then the filtered reconstructed block or the predicted block is generated based on the final filtered pixels.

[0082] As another embodiment, a filter has same implementation both for intra and inter coding unit (CU) filtering.

[0083] Hadamard transform domain filter is always applied to luma reconstructed blocks with non-zero transform coefficients, excluding 4x4 blocks and if slice quantization parameter is larger than 17. The filter parameters are explicitly derived from the coded information. Proposed filter, if applied, is performed on decoded samples right after block reconstruction. The filtered result is used both for output as well as for spatial and temporal prediction.

[0084] The filtering process is discussed, as schematically presented on Figure 3C.

[0085] For each pixel from reconstructed block pixel processing comprises the following steps:

Date Reçue/Date Received 2021-01-18

[0086] Scan for 4 neighboring pixels around processing pixel including current one according to scan pattern.

[0087] 4 point Hadamard transform of read pixels.

[0088] Spectrum filtering based on the formula (1) and (2).

[0089] The first spectrum component corresponding to DC value is bypassed without filtering.

[0090] Inverse 4 point Hadamard transform of filtered spectrum.

[0091] After filtering step the filtered pixels are placed to its original positions into accumulation buffer.

[0092] After completing filtering of pixels the accumulated values are normalized by number of processing groups used for each pixel filtering. Due to use of padding of one sample around the block number of processing groups is equal to 4 for each pixel in the block and normalization is performed by right shifting on 2 bits.

[0093] It can be seen that all pixels in the block can be processed independently in case of maximum parallelism is required.

[0094] In this embodiments, the threshold THR is set to a predefined value, for example, 128, that in case of straightforward implementation requires to store 128 (1 << 7) entries of 7-bit values per each QP.

[0095] The size of LUT influences on amount of on-chip memory requirements and the cost of a hardware implementation of the filter. To reduce amount of on-chip storage the LUT if calculated only for limited set of QPs starting from QP 20 with constant interval of 8. Totally five pre-defined LUTs (for five QPs group) are stored. For filtering of current block CU's QP is rounded to closest one from the table.

[0096] For further reduction of LUT size the N lowest bits are dropped (or ignored) during LUT generation. That allows having sparse table representation.

[0097] For exemplary implementation A, N is equal to 2, that leads to 7 - 2 = 5 bits of table depth (32 entries of 7-bit values);

Date Reçue/Date Received 2021-01-18

[0098] For exemplary implementation B, N is equal to 3, that leads to 7 - 3 = 4 bits of table depth (16 entries of 7-bit values).

[0099] Thus total memory size required for entire LUT storage:

[00100] For exemplary implementation A: 5x32x7 bits = 1120 bits = 140 bytes;

[00101] For exemplary implementation B: 5x16x7 bits = 560 bits = 70 bytes;

[00102] Exemplary implementation B is targeting to 16 bytes of LUT size to enable parallel access in software implementation due to ability to store entire LUT in one 16 bytes SSE register therefore this configuration is suggested.

[00103] If Hadamard transform is used, and a filtered pixel is placed to its original position according to the predefined scan template, then the following pseudo-code describes filtering process of method 300:

```
// reconstructed/predicted pixels scan
const int x0 = pIn[p0];
const int x1 = pIn[p1];
const int x2 = pIn[p2];
const int x3 = pIn[p3];    // p0-p3 define scan pattern

// 1D forward Hadamard transform
const int y0 = x0 + x2;
const int y1 = x1 + x3;
const int y2 = x0 - x2;
const int y3 = x1 - x3;
const int t0 = y0 + y1;
const int t1 = y0 - y1;
const int t2 = y2 + y3;
const int t3 = y2 - y3;
// frequency domain filtering
const int z0 = pTbl[t0];
const int z1 = pTbl[t1];
```


Date Reçue/Date Received 2021-01-18

```
const int z2 = pTbl[t2];
const int z3 = pTbl[t3];

// backward Hadamard transform
const int iy0 = z0 + z2;
const int iy1 = z1 + z3;
const int iy2 = z0 - z2;
const int iy3 = z1 - z3;

    // output filtered pixel
pOut[p0_out] = iy0 + iy1;
```

[00104] If Hadamard transform is used, and more than one filtered pixels from linear buffer of filtered samples are added to accumulation buffer, then the following pseudo-code describes filtering process of this scenario:

```
// reconstructed/predicted pixels scan
const int x0 = pIn[p0];
const int x1 = pIn[p1];
const int x2 = pIn[p2];
const int x3 = pIn[p3];    // p0-p3 define scan pattern

// 1D forward Hadamard transform
const int y0 = x0 + x2;
const int y1 = x1 + x3;
const int y2 = x0 - x2;
const int y3 = x1 - x3;
const int t0 = y0 + y1;
const int t1 = y0 - y1;
const int t2 = y2 + y3;
const int t3 = y2 - y3;
// frequency domain filtering
const int z0 = pTbl[t0];
```


Date Reçue/Date Received 2021-01-18

```
const int z1 = pTbl[t1];
const int z2 = pTbl[t2];
const int z3 = pTbl[t3];

// backward Hadamard transform
const int iy0 = z0 + z2;
const int iy1 = z1 + z3;
const int iy2 = z0 - z2;
const int iy3 = z1 - z3;
```

[00105] As alternative embodiment the accumulation buffer should be initialized by unfiltered pixel values multiplied by maximum number of pixel values to be added in the block. The maximum number of pixel values to be added in the block is defined based on scan template. Indeed scan template defines a number of pixel values added for each position. Based on that, the maximum number from all positions in the block can be selected and used during accumulation buffer initialization. Then, during each accumulation step, the unfiltered pixel value is subtracted from corresponding filtered value and added to accumulation buffer:

```
// filtered pixels accumulation
pOut[p0] += iy0 + iy1    // p0-p3 define scan pattern
pOut[p1] += iy0 - iy1
pOut[p2] += iy2 + iy3
pOut[p3] += iy2 - iy3
```

For reducing bit depth of accumulated pixel values before placing into accumulation buffer result of backward transform may be normalized on size of transform (*m*):

```
pOut[p0] += ((iy0 + iy1) >> HTDF_BIT_RND4);
pOut[p1] += ((iy0 - iy1) >> HTDF_BIT_RND4);
pOut[p2] += ((iy2 + iy3) >> HTDF_BIT_RND4);
pOut[p3] += ((iy2 - iy3) >> HTDF_BIT_RND4);
```

where HTDF_BIT_RND4 is equal to 2 for transform size of 4.

[00106] This embodiment allows the system to avoid storing the number of pixels added to current position and allows for replacement division and multiplication by shift operation at

Date Reçue/Date Received 2021-01-18

the last normalization step and accumulation buffer initialization step correspondingly if the maximum number of pixel values added is a power of e.g., 2, 4, 8 etc.

To keep precision of normalization stage it can be performed in a following way:

```
// normalization
pFiltered[p0] = CLIP3(0, (1 << BIT_DEPTH) - 1, (pOut[p0] + HTDF_CNT_SCALE_RND) >> HTDF_CNT_SCALE);
```

where HTDF_CNT_SCALE is Log2 of amount of pixels placed into accumulating buffer, e.g. for amount of 4 HTDF_CNT_SCALE is equal to 2, and HTDF_CNT_SCALE_RND is equal to (1 << (HTDF_CNT_SCALE - 1)). CLIP3 is a clipping function which ensures filtered sample is in allowed range between minimum and maximum sample value.

[00107] As was mentioned above to avoid changing of average luminance of filtered block it may be advantageous to skip filtering of first spectrum component (corresponding to DC value). That further allows to simplify filter implementation. In this case filtering step is as follows:

```
// frequency domain filtering
const int z0 = t0;
const int z1 = pTbl[t1];
const int z2 = pTbl[t2];
const int z3 = pTbl[t3];
```

[00108] For each pixel inside of reconstructed block or predicted block, a scan template is chosen based on position of filtering pixel inside reconstructed block or predicted block for steps 302 and 310. Scan template is chosen to guarantee all pixels be inside reconstructed CU or predicted CU and place close to processing pixel. Arbitrary scan order can be used inside template. For example, the predefined scan template is defined as set of spatial or raster offsets relative to a position of the current pixel inside the reconstructed block or predicted block, where offsets point to neighbor pixels are inside the reconstructed block or predicted block. Below is an example of scan template:

(0,0), (0,1), (1,0), (1,1)

[00109] FIG. 4A illustrates an example of templates for different pixel position inside square block (For example, square CU predicted block, or square CU reconstructed block).

Date Reçue/Date Received 2021-01-18

According to this figure boundary pixels can be filtered based on 4 point transform and central pixels can be filtered based on 8 point transform.

[00110] For rectangular reconstructed blocks or predicted blocks, wherein size of one side is more size of other side the scan should be performed along long side. For example for horizontal rectangular block the following scan order can be used

(0,-3), (0,-2), (0,-1), (0,0), (0,1), (0,2), (0,3), (0,4),

where in each pair (y,x) x is horizontal offset and y is vertical offset in respect to position of filtering pixel inside filtering reconstructed block or predicted block.

[00111] The proposed filter can be selectively applied depending on conditions:

- for reconstructed blocks or predicted blocks with non-zero residual signal;
- depending on block size, e.g. for small reconstructed block or predicted block (minimal size is less than threshold);
- depending on an aspect ratio of the reconstructed block or predicted block;
- depending on prediction mode (Intra or Inter) of the reconstructed block or predicted block, e.g., by applying filter to inter-predicted blocks only; or
- for any combination of described above conditions.

For example, to avoid processing of small blocks thy filter can be bypassed (not applied) if block size is less or equal to 4x4 pixels. That reduces worst-case complexity which is usual corresponds to smallest block processing.

As another example filter is applied only to blocks that have non-zero residual signal. That is beneficial if quantization or residuals was used since the filter is aimed to improve quantization error. If block has no residual that maybe an indication that prediction is good and no further filtering is required.

As another example, since intra prediction normally is worse than inter, the filter can be applied to intra predicted blocks independently of presence of non-zero residual and applied to inter predicted blocks only if block has non-zero residual signal.

[00112] Filter parameter sigma and scan pattern may vary depending on conditions listed above.

Date Reçue/Date Received 2021-01-18

[00113] Fig. 4B illustrates equivalent filter shape considering one pixel inside of current block for exemplary scan template (0,0), (0,1), (1,0), (1,1). For the filtering of current pixel square area of 3x3 pixels is used (current pixel is marked by dark-gray color in the center of 3x3 square). Filtered pixel is obtained by combining transform domain filtered samples from four 2x2 groups. It can be understood that if current pixel is located in block border (e.g. top border) top left and top right 2x2 groups are unavailable and only two 2x2 groups (bottom left and bottom right) can be used for filtering. Furthermore if current pixel is located in block corner (e.g. top-left corner) only one 2x2 group (bottom right) can be used for filtering.

[00114] To increase quality of filtering by using more four 2x2 groups for border and corner pixels, the current block can be padded by additional samples. Fig.4C gives an example of padding on left and top sides. Padding samples can be taken from already reconstructed blocks.

[00115] For further unification of filtering process for all pixels in block (four 2x2 groups are used for filtering of all pixels in current block), in addition to top-left padding current block can also be extended by bottom-right padding as illustrated on Fig. 4D. Unification of filtering is beneficial due to simplifying implementation by excluding special processing cases for corner pixels and/or border pixels.

[00116] Padding samples are preferably taken from adjusted neighboring samples from already reconstructed blocks. In state-of-the-art video codecs those already reconstructed blocks can be located either on left or top side from current block or on right or bottom side depending on block reconstruction order. Using more information from adjustment samples, it improves filtering quality and makes transition between blocks more smooth.

[00117] Retrieving reconstructed samples from adjusted blocks or previously reconstructed blocks can require additional memory load for hardware or software implementation. To minimize or exclude additional memory, it is beneficial to use samples intended for intra prediction of current block which are commonly taken from one, two or more rows and columns from neighboring blocks adjusted to current block borders. These samples are usually stored in fast memory (also known as “line” buffer) for easy access for intra prediction and called reference samples of intra prediction.

Date Reçue/Date Received 2021-01-18

[00118] It should be further noted that in some implementation, before performing intra prediction, reference sample(intra reference samples) are pre-processed before prediction e.g. by smoothing, sharpening, de-ringing or bilateral filtering. In this case it may be beneficial to use pre-processed samples for padding of current block.

[00119] If some samples in the padded area are not available, due to order of adjusted block reconstruction, required samples can be padded from the current block expanding border pixels to the padded area as illustrated on Fig. 4D.

[00120] Figure 5A shows a flow diagram illustrating steps of a corresponding in-loop filtering method 500 according to an embodiment. The reconstructed block comprises a plurality of pixels. The method 500 comprises the following steps: loading (502) a current pixel and its neighboring pixels to a linear buffer according to a predefined scan template; obtaining (504) spectrum components by performing 1D transform for pixels in the linear buffer; obtaining (506) filtered spectrum components by multiplying each spectrum component with a gain coefficient, wherein the gain coefficient depends on a corresponding spectrum component and a filtering parameter; obtaining (508) filtered pixels by performing inverse 1D transform for filtered spectrum components; and generating (510) a filtered reconstructed block based on the filtered pixels estimated on previous processing steps. Method 500 can be performed by the encoding apparatus as shown in FIG.1 and the decoding apparatus as shown in FIG. 2. Detailed information 300 of FIG. 3A or information 300' of FIG. 3B are also applied to method 500 as shown in FIG. 5A.

[00121] Similar to Figure 5A, Figure 5B shows a flow diagram illustrating steps of a corresponding in-loop filtering method 500' according to another embodiment. At this example, the block (or frame) is a predicted block, and the filtered block is a filtered predicted block. Detailed description of Figure 5B is similar to Figure 5A.

[00122] The hardware implementation is discussed.

[00123] Hadamard transform domain filter is placed just after block reconstruction and process samples that can be involved into subsequent blocks reconstruction particularly as a reference samples of intra prediction. Thus the latency introduced by the filter needs to be minimize to ensure entire reconstruction pipeline is not affected much.

Date Reçue/Date Received 2021-01-18

[00124] Hadamard transform is considered to be relatively simple for hardware implementation. Only additions are required for its implementation but multiplications. As can be seen from below Pseudo code 1, forward and backward transform contains 4 addition which can be done in parallel or by reusing of intermediate results with two sequential addition operations.

Pseudo code 1

[00125]

```
for (int r = 0; r < height - 1; ++r)
{
  Pel *pIn = &block[r*strideBlk];
  Pel *pAcc = &accBlock[r*strideAcc];

  for (int c = 0; c < width - 1; ++c, pIn++, pOut++)
  {
    const int x0 = pIn[p0];
    const int x1 = pIn[p1];
    const int x2 = pIn[p2];
    const int x3 = pIn[p3];

    // forward transform
    const int y0 = x0 + x2;
    const int y1 = x1 + x3;
    const int y2 = x0 - x2;
    const int y3 = x1 - x3;

    const int t0 = y0 + y1;
    const int t1 = y0 - y1;
    const int t2 = y2 + y3;
    const int t3 = y2 - y3;

    // filtering
    const int z0 = t0;  // skip DC
    const int z1 = RdTbl(t1, tbl, thr);
    const int z2 = RdTbl(t2, tbl, thr);
    const int z3 = RdTbl(t3, tbl, thr);

    // backward transform
    const int iy0 = z0 + z2;
    const int iy1 = z1 + z3;
    const int iy2 = z0 - z2;
    const int iy3 = z1 - z3;

    pAcc[p0_out] += ((iy0 + iy1) >> HTDF_BIT_RND4);
    pAcc[p1_out] += ((iy0 - iy1) >> HTDF_BIT_RND4);
    pAcc[p2_out] += ((iy2 + iy3) >> HTDF_BIT_RND4);
    pAcc[p3_out] += ((iy2 - iy3) >> HTDF_BIT_RND4);

    // normalization
    pIn[p0] = ClipPel((pOut[p0_out] + HTDF_CNT_SCALE_RND) >> HTDF_CNT_SCALE, clpRng);
  }
}
```


Date Reçue/Date Received 2021-01-18

[00126] Forward and backward Hadamard transform can be implemented in hardware by using combinational logic. The more attention is required to fast and parallel access to the LUT.

[00127] The SRAM based LUT is discussed.

[00128] In this example implementation, the LUT is stored in on-chip single port Static RAM (Figure 6).

[00129] Once data prom previous processing step are available in the buffer by rising edge of the clock it is accessed by combinational logic implementing forward Hadamard transform (containing two subsequential additions). After completing the combinational logic address is available for each LUT. Using invertor and falling edge of the clock the data is accessed from the SRAM. Second combinational logic implementing backward Hadamard transform and normalization is started immediately after data from LUT is available. The output filtered samples became available at the end of current clock cycle and are ready for processing by next algorithm at the next rising edge of the clock.

[00130] The flip-flop based LUT is discussed.

[00131] Considering that one table for filtering process is limited by 16 entries it looks more efficient to implement LUT based on flip-flops. Such design does not require several LUTs for parallel processing and clock edge for data access. Parallel access is provided by multiplexer as depicted in Figure 7 illustrating exemplary design for processing one 2x2 group. In suggested design 16 flip-flops of 7 bits are required to provide parallel access during the filtering process. The QP specific LUT can be loaded to flip-plops once QP for current CU is available.

[00132] Combining results of four 2x2 groups filtering with reusing of results of same spatial group final filter output is generated as depicted on Figure 8.

[00133] Given above analysis allows came to the conclusion that proposed filter can be implemented in hardware within one clock using either SRAM or flip-flop based LUT implementation.

[00134] A complexity analysis is discussed.

[00135] Measure impact on Bitrate/PSNR relative to the anchor(s).

Date Reçue/Date Received 2021-01-18

[00136] Complexity analysis (e.g., encoding and decoding time measure, complexity analysis by filling the table below).

Test #	filter shape	Computational complexity (# of multiplications/ additions/shifts/checks*) per sample	Precision of multiply	Parallel friendly (each sample can be filtered independently from other samples)	Number of clock cycles	memory requirement	How to derive filter coeffs	Minimal CU Size for applying filters
14.3 a	3x3	0/20+4 l-bit add for round/5/6	n/a	yes	< 1 clock	140 bytes (32 7-bit values per qp group)	Precal-culated in LUT	4x8, 8x4
14.3 b	3x3	0/20+4 l-bit add for round /5/6	n/a	yes	< 1 clock	70 bytes (16 7-bit values per qp group)	Precal-culated in LUT	4x8, 8x4

* max/min/abs are countered as checks

Table 1. CE14-3 complexity analysis summary

[00137] Experimental results are discussed.

[00138] Objective results are discussed.

[00139] The objective performance is presented in the following tables:

	All Intra Main10				
	Over BMS-2.0.1 with VTM config				
	Y	U	V	EncT	DecT
Class A1	-0.19%	0.29%	0.24%	110%	115%
Class A2	-0.35%	0.31%	0.32%	109%	112%
Class B	-0.42%	0.29%	0.32%	109%	113%
Class C	-0.83%	0.23%	0.42%	110%	110%

Date Reçue/Date Received 2021-01-18

Class E	-0.54%	0.28%	0.19%	109%	114%
Overall	-0.48%	0.28%	0.31%	109%	112%
Class D	-0.61%	0.47%	0.01%	109%	108%
Class F (optional)	-0.82%	0.07%	0.04%	108%	107%

	Random Access Main 10				
	Over BMS-2.0.1 with VTM config				
	Y	U	V	EncT	DecT
Class A1	-0.43%	-0.23%	-0.25%	105%	104%
Class A2	-0.92%	-0.57%	-0.43%	105%	103%
Class B	-0.69%	-0.02%	-0.27%	106%	104%
Class C	-0.75%	0.10%	0.14%	105%	104%
Class E					
Overall	-0.70%	-0.14%	-0.19%	105%	104%
Class D	-0.67%	-0.14%	0.53%	104%	103%
Class F (optional)	-0.77%	0.10%	0.13%	104%	102%

	Low delay B Main10				
	Over BMS-2.0.1 with VTM config				
	Y	U	V	EncT	DecT
Class A1					
Class A2					
Class B	-0.58%	0.52%	0.54%	104%	104%
Class C	-0.74%	0.33%	0.52%	104%	104%
Class E	-0.75%	0.31%	1.09%	102%	101%
Overall	-0.68%	0.40%	0.67%	104%	103%
Class D	-0.90%	0.75%	0.28%	104%	104%
Class F (optional)	-0.73%	0.20%	-0.09%	103%	102%

Date Reçue/Date Received 2021-01-18

Table 2. Coding performance of test 14-3a

	All Intra Main10				
	Over BMS-2.0.1 with VTM config				
	Y	U	V	EncT	DecT
Class A1	-0.17%	0.28%	0.28%	110%	115%
Class A2	-0.33%	0.27%	0.29%	109%	112%
Class B	-0.40%	0.31%	0.39%	109%	113%
Class C	-0.82%	0.20%	0.32%	109%	110%
Class E	-0.54%	0.34%	0.25%	109%	113%
Overall	-0.47%	0.28%	0.32%	109%	112%
Class D	-0.65%	0.43%	0.22%	109%	108%
Class F (optional)	-0.81%	0.01%	0.12%	108%	107%

	Random Access Main 10				
	Over BMS-2.0.1 with VTM config				
	Y	U	V	EncT	DecT
Class A1	-0.41%	-0.36%	-0.24%	105%	105%
Class A2	-0.92%	-0.49%	-0.37%	105%	103%
Class B	-0.69%	-0.12%	0.12%	106%	104%
Class C	-0.75%	0.14%	0.10%	105%	104%
Class E					
Overall	-0.70%	-0.17%	-0.06%	105%	104%
Class D	-0.69%	-0.30%	0.49%	104%	103%
Class F (optional)	-0.82%	0.11%	0.11%	104%	102%

	Low delay B Main10				
	Over BMS-2.0.1 with VTM config				
	Y	U	V	EncT	DecT
Class A1					
Class A2					

Date Reçue/Date Received 2021-01-18

Class B	-0.57%	0.49%	0.69%	104%	104%
Class C	-0.76%	0.31%	0.42%	104%	106%
Class E	-0.67%	-0.66%	-0.48%	102%	102%
Overall	-0.66%	0.14%	0.31%	103%	104%
Class D	-0.88%	0.92%	0.31%	103%	105%
Class F (optional)	-0.74%	0.38%	-0.15%	103%	102%

Table 3. Coding performance of test 14-3b

[00140] Suggested LUT 70bytes with 16 bytes per QP, allowing 1 clock HW implementation. Etc. Proposed to adopt Hadamard transform domain filter to next version of VTM.

[00141] The following document is also referenced:

[00142] Joint Video Experts Team (JVET) document JVET-K0068.

[00143] The below illustrate examples for optimizing the LUT.

[00144] As one example 1, a set of quantization parameters (QPs) are chosen to generate the lookup table (LUT), where the set of QPs include a first QP corresponding to (i) index and a second QP corresponding to (i+1) index, and the first QP and the second QP have a constant interval. For example, the interval may equal to 8, 10 or 16.

[00145] For example, taking the constant interval is 8 as an example, sparse table by having LUT for qp = {20, 28, 36, 44, 52}. The interval between the first gp 20 and the second gp 28 is 8. Similarly, the interval between the second gp 28 and the third gp 36 is 8. During filtering table with closest QP is chosen.

[00146] As another example, taking the constant interval is 8 as an example, sparse table by having LUT for qp = {18, 26, 34 ,42, 50}. The interval between the first gp 18 and the second gp 26 is 8. Similarly, the interval between the second gp 26 and the third gp 34 is 8. During filtering table with closest QP is chosen.

[00147]

LUT size: 5x128 = 640 bytes

Date Reçue/Date Received 2021-01-18

Below is pseudo code 2 reflecting which QPs are chosen to generate lookup tables (LUT).

pseudo code 2

```
#define HTDF_MIN_QP                                          20
#define HTDF_MAX_QP                                          (MAX_QP+1)
#define HTDF_QP_ROUND                                        8
 for (int qp_in = HTDF_MIN_QP; qp_in <  HTDF_MAX_QP; qp_in++)
 {
   int idx = ( (qp_in - HTDF_MIN_QP) + (HTDF_QP_ROUND>>1) ) / HTDF_QP_ROUND;
   int qp = idx*HTDF_QP_ROUND + HTDF_MIN_QP;

   if (idxPrev != idx)
   {
     tblGen(HTDFTable[idx], qp);
     idxPrev = idx;
   }
 }
```

[00148] At the pseudo code, HTDF_QP_ROUND represents the constant interval. Having the interval as power of two is advantageous allowing to implement division operation for index calculation as a shift. It should be noted that different values of constant interval may be chosen e.g. 2, 4, 10, 15 or 16 etc. Moreover as alternative embodiment an interval may be arbitrary and which LUT is calculated for arbitrary set of QPs.

[00149] During filtering process index corresponding LUT for given QP is calculated as:

```
int idx = ((qp - HTDF_MIN_QP) + (HTDF_QP_ROUND >> 1)) / HTDF_QP_ROUND;
```

or alternatively with less precision:

```
int idx = (qp - HTDF_MIN_QP) / HTDF_QP_ROUND;
```

If constant interval is power of 2, e.g. then index of LUT can be advantageously calculated using shift operation instead division:

```
int idx = (qp - HTDF_MIN_QP) >> HTDF_QP_ROUND_LOG2 = (qp - HTDF_MIN_QP) >> 3;
```

[00150] As another example 2, decoder or encoder drops N bits from table values, and N is an integer. That allows have sparse LUT representation storing only selected values from given range. For example, N is 3. Max table value is 127 (7 bits), dropping 3 bits, the result is 4 bits, that is 16 entries of 7 bit values – 16 bytes roughly.

Date Reçue/Date Received 2021-01-18

[00151] Below is pseudo code 3 describing how LUT is generated according to given qp value.

pseudo code 3

```
#define HTDF_TBL_SH                              3
#define HTDF_TBL_RND                             ((1<<HTDF_TBL_SH)>>1)

void HTDFilter::tblGen(HTDF_TBL_TYPE *tbl, int qp)
{
  float sigma = (2.64f)*powf(2.0f, (0.1269f)*(qp - 11));
  int sigma_l = (int)(sigma * HTDF_SIGMA_SCALE );

  sigma_l *= sigma_l;
  sigma_l *= 4;

  for (int k_in = 0; k_in < HTDF_SHORT_TBL_THR; k_in++)
  {
    const int Idx = (k_in + HTDF_TBL_RND)>>HTDF_TBL_SH;
    const int k = Idx << HTDF_TBL_SH;

    unsigned int num = (k*k*k) << 10;
    int den = ((k*k) << 10) + sigma_l;
    int value_long = num / den;
    HTDF_TBL_TYPE value_int = (HTDF_TBL_TYPE)value_long;

    tbl[Idx] = value_int;
  }
}
```

In given example HTDF_TBL_SH defines number of bits to drop which may be 1, 2, 3, 4 etc. Below is pseudo code illustrating access to sparse LUT during filtration:

```
tbl[(z + HTDF_TBL_RND)>> HTDF_TBL_SH]
```

[00152] When combine the above examples 1 and 2, FIG. 9 as an example, shows the result of optimizing the LUT. As shown in FIG. 9, combining examples 1 and 2: 5 tables x 16 entries x 7 bits = 560 bits = 70 bytes.

[00153] It should be noted that amount of LUT entries is defined by HTDF_SHORT_TBL_THR (threshold value from para [0076]) and HTDF_TBL_SH number of bits dropped. Considering threshold equal to 128 (that is 1 << 7) and bits dropped 3 gives

Date Reçue/Date Received 2021-01-18

number of tables entries equal to 1 << (7 - 3) = 1 << 4 = 16. As describe above the LUT threshold is preferably chosen to make result of equation from para [0077] close to 1 and as it also described there the threshold may differ depending on QP. Thus for LUT generation for higher QP values it may be beneficial to increase threshold from 128 (1 << 7) to e.g. 256 (1 << 8). In that case keeping same precision of LUT entries (e.g. with dropped 3 bits) will require 32 entries (32 = 1 << (8 – 3) = 1 << 5). Alternatively to keep same LUT size for higher QPs as for lower QPs the precision may be further reduced to 4 to keep 16 entries of table 16 = 1 << (8 - 4) = 1 << 4.

[00154] In some implementation keeping LUT size limited and having THR in order to keep of equation from para [0077} maybe contradictory. Indeed at high QP values (which leads to high σ value) keeping LUT size limited by e.g. 16 entries may lead to undesirable gap in filter transfer function (which is represented by LUT) around value 120 as depicted on FIG. 10 (which also includes method of LUT subsampling by dropping of 3 least significant bits off).

[00155] FIG. 11 represents same table with table entries being plot one-by-one without consideration LUT subsampling effect illustrating gap between last LUT entry at index 15 and next point in filter transfer function at index 16.

[00156] FIG. 12 illustrates the method how gap can be eliminated using auxiliary function coming through THR value at the argument of corresponding to last LUT entry + 1, e.g. by using straight line equation (pointed by green color), coming through point THR=128 at argument 16 that corresponds to argument of last LUT entry (which is 15) increased by 1 or in other words LUT size (which is 16) and crossing x-axis at some point (e.g. at a value of 11 like in given example). It should be noted that other types of auxiliary functions can be used based on same principle, e.g. exponential, logarithmic, parabolic etc. or combination of them.

[00157] FIG. 13 Illustrates the example of eliminating gap by taken maximum from two values while generating the LUT, where the first value is LUT entry as described above $LUT(R_i, \sigma) = \frac{R_i^3}{R_i^2 + m * \sigma^2}$; and the second is a value of AuxiliaryFunction (straight line in this example) at the same argument i:

$$LUT(R_i, \sigma) = \max\left(\frac{R_i^3}{R_i^2 + m * \sigma^2}, AuxiliaryFunc_\sigma(i)\right);$$

Date Reçue/Date Received 2021-01-18

where $AuxiliaryFunc_{\sigma}(i)$ represents the auxiliary function, and the auxiliary function has a value equal to THR at *i* corresponding to an argument of last LUT + 1.

[00158] FIG. 14 illustrates filter transfer function after applying described above method of using auxilary straight line equation and LUT subsampling by dropping 3 least significant bits.

[00159] As described in para [00144] one LUT can used for group of QPs. To cover the possible QP range the predefined QP set is used and one LUT is generated for each QP from the set. The FIG.15 illustrates example of filter transfer functions depending on five QPs in the set and corresponding tables (from 0 to 4). In this example the table 4 is generated using method described in paras [00156]-[00157] with straight-line auxiliary function crossing x-axis at the value of 11; and table 3 is generated using same method with straight-line auxiliary function crossing x-axis at the value of 9. The tables used in this example have values according to:

table0 = { 0, 2, 10, 19, 28, 36, 45, 53, 61, 70, 78, 86, 94, 102, 110, 118, },
table1 = { 0, 0, 5, 12, 20, 29, 38, 47, 56, 65, 73, 82, 90, 98, 107, 115, },
table2 = { 0, 0, 1, 4, 9, 16, 24, 32, 41, 50, 59, 68, 77, 86, 94, 103, },
table3 = { 0, 0, 0, 1, 3, 5, 9, 14, 19, 25, 32, 40, 55, 73, 91, 110, },
table4 = { 0, 0, 0, 0, 0, 1, 2, 4, 6, 8, 11, 14, 26, 51, 77, 102, },

[00160] As described above in paras [00150]-[00153] the method of table subsampling can drop N bits from table values to reduce table size. As also mentioned in these paras N may be different depending on QP used for certain table generation and selected THR value for this table. For example for lower QP values the filtering parameter σ is relatively lower than for higher QPs. Thus the absolute value of THR can be reduced without sacrificing of performance. Furthermore to keep table size same for all QPs in the set (that is beneficial for simplifying implementation) and reduce subsampling rate for lower QPs (which correspond for less compression level and better reconstructed picture quality) it may be beneficial to reduce the amount of dropped bits N in comparison to other QP tables, e.g. by setting N for lover QP equal to 2 and setting THR to 64. The FIG. 16 illustrates example of filter transfer functions for five QPs in the set based on corresponding tables (from 0 to 4) with N set to 2 for first table (corresponding to lover QP range) and N set to 3 for other tables. This example also includes method of using *AuxiliaryFunction* for table 3 and table 4 generation as described in paras

Date Reçue/Date Received 2021-01-18

[00159]-[00160]. For table 4 straight-line auxiliary function crosses the x-axis at the value of 11. For table 4 straight-line auxiliary function crosses the x-axis at the value of 9. The tables used in this example have values according to:

table0 = { 0, 0, 2, 6, 10, 14, 19, 23, 28, 32, 36, 41, 45, 49, 53, 57, },
table1 = { 0, 0, 5, 12, 20, 29, 38, 47, 56, 65, 73, 82, 90, 98, 107, 115, },
table2 = { 0, 0, 1, 4, 9, 16, 24, 32, 41, 50, 59, 68, 77, 86, 94, 103, },
table3 = { 0, 0, 0, 1, 3, 5, 9, 14, 19, 25, 32, 40, 55, 73, 91, 110, },
table4 = { 0, 0, 0, 0, 0, 1, 2, 4, 6, 8, 11, 14, 26, 51, 77, 102, },

[00161] As described above in paras [00150]-[00153] the method of table subsampling can drop N bits from table values to reduce table size. As also mentioned in these paras N may be different depending on QP used for certain table generation and selected THR value for this table. For example for higher QP values the filtering parameter σ is relatively higher than for lower QPs that may require on increase THR value to keep equation from para [0077] closer to 1. At the same time to keep LUT size same for all QPs in the set (which is beneficial due to simplification of implementation) and also considering that for higher QP values reconstructed picture has more distortions and increasing subsampling of LUT is acceptable due to subjectively unnoticeable effect of LUT subsampling in presence of strong compression artifacts the value N of least significant bitts dropped may be increased to 4 e.g. for last and for second last table in the set. FIG. 17 illustrates example of filter transfer functions for five QPs in the set based on corresponding tables (from 0 to 4) with N set to 2 for first table (table 0 corresponding to lover QP range), N set to 4 for last and second last table (table 3 and table 4) and N set to 3 for other tables. In this example TRH is set to 64 for first table generation, set to 256 for last end second last table and set to 128 for rest tables. This example also includes method of using *AuxiliaryFunction* for table 3 and table 4 generation as described in paras [00159]-[00160]. For table 4 straight-line auxiliary function crosses the x-axis at the value of 6. For table 4 straight-line auxiliary function crosses the x-axis at the value of 8. The tables used in this example have values according to:

table0 = { 0, 0, 2, 6, 10, 14, 19, 23, 28, 32, 36, 41, 45, 49, 53, 57, },
table1 = { 0, 0, 5, 12, 20, 29, 38, 47, 56, 65, 73, 82, 90, 98, 107, 115, },
table2 = { 0, 0, 1, 4, 9, 16, 24, 32, 41, 50, 59, 68, 77, 86, 94, 103, },
table3 = { 0, 0, 3, 9, 19, 32, 47, 64, 81, 99, 117, 135, 154, 179, 205, 230, },
table4 = { 0, 0, 0, 2, 6, 11, 18, 27, 38, 51, 64, 96, 128, 160, 192, 224, },

Date Reçue/Date Received 2021-01-18

[00162] FIG. 18 is a block diagram of an apparatus 600 that can be used to implement various embodiments. The apparatus 600 may be the encoding apparatus as shown in FIG.1 and the decoding apparatus as shown in FIG. 2. Additionally, the apparatus 600 can host one or more of the described elements. In some embodiments, the apparatus 600 is equipped with one or more input/output devices, such as a speaker, microphone, mouse, touchscreen, keypad, keyboard, printer, display, and the like. The apparatus 600 may include one or more central processing units (CPUs) 610, a memory 620, a mass storage 630, a video adapter 640, and an I/O interface 660 connected to a bus. The bus is one or more of any type of several bus architectures including a memory bus or memory controller, a peripheral bus, a video bus, or the like.

[00163] The CPU 610 may have any type of electronic data processor. The memory 620 may have, or be, any type of system memory such as static random access memory (SRAM), dynamic random access memory (DRAM), synchronous DRAM (SDRAM), read-only memory (ROM), a combination thereof, or the like. In an embodiment, the memory 620 may include ROM for use at boot-up, and DRAM for program and data storage for use while executing programs. In embodiments, the memory 620 is non-transitory. The mass storage 630 includes any type of storage device that stores data, programs, and other information and to make the data, programs, and other information accessible via the bus. The mass storage 630 includes, for example, one or more of a solid state drive, hard disk drive, a magnetic disk drive, an optical disk drive, or the like.

[00164] The video adapter 640 and the I/O interface 660 provide interfaces to couple external input and output devices to the apparatus 600. For example, the apparatus 600 may provide SQL command interface to clients. As illustrated, examples of input and output devices include a display 690 coupled to the video adapter 640 and any combination of mouse/keyboard/printer 670 coupled to the I/O interface 660. Other devices may be coupled to the apparatus 600, and additional or fewer interface cards may be utilized. For example, a serial interface card (not shown) may be used to provide a serial interface for a printer.

[00165] The apparatus 600 also includes one or more network interfaces 650, which includes wired links, such as an Ethernet cable or the like, and/or wireless links to access nodes or one or more networks 680. The network interface 650 allows the apparatus 600 to communicate with remote units via the networks 680. For example, the network interface 650

Date Reçue/Date Received 2021-01-18

may provide communication to database. In an embodiment, the apparatus 600 is coupled to a local-area network or a wide-area network for data processing and communications with remote devices, such as other processing units, the Internet, remote storage facilities, or the like.

[00166] Proposed design of in-loop filter or prediction filter has the following advantages in respect to conventional adaptive filtering methods such as ALF:

- The proposed frequency domain filter derives filtering parameters (frequency domain gain coefficients) from reconstructed frame or predicted block on the decoder side and so filtering parameters is not required to be transferred from encoder to decoder side.
- ALF requires complex rate distortion optimization (RDO) on the encoder side for decreasing number of weighted coefficients for transmission. Proposed method does not require complex RDO on the encoder side (no parameters transferring) and applied for all blocks which satisfy the predefined conditions
- ALF is linear filter in pixel domain. The proposed filter is non-linear because gain coefficient for each 1D spectrum component depends on this spectrum component value. It allows to achieve additional coding gain from non-linear processing.
- ALF requires universal multipliers on the decoder side. In proposed method filtering can be implemented as lookup table, because gain for each spectrum coefficient is less one. Therefore the proposed method can be implemented without any multiplication.

[00167] Thus, the filter is provided allowing improving the efficiency for video coding with low complexity.

[00168] While a particular feature or aspect of the disclosure may have been disclosed with respect to only one of several implementations or embodiments, such feature or aspect may be combined with one or more other features or aspects of the other implementations or embodiments as may be desired and advantageous for any given or particular application. Furthermore, to the extent that the terms "include", "have", "with", or other variants thereof are used in either the detailed description or the claims, such terms are intended to be inclusive in a manner similar to the term "comprise". Also, the terms "exemplary", "for example" and "e.g." are merely meant as an example, rather than the best or optimal. The terms “coupled” and “connected”, along with derivatives may have been used. It should be understood that these terms may have been used to indicate that two elements cooperate or interact with each other

Date Reçue/Date Received 2021-01-18

regardless whether they are in direct physical or electrical contact, or they are not in direct contact with each other.

[00169] Although specific aspects have been illustrated and described herein, it will be appreciated by those of ordinary skill in the art that a variety of alternate and/or equivalent implementations may be substituted for the specific aspects shown and described without departing from the scope of the present disclosure. This application is intended to cover any adaptations or variations of the specific aspects discussed herein.

[00170] Although the elements in the following claims are recited in a particular sequence with corresponding labeling, unless the claim recitations otherwise imply a particular sequence for implementing some or all of those elements, those elements are not necessarily intended to be limited to being implemented in that particular sequence.

[00171] Many alternatives, modifications, and variations will be apparent to those skilled in the art in light of the above teachings. Of course, those skilled in the art readily recognize that there are numerous applications of the invention beyond those described herein. While the present invention has been described with reference to one or more particular embodiments, those skilled in the art recognize that many changes may be made thereto without departing from the scope of the present invention. It is therefore to be understood that within the scope of the appended claims and their equivalents, the invention may be practiced otherwise than as specifically described herein.

Date Reçue/Date Received 2021-01-18

CLAIMS:

1. A method for processing a block, wherein the block comprises a plurality of pixels, wherein the method comprises:

scanning a current pixel of the block and its neighboring pixels of the current pixel according to a predefined scan template, wherein the block is a reconstructed block or a predicted block;

obtaining spectrum components by performing transform for the current pixel and its neighboring pixels;

determining whether a filter is applied to the spectrum components based on conditions;

when the filter is applied to spectrum component, obtaining filtered spectrum components based on a filtering parameter and the spectrum components;

when the filter is not applied to spectrum component, saving spectrum component without changing;

obtaining filtered pixels by performing inverse transform for the filtered spectrum components; and

generating a filtered block based on the filtered pixels;

wherein the conditions comprises at least one of:

the block has non-zero residual signal;

a size of the block;

an aspect ratio of the block;

a prediction mode of the block;

a prediction mode of the block in intra; or

a prediction mode of the block in inter.

2. The method of claim 1, wherein the filter parameter and/or the predefined scan template are depending on the conditions.

3. The method of claim 1, wherein the filter is applied if size of the block obtained as block width multiplied by block height is greater than 4x4.

Date Reçue/Date Received 2022-04-22

4. The method of claim 1, wherein the filter is applied if the prediction mode of the block is inter and the block has non-zero residual signal.

5. The method of claim 1, wherein the filter is applied if the prediction mode of the block is intra.

6. The method of claim 1, wherein the filter is not applied if the prediction mode of the block is inter and the block width and the block height is greater or equal to 32.

7. The method of claim 6, wherein the obtaining the filtered spectrum components are estimated based on a look up table (LUT).

8. The method of claim 7, a filtered spectrum component $F(i,\sigma)$ is derived by:

$$F(i,\sigma) = \begin{cases} R(i) \ , \ Abs\big(R(i)\big) \geq THR \\ LUT(R(i),\sigma), \ R(i) > 0 \\ -LUT(-R(i),\sigma), \ R(i) \leq 0 \end{cases}$$

wherein (i) is an index of a spectrum component, R(i) is the spectrum component corresponding to (i) index, σ is the filtering parameter, $LUT(R(i),\sigma)$ is an array in the look up table, and THR is a threshold.

9. The method of claim 8, wherein $LUT(R(i),\sigma) = \frac{R(i)^3}{R(i)^2 + \mathrm{m} * \sigma^2}$, m is normalization constant equal to number of spectrum components.

10. The method of any one of claims 7-9, wherein the filtering parameter is derived based on a codec quantization parameter (QP).

11. The method of claim 10, wherein the filtering parameter σ is derived by

$$\boldsymbol{\sigma} = k * \mathbf{2}^{(n*(\boldsymbol{QP}-s))},$$

wherein QP is the codec quantization parameter, k, n and s are constants.

Date Reçue/Date Received 2022-04-22

12. The method of claim 11, wherein k is modified based on bit depth of pixels $k_{mod} = k$*(1 << (bit_depth - 8)).

13. The method of claim 12, wherein the filtering parameter is equal to zero if QP <= 17, and the block is bypassed without filtering.

14. The method of claim 8, THR is dependent of QP value.

15. The method of claim 14, THR is derived by:

$$\frac{THR^2}{THR^2+m*\sigma^2}=C$$

Wherein C is a value close to 1.

16. The method of any one of claims 1-15, wherein the predefined scan template is defined as set of spatial or raster offsets relative to a position of the current pixel inside the block.

17. The method of claim 16, wherein when the predefined scan template is defined as set of raster offsets relative to the position of the current pixel inside the block, offsets point to neighbour pixels are inside the block.

18. The method of claim 17, wherein the predefined scan template is defined as follows: scanTmpl[4] (y, x) ={ (0,0), (0,1), (1,0), (1,1) }.

19. The method of any one of claims 1-18, wherein at least one filtered pixel is placed to its original position according to the predefined scan template.

20. The method of any one of claims 1-19, wherein all filtered pixels are added to an accumulation buffer according to the predefined scan template, and wherein the accumulation buffer is initialized by zero before the obtaining filtered spectrum components.

Date Reçue/Date Received 2022-04-22

21. The method of claim 20, wherein final filtered pixels are obtained as accumulated values in the accumulation buffer divided by number of pixels adding to a current position of the accumulation buffer; and

wherein generating the filtered block based on the final filtered pixels.

22. The method of any one of claims 1-21, wherein differences between all filtered and corresponding unfiltered pixels are added to an accumulation buffer according to the predefined scan template, and wherein the accumulation buffer is initialized by unfiltered pixels multiplied by maximum number of pixel values to be added in the block.

23. The method of claim 22, wherein final filtered pixels are obtained as accumulated values in the accumulation buffer divided by maximum number of pixel values to be added in the block.

24. The method of any one of claims 1-23, wherein the method comprises:
scanning the current pixel of the block and its neighboring pixels of the current pixel according to the predefined scan template to an accumulation buffer; and
obtaining spectrum components by performing transform for pixels in the accumulation buffer.

25. The method of any one of claims 1-24, wherein the transform is a 1D transform.

26. The method of any one of claims 1-25, wherein the transform is a Hadamard transform.

27. A decoder comprising processing circuitry for carrying out the method according to any one of claims 1-26.

28. An encoder comprising processing circuitry for carrying out the method according to any one of claims 1-26.

29. A decoder comprising a filter for carrying out the method according to any one of claims 1-26.

Date Reçue/Date Received 2022-04-22

30. An encoder comprising a filter for carrying out the method according to any one of claims 1-26.

31. A decoder, comprising:

a memory storage comprising instructions; and

one or more processors in communication with the memory, wherein the one or more processors execute the instructions to carry out the method according to any one of claims 1-26.

32. An encoder, comprising:

a memory storage comprising instructions; and

one or more processors in communication with the memory, wherein the one or more processors execute the instructions to carry out the method according to any one of claims 1-26.

33. A non-transitory computer-readable medium storing computer-executable instructions that, when executed by a processor of a computing device, cause the computing device to perform the method of any one of claims 1-26.

34. An apparatus for processing a block, wherein the block comprises a plurality of pixels, and wherein the apparatus comprises:

means for scanning a current pixel of the block and its neighboring pixels of the current pixel according to a predefined scan template, wherein the block is a reconstructed block or a predicted block;

means for obtaining spectrum components by performing transform for the current pixel and its neighboring pixels;

means for determining whether a filter is applied to the spectrum components based on conditions;

means for obtaining filtered spectrum components based on a filtering parameter and the spectrum components when the filter is applied to spectrum component;

means for saving spectrum component without changing when the filter is not applied to spectrum component;

Date Reçue/Date Received 2022-04-22

means for obtaining filtered pixels by performing inverse transform for the filtered spectrum components; and

means for generating a filtered block based on the filtered pixels;

wherein the conditions comprises at least one of:

the block has non-zero residual signal;

a size of the block;

an aspect ratio of the block;

a prediction mode of the block;

a prediction mode of the block in intra; or

a prediction mode of the block in inter.

Date Reçue/Date Received 2022-04-22

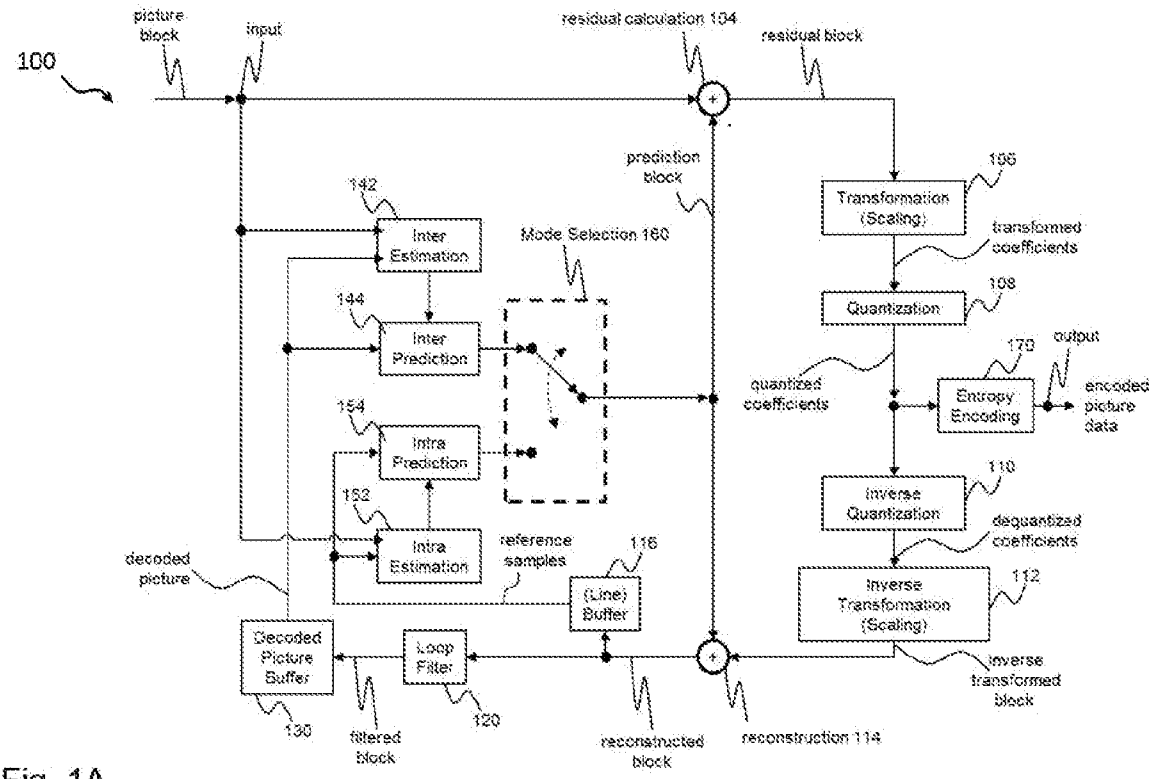

100
picture block
input
residual calculation 104
residual block
prediction block
142
Inter Estimation
Mode Selection 160
144
Inter Prediction
154
Intra Prediction
152
Intra Estimation
reference samples
116
(Line) Buffer
decoded picture
Decoded Picture Buffer
130
Loop Filter
120
filtered block
reconstructed block
reconstruction 114
196
Transformation (Scaling)
transformed coefficients
108
Quantization
quantized coefficients
170
Entropy Encoding
output
encoded picture data
110
Inverse Quantization
dequantized coefficients
112
Inverse Transformation (Scaling)
inverse transformed block

Fig. 1A

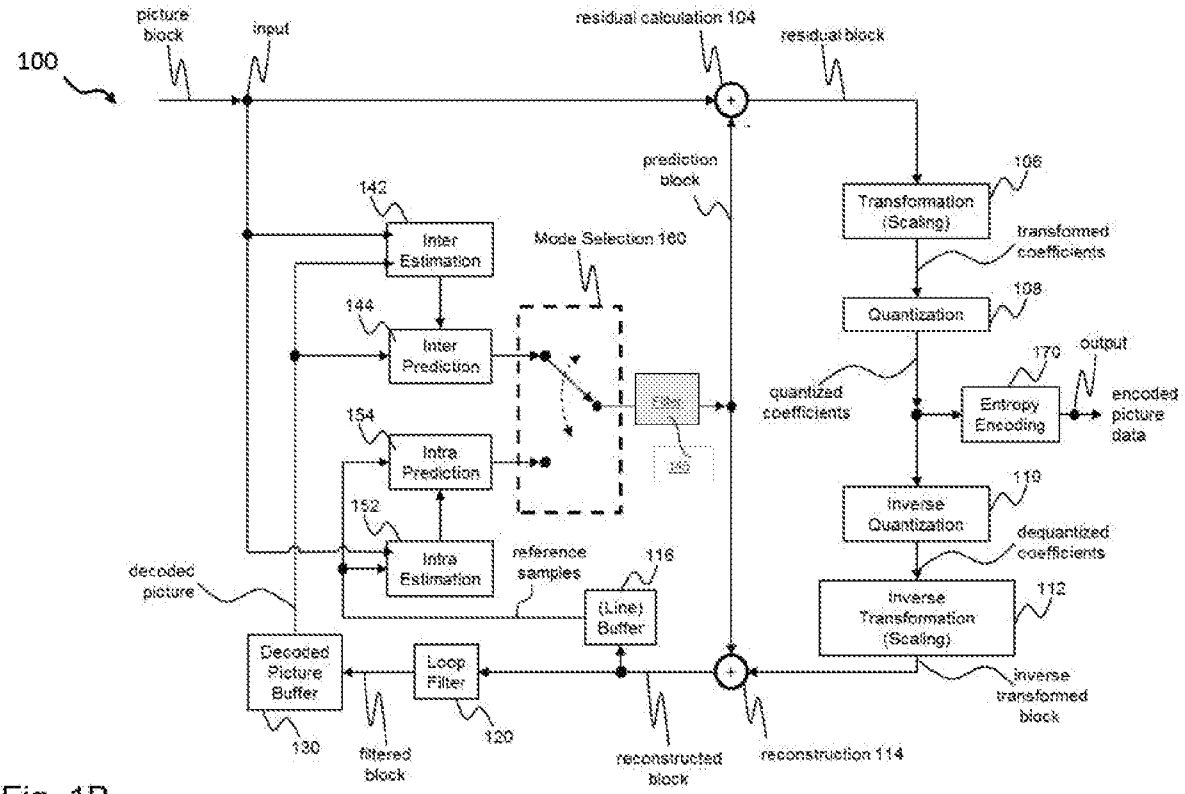

100
picture block
input
residual calculation 104
residual block
prediction block
142
Inter Estimation
Mode Selection 160
144
Inter Prediction
154
Intra Prediction
152
Intra Estimation
reference samples
116
(Line) Buffer
decoded picture
Decoded Picture Buffer
130
Loop Filter
120
filtered block
reconstructed block
reconstruction 114
106
Transformation (Scaling)
transformed coefficients
108
Quantization
quantized coefficients
170
Entropy Encoding
output
encoded picture data
110
Inverse Quantization
dequantized coefficients
112
Inverse Transformation (Scaling)
inverse transformed block

Fig. 1B

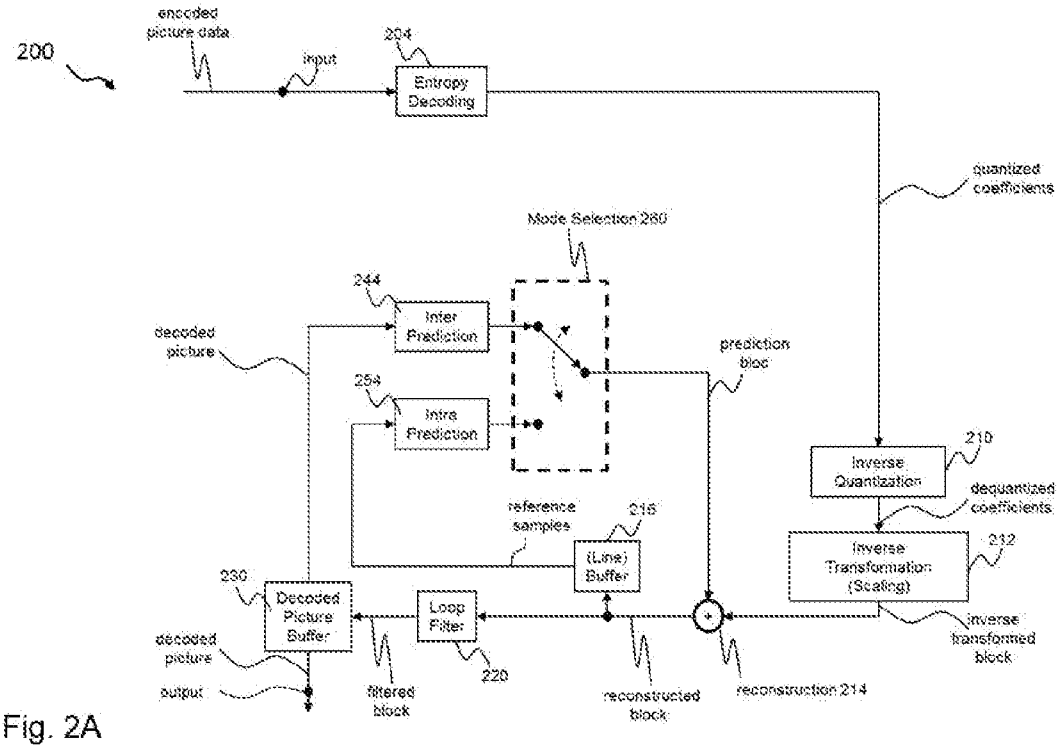

200
encoded picture data
input
204
Entropy Decoding
quantized coefficients
Mode Selection 260
244
Inter Prediction
decoded picture
prediction bloc
254
Intra Prediction
210
Inverse Quantization
dequantized coefficients
reference samples
216
(Line) Buffer
Inverse Transformation (Scaling)
212
230
Decoded Picture Buffer
Loop Filter
inverse transformed block
decoded picture
output
filtered block
220
reconstructed block
reconstruction 214

Fig. 2A

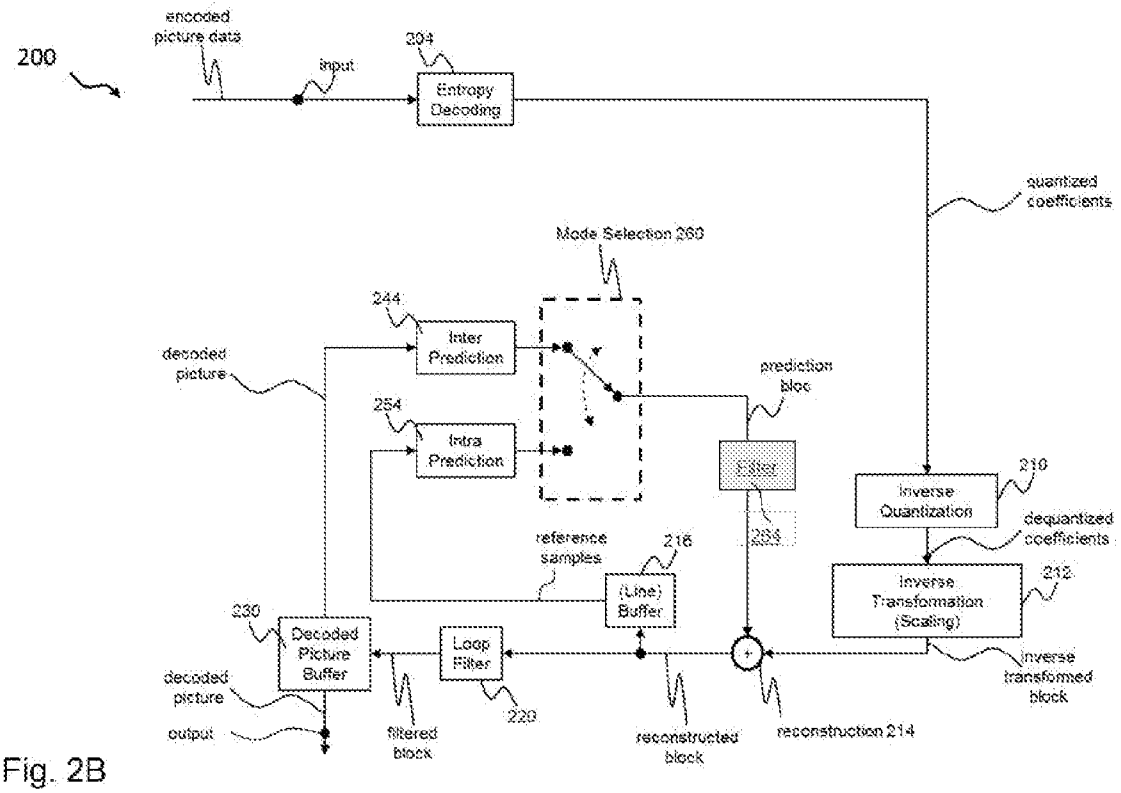

200
encoded picture data
input
304
Entropy Decoding
quantized coefficients
Mode Selection 260
244
Inter Prediction
decoded picture
prediction bloc
254
Intra Prediction
Filter
258
Inverse Quantization
210
dequantized coefficients
reference samples
216
(Line) Buffer
Inverse Transformation (Scaling)
212
230
Decoded Picture Buffer
Loop Filter
inverse transformed block
decoded picture
220
output
filtered block
reconstructed block
reconstruction 214

Fig. 2B

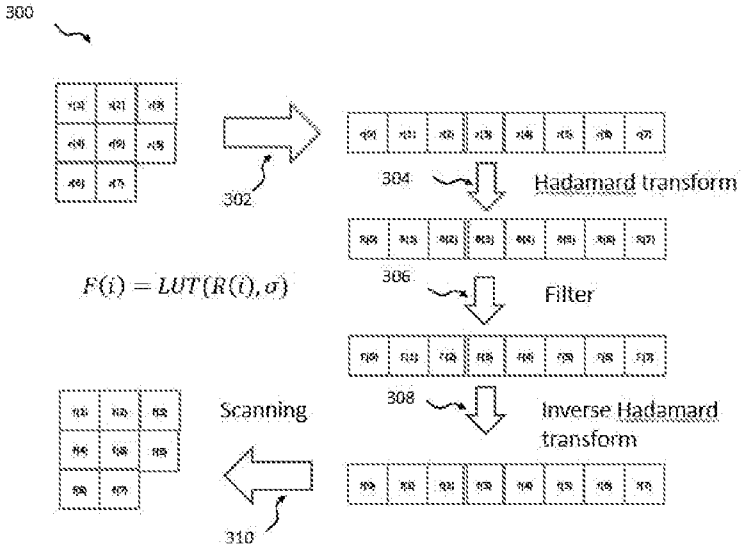

300
302
304 Hadamard transform
306 Filter
$F(i) = LUT(R(i), \sigma)$
308 Inverse Hadamard transform
Scanning
310

Fig. 3A

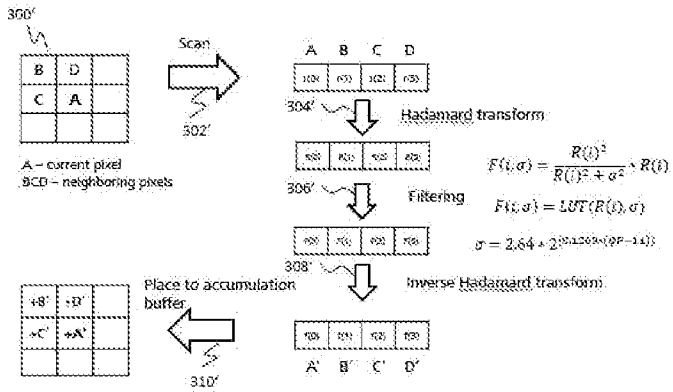

300'
Scan
302'
A – current pixel
BCD – neighboring pixels
A B C D
304' Hadamard transform
306' Filtering
$F(i,\sigma) = \frac{R(i)^2}{R(i)^2 + \sigma^2} \cdot R(i)$
$F(i,\sigma) = LUT(R(i),\sigma)$
308' Inverse Hadamard transform
Place to accumulation buffer
310'
A' B' C' D'

Fig. 3B

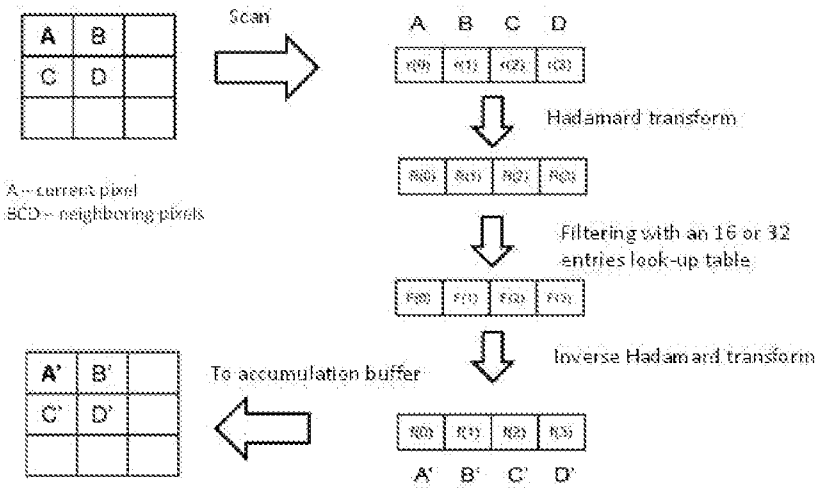

A B
C D
A – current pixel
BCD – neighboring pixels
Scan
A B C D
Hadamard transform
Filtering with an 16 or 32 entries look-up table
Inverse Hadamard transform
To accumulation buffer
A' B'
C' D'
A' B' C' D'

FIG. 3C

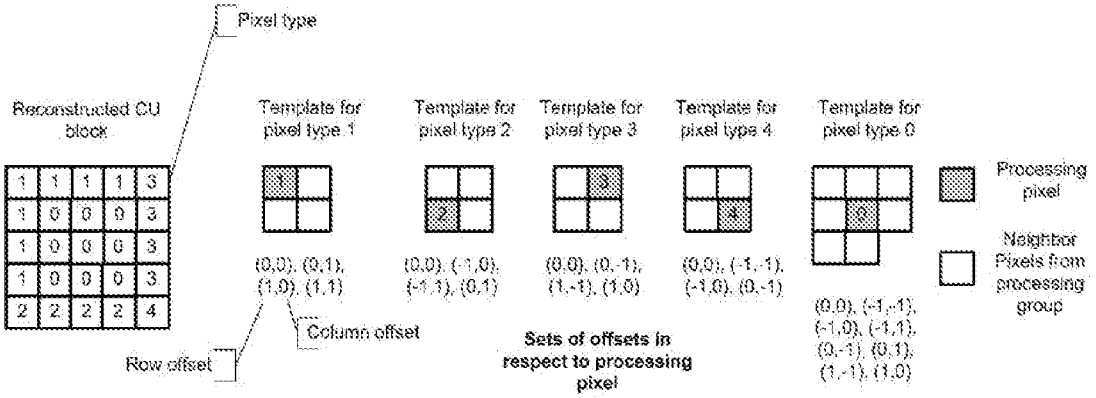

Pixel type
Reconstructed CU block
Template for pixel type 1
Template for pixel type 2
Template for pixel type 3
Template for pixel type 4
Template for pixel type 0
Processing pixel
Neighbor Pixels from processing group
(0,0), (0,1), (1,0), (1,1)
(0,0), (-1,0), (-1,1), (0,1)
(0,0), (0,-1), (1,-1), (1,0)
(0,0), (-1,-1), (-1,0), (0,-1)
(0,0), (-1,-1), (-1,0), (-1,1), (0,-1), (0,1), (1,-1), (1,0)
Row offset
Column offset
Sets of offsets in respect to processing pixel

Fig. 4A

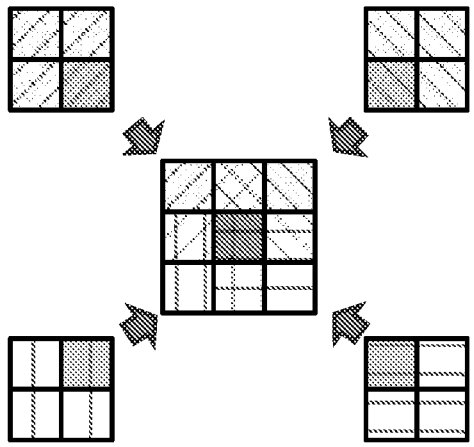


Fig 4B

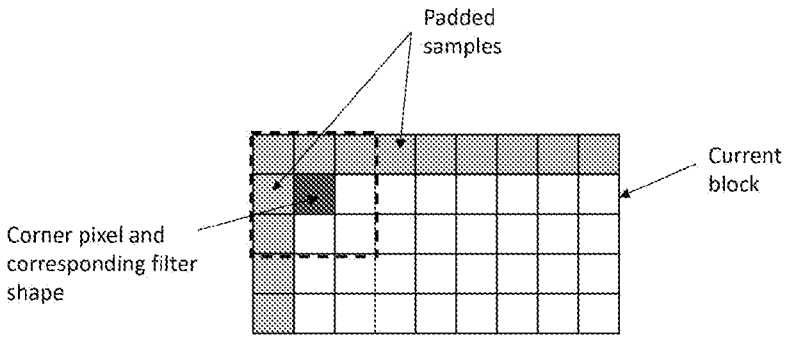

Padded samples
Current block
Corner pixel and corresponding filter shape

FIG. 4C

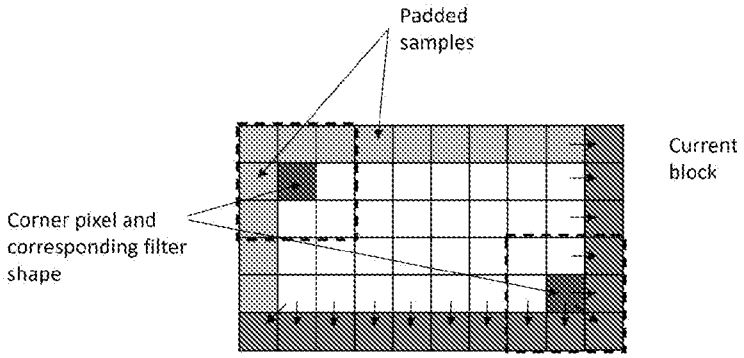

Padded
samples
Current
block
Corner pixel and
corresponding filter
shape

FIG. 4D

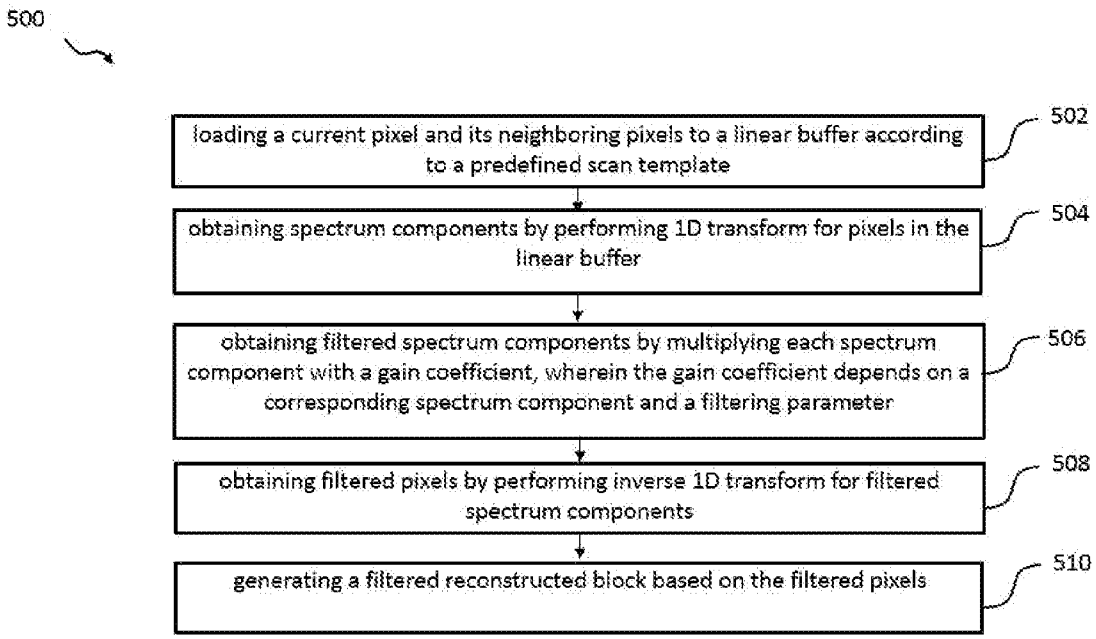

500
loading a current pixel and its neighboring pixels to a linear buffer according to a predefined scan template
502
obtaining spectrum components by performing 1D transform for pixels in the linear buffer
504
obtaining filtered spectrum components by multiplying each spectrum component with a gain coefficient, wherein the gain coefficient depends on a corresponding spectrum component and a filtering parameter
506
obtaining filtered pixels by performing inverse 1D transform for filtered spectrum components
508
generating a filtered reconstructed block based on the filtered pixels
510

Fig. 5A

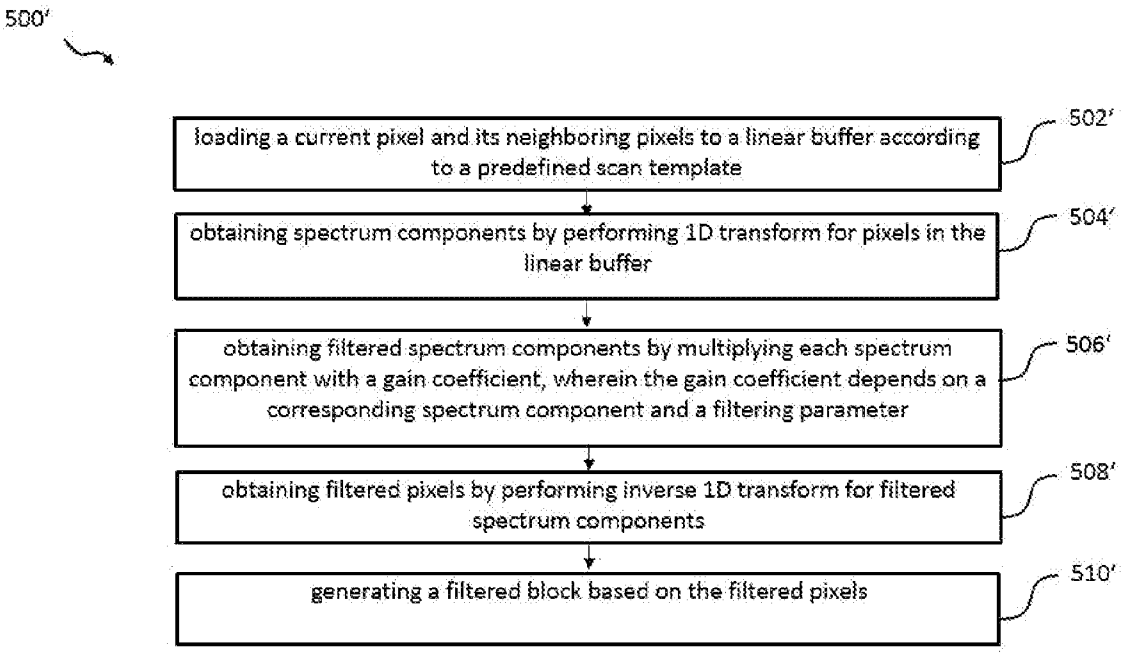

500'
loading a current pixel and its neighboring pixels to a linear buffer according to a predefined scan template
502'
obtaining spectrum components by performing 1D transform for pixels in the linear buffer
504'
obtaining filtered spectrum components by multiplying each spectrum component with a gain coefficient, wherein the gain coefficient depends on a corresponding spectrum component and a filtering parameter
506'
obtaining filtered pixels by performing inverse 1D transform for filtered spectrum components
508'
generating a filtered block based on the filtered pixels
510'

Fig. 5B

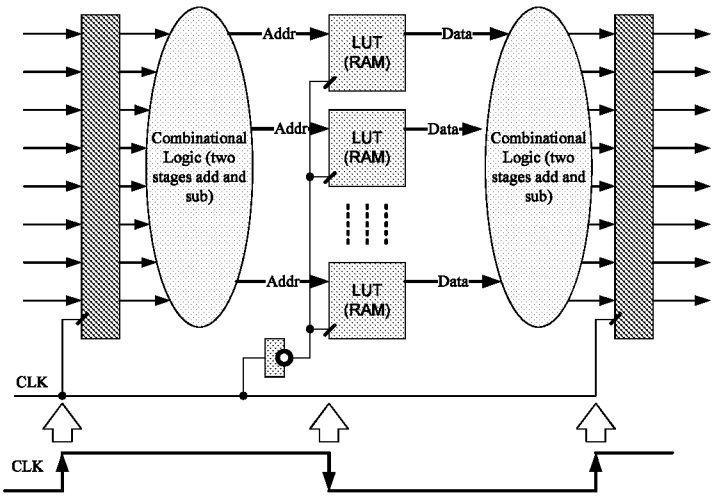


Figure 6

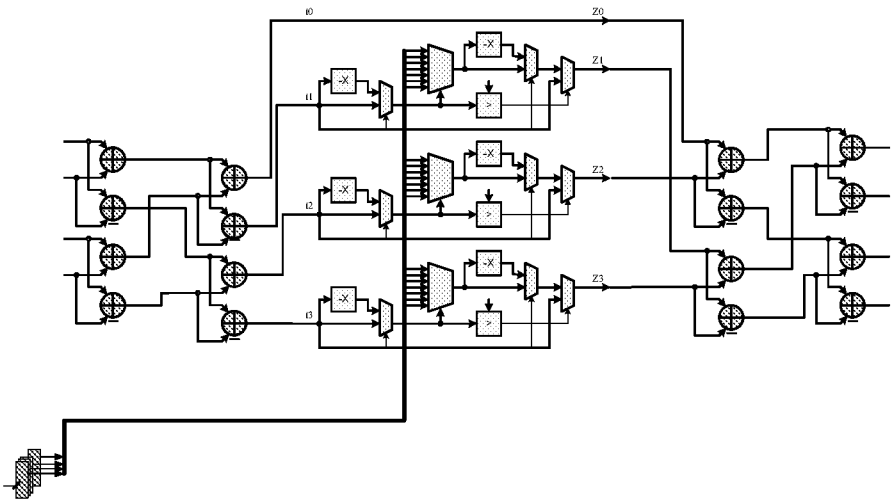


Figure 7

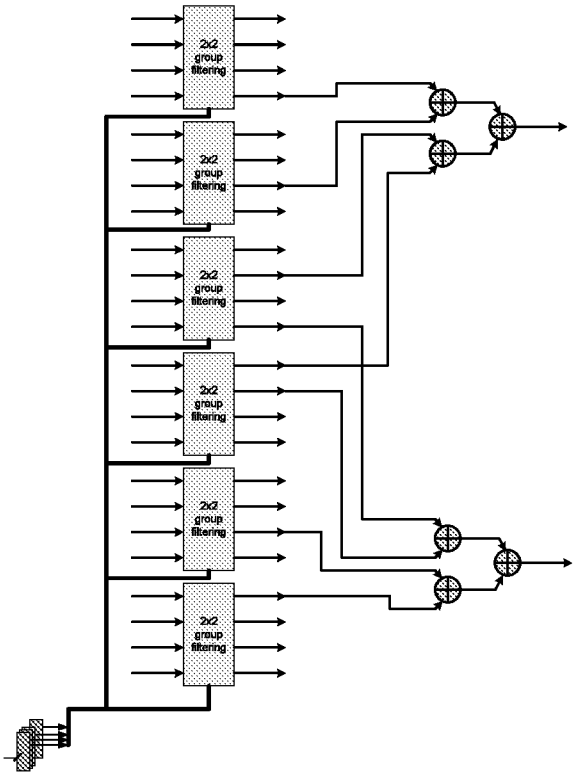


Figure 8

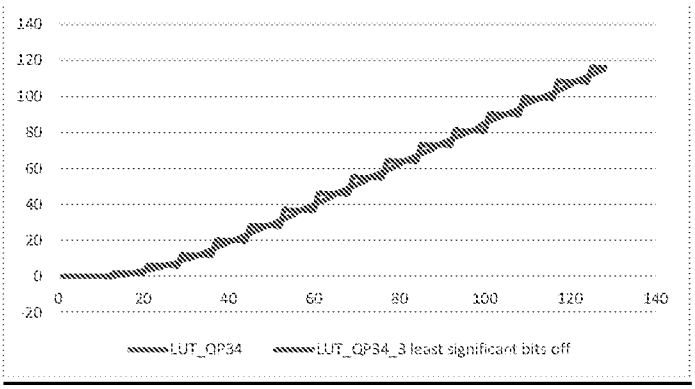


FIG. 9

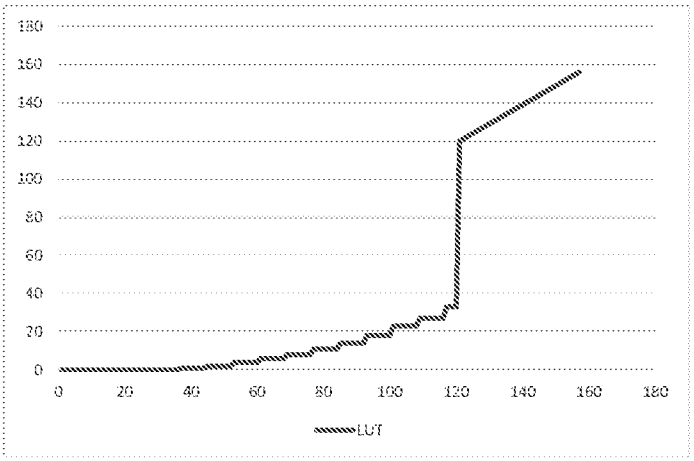

180
160
140
120
100
80
60
40
20
0
0
20
40
60
80
100
120
140
160
180
LUT

FIG. 10

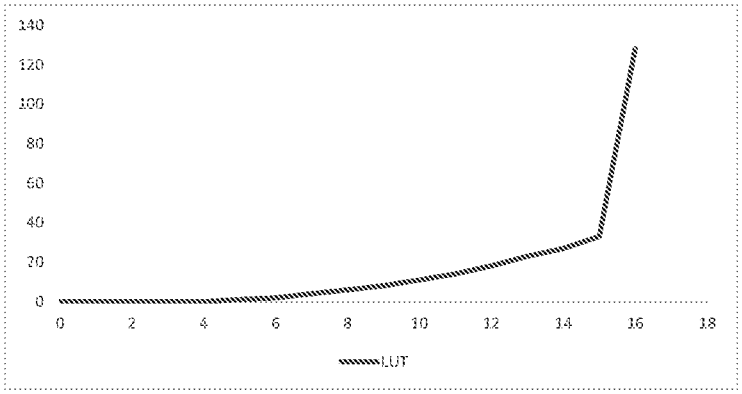

140
120
100
80
60
40
20
0
0 2 4 6 8 10 12 14 16 18
LUT

FIG. 11

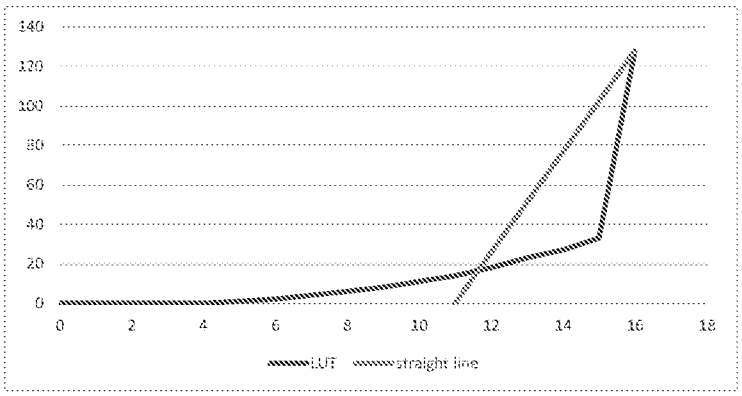

140
120
100
80
60
40
20
0
0 2 4 6 8 10 12 14 16 18
LUT
straight line

FIG. 12

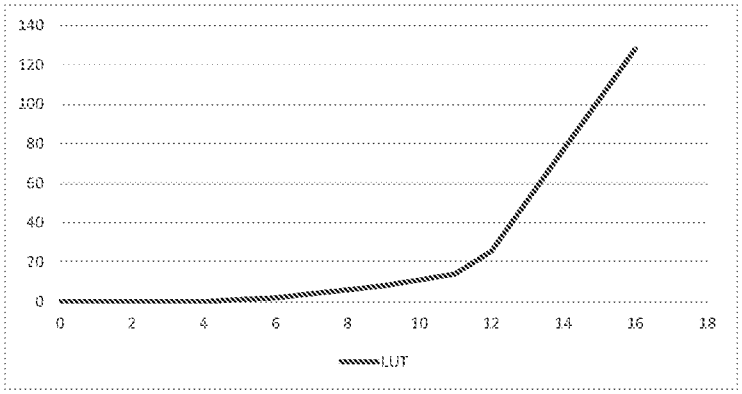

140
120
100
80
60
40
20
0
0
2
4
6
8
10
12
14
16
18
LUT

FIG. 13

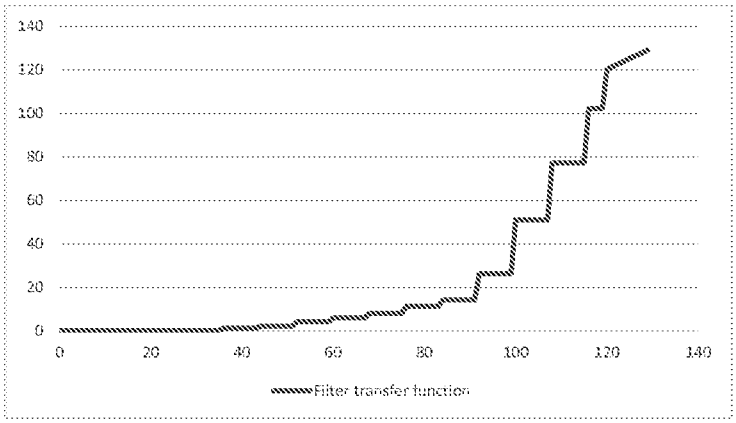

140
120
100
80
60
40
20
0
0
20
40
60
80
100
120
140
Filter transfer function

FIG. 14

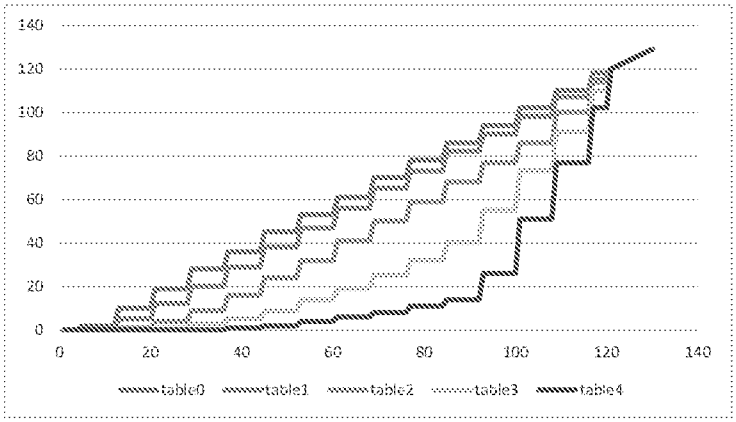

140
120
100
80
60
40
20
0
0
20
40
60
80
100
120
140
table0
table1
table2
table3
table4

FIG. 15

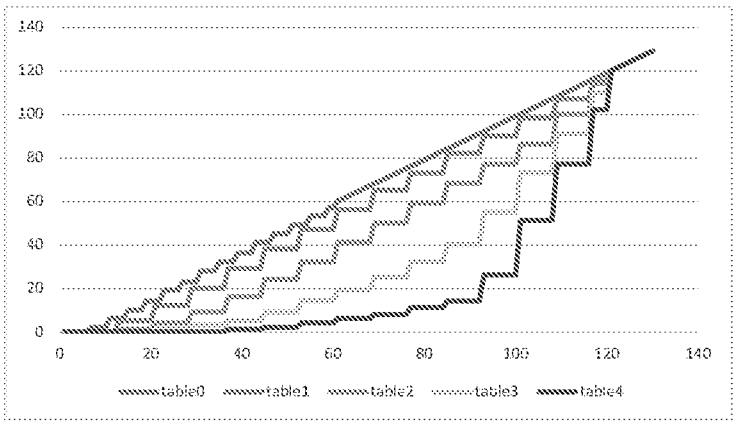

140
120
100
80
60
40
20
0
0
20
40
60
80
100
120
140
table0
table1
table2
table3
table4

FIG. 16

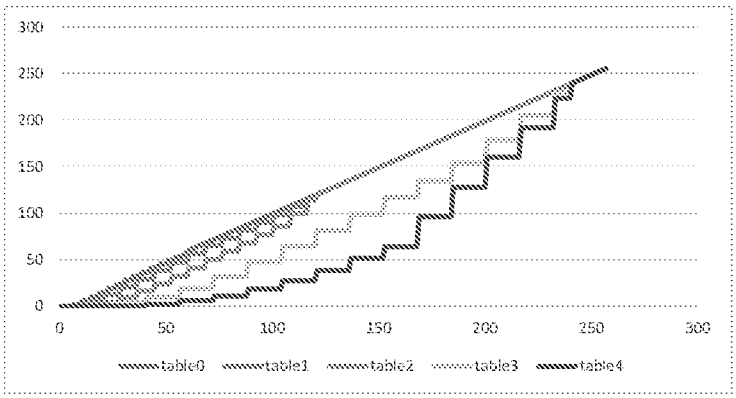

300
250
200
150
100
50
0
0
50
100
150
200
250
300
table0
table1
table2
table3
table4

FIG. 17

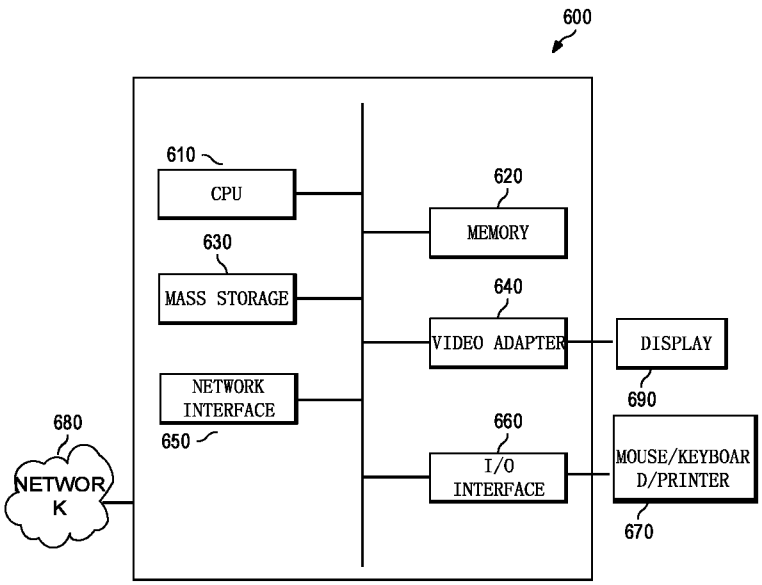
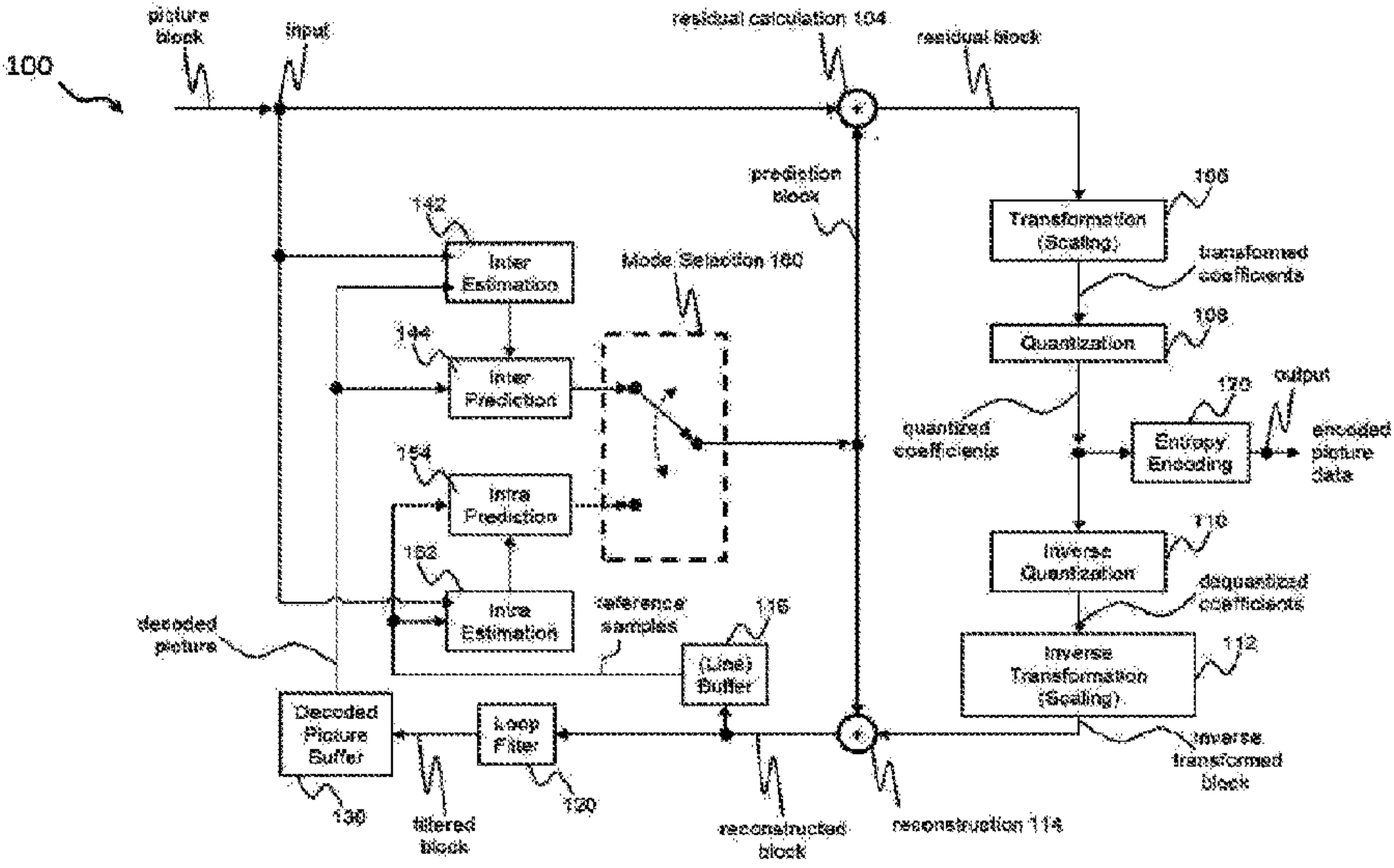

600
610
CPU
620
MEMORY
630
MASS STORAGE
640
VIDEO ADAPTER
DISPLAY
690
NETWORK INTERFACE
650
680
NETWORK
660
I/O INTERFACE
MOUSE/KEYBOARD/PRINTER
670

Fig. 18


100
picture block
input
residual calculation 104
residual block
prediction block
142
Inter Estimation
Mode Selection 160
144
Inter Prediction
154
Intra Prediction
152
Intra Estimation
decoded picture
reference samples
116
(Line) Buffer
Decoded Picture Buffer
130
Loop Filter
120
filtered block
reconstructed block
reconstruction 114
106
Transformation (Scaling)
transformed coefficients
108
Quantization
quantized coefficients
170
Entropy Encoding
output
encoded picture data
110
Inverse Quantization
dequantized coefficients
112
Inverse Transformation (Scaling)
inverse transformed block