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(54) SPUTTERING TARGET, OXIDE SEMICONDUCTING FILM, AND METHOD FOR MAKING THE SAME

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See application file for complete search history.

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(57) ABSTRACT

An oxide semiconductor film includes indium (In), cerium (Ce), zinc (Zn), doping metal element (M) and oxygen (O) elements, and a molar ratio of the In, Ce, and Zn as In:Ce:Zn is in a range of 2:(0.5 to 2):1. A method for making a oxide semiconductor film includes a step of forming an oxide film on a substrate by using a sputtering method and a sputtering target comprising $In_2Ce_xZnO_{4+2x}$, wherein $x=0.5\sim2$.

12 Claims, 7 Drawing Sheets

The In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder are mixed to form a mixture

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The mixture is sintered at a temperature in a range from about 1250°C to about 1650°C

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The In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder are mixed to form a mixture

The mixture is sintered at a temperature in a range from about 1250°C to about 1650°C

FIG. 1

Sputtering an oxide film on a substrate by a sputtering method using the sputtering target

202

Annealing the oxide film in vacuum, nitrogen gas, or noble gas

FIG. 2

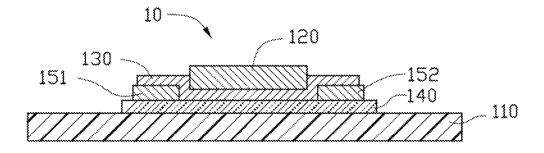


FIG. 3

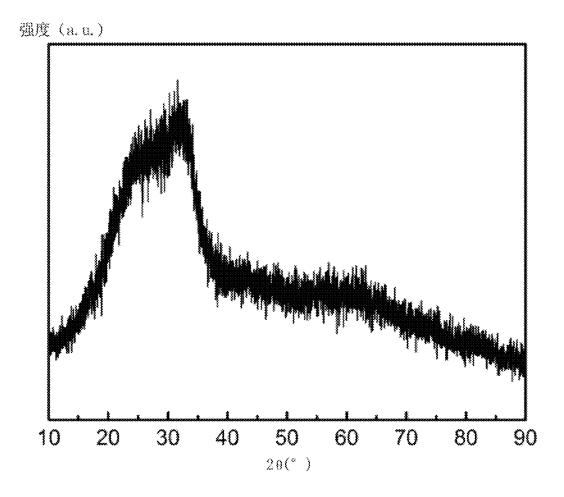


FIG. 4

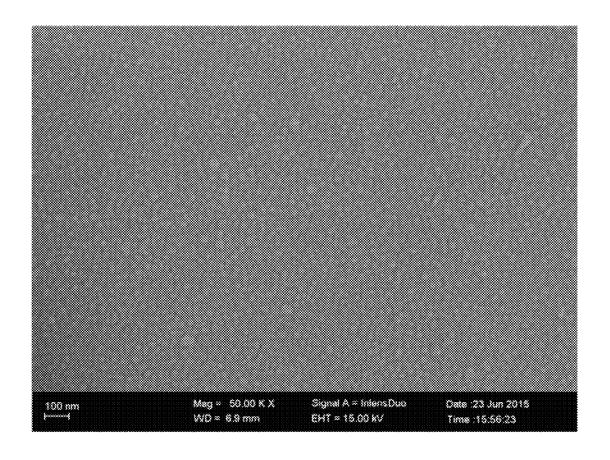


FIG. 5

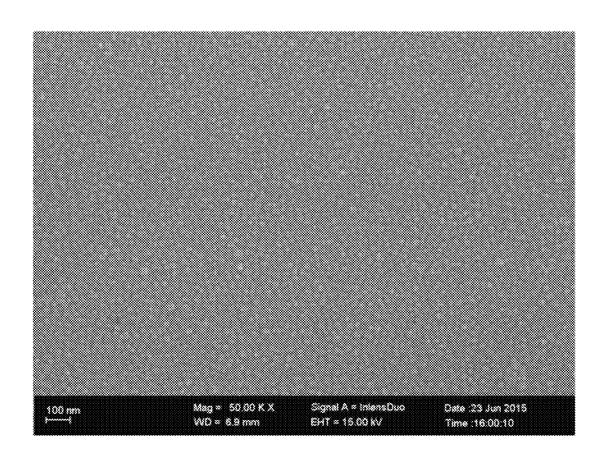


FIG. 6

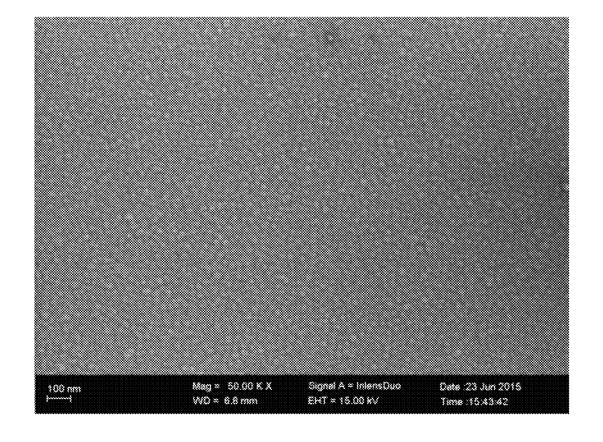


FIG. 7

SPUTTERING TARGET, OXIDE SEMICONDUCTING FILM, AND METHOD FOR MAKING THE SAME

CROSS-REFERENCE TO RELATED APPLICATIONS

This application claims all benefits accruing under 35 U.S.C. §119 from China Patent Application No. 201510477921.1, filed on Aug. 6, 2015, in the China Intellectual Property Office.

FIELD

The present disclosure relates to semiconductor manufacture.

BACKGROUND

Display devices should have high resolution, high 20 response speed, low energy consumption, high transparency, and flexibility. These qualities depend on performances of thin film transistors (TFTs) used in the display devices. An amorphous silicon TFT has a relatively low carrier mobility, large area display. A low temperature poly-silicon (p-Si) TFT can have a high mobility. However, a high cost is incurred in creating a large area display device with the p-Si TFT. Recently, an amorphous oxide semiconductor, indium gallium zinc oxide (InGaZnO₄, or IGZO), has been pro- 30 posed. An IGZO based TFT has a high transparency, a low manufacturing temperature, and a good compatibility with the TFT manufacturing technology.

The mobility of the IGZO TFT is between that of the amorphous silicon TFT and the p-Si TFT, thus improvement $\,^{35}$ is still required. An indium zinc oxide (IZO) device, with high carrier density and a low stability, is not a semiconductor but has a higher carrier mobility than that of IGZO.

BRIEF DESCRIPTION OF THE DRAWINGS

Implementations of the present technology will now be described, by way of example only, with reference to the attached figures.

- FIG. 1 is a flowchart of a method for making a sputtering 45
- FIG. 2 is a flowchart of a method for making an oxide semiconductor film.
 - FIG. 3 is a schematic view of an embodiment of a TFT.
- FIG. 4 shows an X-ray diffraction (XRD) pattern of the 50 oxide semiconductor film in Example 2-1 post.
- FIG. 5 shows a Scanning Electron Microscope (SEM) image of the oxide semiconductor film in Example 2-1.
- FIG. 6 shows an SEM image of the oxide semiconductor film in Example 2-2.
- FIG. 7 shows an SEM image of the oxide semiconductor film in Example 2-3.

DETAILED DESCRIPTION

It will be appreciated that for simplicity and clarity of illustration, where appropriate, reference numerals have been repeated among the different figures to indicate corresponding or analogous elements. In addition, numerous specific details are set forth in order to provide a thorough understanding of the embodiments described herein. However, it will be understood by those of ordinary skill in the

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art that the embodiments described herein can be practiced without these specific details. In other instances, methods, procedures, and components have not been described in detail so as not to obscure the related relevant feature being described. Also, the description is not to be considered as limiting the scope of the embodiments described herein. The drawings are not necessarily to scale and the proportions of certain parts may be exaggerated to better illustrate details and features of the present disclosure.

Several definitions that apply throughout this disclosure will now be presented.

The term "comprise" or "comprising" when utilized, means "include or including, but not necessarily limited to"; it specifically indicates open-ended inclusion or membership in the so-described combination, group, series, and the like. The term "contact" when utilized, means "direct contact" or "physical contact."

One embodiment of a sputtering target comprises an indium cerium zinc oxide represented by formula $In_2Ce_xZnO_{4+2x}$, wherein x=0.5~2, and further comprises a doping metal element (M). In the sputtering target, an atom percentage of M in total metal element (i.e., In+Ce+Zn+M) is in a range from about 0.5% to about 1%.

The sputtering target can be obtained by sintering a which cannot meet the requirements of high resolution and 25 mixture of indium oxide (In₂O₃), cerium oxide (CeO₂), zinc oxide (ZnO), and doping metal oxide. In one embodiment, the sputtering target is obtained by sintering only the mixture of In2O3, CeO2, ZnO, and the doping metal oxide. Impurities may exist in the mixture. Besides In₂O₃, CeO₂, ZnO, and the doping metal oxide, the mixture may only comprise the impurities. The smaller the amount of the impurities in the mixture the better. In one embodiment, the amount of the impurities can be less than 10 ppm.

> The $In_2Ce_xZnO_{4+2x}$ is a crystal (or crystalline solid). The sputtering target can also comprise a non crystalline (amorphous) solid. The amorphous solid comprises indium oxide, cerium oxide, and zinc oxide. In one embodiment, a weight percentage of the crystalline In₂Ce_xZnO_{4+2x} in the sputtering target is above 80%.

> In one embodiment, the sputtering target only comprises In₂Ce_xZnO_{4+2x}, In₂O₃, CeO₂, ZnO, and the doping metal oxide. Besides $In_2Ce_xZnO_{4+2x}$ (and In_2O_3 , CeO_2 , ZnO, the doping metal oxide if have), the sputtering target may only comprise unwanted and trace amounts of impurities. The smaller the amount of impurities in the sputtering target the better. In one embodiment, the amount of impurities in the sputtering target can be less than 10 ppm.

> The doping metal element has a relatively high field strength, low electronegativity, strong combine force to oxygen, and low tendency of forming oxygen vacancy. The doping metal element can be selected from the group consisting of Hf, Mg, Zr, Y, Mo, Sc, Al, Ti, Eu, and combinations thereof.

A relative density of the sputtering target can be larger 55 than or equal to 90%. The relative density is a ratio of a real density of the sputtering target to a theoretical density of $In_2Ce_xZnO_{4+2}$.

A bulk resistance of the sputtering target can be in a range from about $10^{-2} \Omega$ cm to about 10 Ω cm.

An average surface roughness of the sputtering target can be less than or equal to 2 microns, and in one embodiment be less than or equal to 0.5 microns.

An average flexural strength of the sputtering target can be larger than or equal to 50 MPa, and in one embodiment be larger than or equal to 55 MPa.

FIG. 1 presents a flowchart in accordance with an illustrated example embodiment. The embodiment of a method

100 for making the sputtering target is provided by way of example, as there are a variety of ways to carry out the method 100. Each block shown in FIG. 1 represents one or more processes, methods, or subroutines carried out in the exemplary method 100. Additionally, the illustrated order of 5 blocks is by example only and the order of the blocks can be changed. The exemplary method 100 can begin at block 101. Depending on the embodiment, additional steps can be added, others removed, and the ordering of the steps can be changed.

At block 101, the In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder are mixed to form a mixture. In the mixture, a molar ratio of indium (In), cerium (Ce), and zinc (Zn) as In:Ce:Zn is substantially 2:(0.5 to 2):1.

At block 102, the mixture is sintered at a temperature in a range from about 1250° C. to about 1650° C.

In the mixture, an atom percentage of doping metal element (M) in total metal element (i.e., In+Ce+Zn+M) is in a range from about 0.5% to about 1%.

In the mixture, the In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder can respectively have an average diameter that is less than or equal to 10 microns. In one embodiment, the average diameter of the In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder can respectively be in a range from about 0.5 microns to about 2 microns.

The purity of the In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder can be 3N (99.9 mass %) to 5N (99.999%).

A molar ratio of the In₂O₃ powder, CeO₂ powder, and ZnO powder as In₂O₃:CeO₂:ZnO is substantially 2:(1 to 4):2.

The doping metal oxide can be selected from HfO₂, MgO, ZrO₂, Y₂O₃, MoO₃, Sc₂O₃, Al₂O₃, TiO₂, Eu₂O₃, and com- 35 binations thereof.

The In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder can be mixed in air or in a protective atmosphere (e.g., in argon (Ar) or nitrogen (N2) gas). The and doping metal oxide powder can further comprise steps of: ball milling the In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder together in a liquid medium to form a mixed substance; drying the mixed substance to remove the liquid medium to obtain the mixture 45 only substance in the oxide semiconductor film beyond the that is dry. The liquid medium is not reactive with the In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder and can be removed from the wet mixed substance by a drying step, and brings no impurity substance into the mixture. The liquid medium can be water, ethanol, acetone, 50 or combinations thereof. The ball milling can take place in a ball milling machine. The liquid medium, In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder are introduced into the ball milling machine. A rotating speed of the ball milling machine can be about 100 rpm to 55 about 50 nm to about 1000 nm. about 600 rpm. During the ball milling, the In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder mix uniformly, the particle diameters of the powders can decrease, and the specific surface area of the particles of the powders can increase. The ball milling can last until the 60 In₂O₃ powder, CeO₂ powder, ZnO powder, and doping metal oxide powder are uniformly mixed and the particle diameters of the powders have decreased to the desired sizes. The mixed substance is taken out from the ball milling machine and dried, for example at about 30° C. to about 60° C., to 65 remove the liquid medium. The mixed substance can be dried in air or a protective atmosphere (e.g., Ar gas or N₂

gas). In one embodiment, the mixed substance is dried at a high purity (3N to 5N) of protective gas.

The mixture can be sintered in a protective atmosphere (e.g., in argon (Ar) or nitrogen (N_2) gas).

The mixture can be molded or pressed into a desired shape before or during the sintering step. During the sintering step, the In₂O₃ powder, CeO₂ powder, and ZnO powder react to form the crystalline solid $In_2Ce_xZnO_{4+2x}$. The doping metal oxide is doped in the crystalline In₂Ce_xZnO_{4+2x}.

A hot pressing method or a hot isostatic pressing (HIP) method can be used to simultaneously mold/press and sinter the mixture. The hot pressing applies a pressure of about 30 MPa to 100 MPa at the temperature of about 1250° C. to about 1650° C. for about 1 hour to about 24 hours. The hot isostatic pressing applies a pressure of about 100 MPa to 300 MPa at the temperature of about 1250° C. to about 1650° C. for about 1 hour to about 40 hours.

When the sintering step takes place after the molding/ 20 pressing step, the molding/pressing step can be processed by using a cold pressing method or a cold isostatic pressing method. The mixture can be filled into a mold and molded/ pressed by applying a pressure of about 30 MPa to about 300 MPa to form the desired shape. The mixture with the desired shape can be sintered under normal pressure.

The sputtering target can be obtained directly from the sintering step. In another embodiment, the product of the sintering step can be shaped or polished to form the sputtering target.

One embodiment of an oxide semiconductor film comprises In, Ce, Zn, doping metal element (M) and O elements, wherein a molar ratio of In, Ce, and Zn as In:Ce:Zn is 2:(0.5 to 2):1, and an atom percentage of M in total metal element (i.e., In+Ce+Zn+M) is in a range from about 0.5% to about 1%. The oxide semiconductor film is an n-type semiconductor having a carrier density of about 10¹² cm⁻³ to about 10²⁰ cm⁻³, and a carrier mobility of about 5.0 cm² V⁻¹ s⁻¹ to about $46.3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$.

The oxide semiconductor film can be an amorphous solid. mixing of the In₂O₃ powder, CeO₂ powder, ZnO powder, 40 In another embodiment, the oxide semiconductor film can also comprise crystalline solid In₂Ce_xZnO₄₊₂,

> M can be selected from the group consisting of Hf, Mg, Zr, Y, Mo, Sc, Al, Ti, Eu, and combinations thereof.

> In one embodiment, unwanted and trace impurities are the elements of In, Ce, Zn, M and O. The smaller the amount of impurities in the sputtering target the better. In one embodiment, the amount of impurities in the oxide semiconductor film can be less than 10 ppm.

> The oxide semiconductor film can have a band gap of about 3.0 eV to about 3.5 eV.

> The oxide semiconductor film can have a visible light transmittance of about 60% to about 90%.

> The oxide semiconductor film can have a thickness of

The oxide semiconductor film can have a carrier density of about 10^{13} cm^{-3} to about 10^{15} cm^{-3} .

The oxide semiconductor film can have a carrier mobility of about 12.3 cm 2 V $^{-1}$ s $^{-1}$ to about 46.3 cm 2 V $^{-1}$ s $^{-1}$

The oxide semiconductor film can be obtained by a sputtering method using the sputtering target as described

FIG. 2 reveals one embodiment of a method 200 for making the oxide semiconductor film, comprising a step of sputtering an oxide film on a substrate by the sputtering method using the sputtering target as described above, as shown in block 201.

The sputtering method can be a DC (direct current) sputter method, an AC (alternating current) sputter method, an RF (radio frequency) sputter method, a magnetron sputter method, or a medium frequency magnetron sputter method. The current of the sputtering can be about 0.1 A to about 2.0 5 A. The sputtering can take place for about 1 minute to about 120 minutes.

The sputtering can be at room temperature or a high temperature (e.g., smaller than or equal to 400° C.). When the high temperature is used in the sputtering, the method 10 **200** further comprises a step of preheating the substrate. The substrate can be previously heated in vacuum at a temperature of about 50° C. to about 400° C.

A carrier gas is introduced into the sputtering chamber during the sputtering. The carrier gas can be a noble gas, a noble gas and oxygen gas, or a mixture of a noble gas and hydrogen gas. In one embodiment, the noble gas can be Ar gas. For example, the carrier gas can be a mixture of Ar gas and oxygen gas. A flow rate of the oxygen gas can be less than 3 sccm. A purity of the carrier gas can be a solution gas can be a noble gas, a noble gas and oxygen gas. In one embodiment, the noble gas can be Ar gas. For example, the carrier gas can be a mixture of Ar gas and oxygen gas. A flow rate of the oxygen gas can be a solution gas can be a solution gas can be a noble gas, a noble gas and hydrogen gas. In one embodiment, the noble gas can be Ar gas. For example, the carrier gas can be a mixture of Ar gas and oxygen gas. A flow rate of the oxygen gas can be a noble gas, a noble gas and hydrogen gas. In one embodiment, the noble gas can be Ar gas. For example, the carrier gas can be a conventional except the semiconducting layer 140, the source electrode 152. The gate electrode 120 is instituted from the semiconducting layer 140, the source electrode 152 by the insulating layer 130. The semiconducting layer 140 can be the above described oxide semiconductor film. The thin film transistor 10 can be conventional except the semiconducting layer 140 can be the above described oxide semiconductor film. The thin film transistor 10 can be conventional except the semiconducting layer 140, the source electrode 152 by the insulating layer 130.

The pressure in the sputtering chamber during the sputtering can be about 0.1 Pa to about 2.0 Pa.

The substrate can be an insulating substrate capable of enduring the sputtering temperature. Glass, silicon, or polymer (PET, PI, PE, etc.) can be used as the substrate. When the sputtering is at a relatively low temperature (e.g., at room temperature), the choice of the substrate material is wide.

Before the sputtering, the substrate can be previously cleaned to remove impurities on the surface of the substrate. 30

Before the sputtering, the sputtering target can be fixed on a support. An outer surface of the sputtering target can be parallel to the surface of the substrate. In another embodiment, an angle can be formed between the outer surface of the sputtering target and the surface of the substrate, the 35 angle can be 20° to 85°. A distance between the outer surface of the sputtering target and the surface of the substrate can be smaller than or equal to 8 cm.

The oxide film formed on the surface of the substrate in block 201 can be directly used as the oxide semiconductor 40 film. In another embodiment, the method can also comprise the annealing step shown in block 202.

In block 202, the oxide film can be annealed in a vacuum, or in an $\rm N_2$ or noble gas (e.g., Ar gas) atmosphere. The background vacuum used in the annealing can be about 10^{-3} 45 Pa to about 10 Pa. The annealing temperature can be in a range from about 100° C. to about 400° C. A speed of temperature increase can be in a range from about 1° C./min to about 20° C./min to increase the temperature of the oxide film to the annealing temperature. The oxide film can be 50 annealed for about 1 hour to about 10 hours. The annealing step can slightly increase the crystallization of the oxide film to adjust the performance of the oxide semiconductor film.

The carrier density of the n-type oxide semiconductor film can be about $10^{12}~\rm cm^{-3}$ to about $10^{20}~\rm cm^{-3}$, and the carrier 55 mobility of the n-type oxide semiconductor film can be about $5.0~\rm cm^2~\rm V^{-1}~\rm s^{-1}$ to about $46.3~\rm cm^2~\rm V^{'1}~\rm s^{-1}$. The n-type oxide semiconductor film can be used in an n-type TFT. The amount of Ce in the oxide semiconductor film cannot be too large or too small. When x<0.5, the oxide semiconductor 60 film has properties similar to those of IZO, which has a relatively low stability, and thus not proper as a TFT semiconductor. When x>2, the oxide semiconductor film has a relatively low carrier mobility that is not suitable for high resolution display devices. The doping metal element (M) is 65 introduced to further decrease the oxygen vacancy in the oxide semiconductor film, thus further improves the stability

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of the device using the oxide semiconductor film, such as the TFT, especially the stabilities under electrical bias and light illumination.

One embodiment of a semiconducting device comprises the above described oxide semiconductor film.

Referring to FIG. 3, one embodiment of a thin film transistor 10 comprises an insulating substrate 110, a semi-conducting layer 140, a source electrode 151, a drain electrode 152, a gate electrode 120, and a insulating layer 130. The source electrode 151 and the drain electrode 152 are spaced from each other. The semiconducting layer 140 is electrically connected between the source electrode 151 and the drain electrode 152. The gate electrode 120 is insulated from the semiconducting layer 140, the source electrode 151, and the drain electrode 152 by the insulating layer 130. The semiconducting layer 140 can be the above described oxide semiconductor film. The thin film transistor 10 can be conventional except the semiconducting layer. The thin film transistor 10 can be a top gate structure as shown in FIG. 3, or a bottom gate structure.

EXAMPLE 1

Sputtering Target

EXAMPLE 1-1-1

209 g of $\rm In_2O_3$ powder, 260 g of $\rm CeO_2$ powder and 61 g of ZnO powder with a molar ratio as $\rm In_2O_3$: $\rm CeO_2$:ZnO is 1:2:1, and $\rm HfO_2$ powder all having purities of 4N are mixed in a ball milling machine having water as the liquid medium, at a rotating speed of about 200 rpm for about 10 hours. The atom percentage of Hf in total metal element (i.e., In+Ce+Zn+Hf) is 0.5%. After that, the mixed substance is dried at a pressure of about 1 atm in an Ar gas atmosphere (5N) for about 1 hour to remove the water. The mixture is hot pressed in Ar gas atmosphere at a pressure of about 50 MPa and a temperature of about 1350° C. for about 5 hours with a speed of temperature increase of about 15° C./min. The obtained sputtering target has a relative density >87% and a bulk resistance of 0.70 $\rm \Omega cm$ -0.86 $\rm \Omega cm$.

EXAMPLE 1-1-2

This example is the same as Example 1-1-1 except that the HfO₂ powder is replaced with ZrO₂. The atom percentage of Zr in total metal element (i.e., In+Ce+Zn+Zr) is 0.5%.

EXAMPLE 1-2-1

249 g of In₂O₃ powder, 231 g of CeO₂ powder and 73 g of ZnO powder with a molar ratio as In₂O₃:CeO₂:ZnO is 2:3:2, and HfO₂ powder all having purities of 4N are mixed in a ball milling machine having ethanol as the liquid medium at a rotating speed of about 400 rpm for about 20 hours. The atom percentage of Hf in total metal element (i.e., In+Ce+Zn+Hf) is 0.5%. After that, the mixed substance is dried at a pressure of about 1 atm in an Ar gas atmosphere (5N) for about 1 hour to remove the ethanol. The mixture is placed in a mold and cold pressed at a pressure of about 75 MPa for about 60 minutes. The molded mixture is sintered at a normal pressure in N2 gas atmosphere (5N) at a temperature of about 1450° C. for about 8 hours with a speed of temperature increase of about 10° C./min. The obtained sputtering target has a relative density >85% and a bulk resistance of 0.12 Ωcm~0.34 Ωcm.

EXAMPLE 1-2-2

This example is the same as Example 1-2-1 except that the HfO_2 powder is replaced with ZrO_2 . The atom percentage of Zr in total metal element (i.e., In+Ce+Zn+Zr) is 0.5%.

EXAMPLE 1-3-1

209 g of In₂O₃ powder, 260 g of CeO₂ powder and 61 g of ZnO powder with a molar ratio as In₂O₃:CeO₂:ZnO is 1:2:1, and HfO₂ powder all having purities of 4N are mixed in a ball milling machine having water as the liquid medium at a rotating speed of about 500 rpm for about 10 hours. The atom percentage of Hf in total metal element (i.e., In+Ce+ In+Hf) is 0.5%. After that, the mixed substance is dried at a pressure of about 1 atm in an Ar gas atmosphere (5N) for about 1 hour to remove the water. The mixture is hot isostatically pressed in Ar gas atmosphere (5N) at a pressure of about 100 MPa and a temperature of about 1450° C. for about 20 hours with a speed of temperature increase of about 10° C./min. The obtained sputtering target has a relative density >86% and a bulk resistance of 0.65 Ωcm~0.78 Ωcm. 25

EXAMPLE 1-3-2

This example is the same as Example 1-3-1 except that the HfO₂ powder is replaced with ZrO₂. The atom percentage of 30 The glass substrate is cleaned and dried by N₂ gas blowing. The glass substrate and the sputtering target of Example Zr in total metal element (i.e., In+Ce+Zn+Zr) is 0.5%.

EXAMPLE 2

Oxide Semiconductor Film

EXAMPLE 2-1-1

A glass substrate is cleaned and dried by N_2 gas blowing. The glass substrate and the sputtering target of Example 40 1-1-1 are disposed in a sputtering chamber. The surfaces of the sputtering target and the substrate are parallel to each other about 8 cm distant. The carrier gas, which is Ar gas in this example, is introduced into the sputtering chamber. The 45 Ar gas has a flow rate of about 40 sccm. The pressure in the sputtering chamber is about 0.7 Pa. The sputtering is at room temperature by using a current of about 1.0 A for about 28 minutes to form the oxide semiconductor film with a thickness of about 250 nm. The XRD pattern of the oxide semiconductor film is shown in FIG. 4 showing that the oxide semiconductor films are amorphous films. The surface morphology of the oxide semiconductor film is shown in FIG. 5.

EXAMPLE 2-1-2

This example is the same as Example 2-1-1 except that the sputtering target is replaced with Example 1-1-2.

EXAMPLE 2-1-3

This example is the same as Example 2-1-1 except that the carrier gas is a combination of Ar gas and $\rm O_2$ gas. The $\rm O_2$ gas has a flow rate of about 0.5 sccm.

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EXAMPLE 2-1-4

This example is the same as Example 2-1-2 except that the carrier gas is a combination of Ar gas and O_2 gas. The O_2 gas has a flow rate of about 0.5 sccm.

EXAMPLE 2-1-5

This example is the same as Example 2-1-1 except that the 10 O_2 gas has a flow rate of about 1.0 sccm.

EXAMPLE 2-1-6

This example is the same as Example 2-1-2 except that the O_2 gas has a flow rate of about 1.0 sccm.

Hall tests are conducted on the oxide semiconductor films of Examples 2-1, and reveal that the Hall mobility of the oxide semiconductor films are about 13.5 cm 2 V $^{-1}$ s $^{-1}$ to about 26.3 cm 2 V $^{-1}$ s $^{-1}$, and the carrier densities of the oxide semiconductor films are about 10^{13} cm $^{-3}$ to about 10^{20} cm $^{-3}$. The Hall mobility and carrier densities of the oxide semiconductor films of Examples 2-1 change with the O_2 gas flow rates are shown in Table 1.

EXAMPLE 2-2-1

A glass substrate is cleaned and dried by N₂ gas blowing.

The glass substrate and the sputtering target of Example 1-2-1 are disposed in a sputtering chamber. The surfaces of the sputtering target and the substrate are parallel to each other at about 8 cm distant. The substrate is preheated to about 250° C. The carrier gas, which is Ar gas in this example, is introduced into the sputtering chamber. The Ar gas has a flow rate of about 40 sccm. The pressure in the sputtering chamber is about 0.7 Pa. The sputtering is at 250° C. by using a current of about 1.0 A for about 28 minutes to form the oxide semiconductor film with a thickness of about 250 nm. The surface morphology of the oxide semiconductor film is shown in FIG. 6.

EXAMPLE 2-2-2

This example is the same as Example 2-2-1 except that the sputtering target is replaced with Example 1-2-2.

EXAMPLE 2-2-3

This example is the same as Example 2-2-1 except that the carrier gas is a combination of Ar gas and O_2 gas. The O_2 gas has a flow rate of about 1 sccm.

EXAMPLE 2-2-4

This example is the same as Example 2-2-2 except that the carrier gas is a combination of Ar gas and O_2 gas. The O_2 gas has a flow rate of about 1 sccm.

EXAMPLE 2-2-5

This example is the same as Example 2-2-1 except that the O_2 gas has a flow rate of about 2 sccm.

EXAMPLE 2-2-6

This example is the same as Example 2-2-2 except that the O_2 gas has a flow rate of about 2 sccm.

Hall tests are conducted on the oxide semiconductor films oxide semiconductor films are about 18.7 cm 2 V $^{-1}$ s $^{-1}$ to about 46.3 cm 2 V $^{-1}$ s $^{-1}$, and the carrier densities of the oxide semiconductor films are about 10^{15} cm $^{-3}$ to about 10^{20} cm $^{-3}$. The Hall mobility and carrier densities of the oxide semi- conductor films of Examples 2-2 change with the $\rm O_2$ 10 gas flow rates are shown in Table 1.

EXAMPLE 2-3-1

A glass substrate is cleaned and dried by N₂ gas blowing. The glass substrate and the sputtering target of Example 1-3-1 are disposed in a sputtering chamber. The surfaces of the sputtering target and the substrate are parallel to each other at about 8 cm distant. The carrier gas, which is Ar gas in this example, is introduced into the sputtering chamber. The Ar gas has a flow rate of about 40 sccm. The pressure in the sputtering chamber is about 0.7 Pa.

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EXAMPLE 2-3-5

This example is the same as Example 2-3-1 except that the annealing temperature is about 350° C.

EXAMPLE 2-36

This example is the same as Example 2-3-2 except that the annealing temperature is about 350° C.

Hall tests are conducted on the oxide semiconductor films of Examples 2-3 before and after the annealing step. Before being annealed, the Hall mobility of the oxide semiconductor films reach about 20.3 cm² V⁻¹ s⁻¹, and the carrier densities of the oxide semiconductor films reach about 10²⁰ cm⁻³. After being annealed, the Hall mobility of the oxide semiconductor films are about 15.5 cm² V⁻¹ s⁻¹ to about 23.7 cm² V⁻¹ s⁻¹, and the carrier densities of the oxide semiconductor films are about 10¹⁴ cm⁻³ to about 10¹⁹ cm⁻³. The Hall mobility and carrier densities of the oxide semiconductor films of Examples 2-3 change with the annealing temperatures are shown in Table 1.

TABLE 1

Example	In:Ce:Zn	Doping metal element	O ₂ gas flow rate (sccm)	annealing temperature (° C.)	Hall mobility $(cm^2V^{-1}s^{-1})$	carrier density (cm ⁻³)
2-1-1	2:2:1	Hf	0		20.3	5.21×10^{18}
2-1-2	2:2:1	Zr	0	_	19.1	3.75×10^{18}
2-1-3	2:2:1	$_{ m Hf}$	0.5	_	26.3	1.25×10^{20}
2-1-4	2:2:1	Zr	0.5	_	25.7	1.12×10^{20}
2-1-5	2:2:1	Hf	1	_	15.6	1.05×10^{14}
2-1-6	2:2:1	Zr	1	_	13.5	8.72×10^{13}
2-2-1	4:3:2	Hf	0	_	32.4	5.27×10^{19}
2-2-2	4:3:2	Zr	0	_	29.7	3.56×10^{19}
2-2-3	4:3:2	Hf	1	_	46.3	6.63×10^{20}
2-2-4	4:3:2	Zr	1	_	43.1	5.16×10^{20}
2-2-5	4:3:2	Hf	2	_	20.2	2.11×10^{16}
2-2-6	4:3:2	Zr	2	_	18.7	3.42×10^{15}
2-3-1	2:2:1	Hf	0	150	23.7	1.08×10^{19}
2-3-2	2:2:1	Zr	0	150	22.6	8.81×10^{18}
2-3-3	2:2:1	Hf	0	250	19.7	2.79×10^{17}
2-3-4	2:2:1	Zr	0	250	18.5	9.34×10^{16}
2-3-5	2:2:1	Hf	0	350	16.1	5.41×10^{14}
2-3-6	2:2:1	Zr	0	350	15.5	3.17×10^{14}

The sputtering is at room temperature by using a current of about 1.0 A for about 28 minutes to form the oxide semiconductor film with a thickness of about 250 nm. The oxide semiconductor film is then annealed an atmospheric pressure of 1 Pa for about 1 hour. The annealing temperature is about 150° C. The surface morphology of the oxide semiconductor film is shown in FIG. 7.

EXAMPLE 2-3-2

This example is the same as Example 2-3-1 except that the sputtering target is replaced with Example 1-3-2.

EXAMPLE 2-3-3

This example is the same as Example 2-3-1 except that the annealing temperature is about 250° C.

EXAMPLE 2-3-4

This example is the same as Example 2-3-2 except that the annealing temperature is about 250° C.

Depending on the embodiment, certain of the steps of methods described may be removed, others may be added, and the sequence of steps may be altered. It is also to be understood that the description and the claims drawn to a method may comprise some indication in reference to certain steps. However, the indication used is only to be viewed for identification purposes and not as a suggestion as to an order for the steps.

The embodiments shown and described above are only examples. Even though numerous characteristics and advantages of the present technology have been set forth in the foregoing description, together with details of the structure and function of the present disclosure, the disclosure is illustrative only, and changes may be made in the detail, especially in matters of shape, size, and arrangement of the parts within the principles of the present disclosure, up to and including the full extent established by the broad general meaning of the terms used in the claims. It will therefore be appreciated that the embodiments described above may be modified within the scope of the claims.

What is claimed is:

- 1. An oxide semiconductor film comprising indium (In), cerium (Ce), zinc (Zn), doping metal element (M) and oxygen (O) elements, a molar ratio of In, Ce, and Zn as In:Ce:Zn is in a range of 2:(0.5 to 2):1, a carrier density of 5 about $10^{12}~\rm cm^{-3}$ to about $10^{20}~\rm cm^{-3}$, and a carrier mobility of about $5.0~\rm cm^{2}V^{-1}s^{-1}$ to about $5.0~\rm cm^{2}V^{-1}s^{-1}$, and the oxide semiconductor film is an n-type semiconductor.
- 2. The oxide semiconductor film of claim 1 being an amorphous film.
- 3. The oxide semiconductor film of claim 1, wherein an atom percentage of M in total metal element is in a range from about 0.5% to about 1%.
- **4**. The oxide semiconductor film of claim **1**, wherein M is ¹⁵ selected from the group consisting of Hf, Mg, Zr, Y, Mo, Sc, Al, Ti, Eu, and combinations thereof.
- 5. A sputtering target comprising an oxide represented by $In_2Ce_xZnO_{4+2x}$, wherein x=0.5~2, and further comprising a doping metal element (M).
- **6**. The sputtering target of claim **5**, wherein an atom percentage of M in total metal element is in a range from about 0.5% to about 1%.
- 7. The sputtering target of claim 5, wherein M is selected 25 from the group consisting of Hf, Mg, Zr, Y, Mo, Sc, Al, Ti, Eu, and combinations thereof.

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- **8**. A method for making a sputtering target comprising: mixing indium oxide (In₂O₃) powder, cerium oxide (CeO₂) powder, zinc oxide (ZnO) powder, and doping metal oxide powder to form a mixture, a molar ratio of indium (In), cerium (Ce), and zinc (Zn) as In:Ce:Zn in the mixture is 2:(0.5 to 2):1; and
- sintering the mixture at a temperature in a range from about 1250° C. to about 1650° C.
- 9. The method of claim 8, wherein the doping metal oxide powder is selected from HfO_2 , MgO, ZrO_2 , Y_2O_3 , MoO_3 , Sc_2O_3 , Al_2O_3 , TiO_2 , Eu_2O_3 , and combinations thereof.
- 10. The method of claim 8, wherein the sintering comprises using a hot pressing method or a hot isostatic pressing method to simultaneously press and sinter the mixture, the hot pressing method comprises applying a pressure of about 30 MPa to 100 MPa at the temperature of about 1250° C. to about 1650° C. for about 1 hour to about 24 hours to the mixture, the hot isostatic pressing method comprises applying a pressure of about 100 MPa to 300 MPa at the temperature of about 1250° C. to about 1650° C. for about 1 hour to about 40 hours to the mixture.
- 11. The method of claim 8 further comprising pressing the mixture into a desired shape before the sintering, and the pressing comprising applying a pressure of about 30 MPa to about 300 MPa to the mixture.
- 12. The method of claim 8, wherein the sintering takes place in a protective gas.

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