

SUPPLEMENTARY EUROPEAN SEARCH REPORT

Application Number EP 11 81 2914

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CLAIMS INCURRING FEES
The present European patent application comprised at the time of filing claims for which payment was due.
Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s):
No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due.
LACK OF UNITY OF INVENTION
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:
see sheet B
All further search fees have been paid within the fixed time limit. The present (supplementary) European search report has been drawn up for all claims.
As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.
Only part of the further search fees have been paid within the fixed time limit. The present (supplementary) European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
None of the further search fees have been paid within the fixed time limit. The present (supplementary) European search report has been drawn up for those parts of the European patent application which relate to the first mentioned in the claims, namely claims: 1-4, 11





LACK OF UNITY OF INVENTION SHEET B

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The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-4, 11

Formation of 3DICs: implant a doped layer in donor wafer before layer transfer, and form transistor gates after layer transfer

2. claims: 5, 6, 14

Form different 3DICs with common masks

3. claim: 7

Concurrent patterning of two monocrystalline layers

4. claims: 8, 12

Thermal conduction path, e. g. comprising through-layer-vias, from a second device layer in a 3DIC to a heat sink

5. claims: 9, 10

Partitioning in 3D semiconductor device comprising logic circuits

6. claims: 13, 15

3DIC with three metal layers between two device layers, the third metal layer having a tighter pitch than a second metal layer

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

15-03-2017

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