



US00D467930S

(12) **United States Design Patent** (10) **Patent No.:** **US D467,930 S**  
**Hwang** (45) **Date of Patent:** **\*\* Dec. 31, 2002**

(54) **RAISER USED IN STACKED GIGABYTE INTERFACE CONVERTER GUIDE RAIL ASSEMBLY FOR REMOVABLE OPTOELECTRONIC MODULES**

5,963,681 A \* 10/1999 Clements ..... 385/14  
6,396,992 B1 \* 5/2002 Debal ..... 385/135  
6,415,091 B1 \* 7/2002 Daub et al. .... 385/135

\* cited by examiner

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(\*\*) Term: **14 Years**

(57) **CLAIM**

(21) Appl. No.: **29/145,371**

The ornamental design for a raiser used in stacked gigabyte interface converter guide rail assembly for removable optoelectronic modules, as shown and described.

(22) Filed: **Jul. 20, 2001**

**DESCRIPTION**

(51) **LOC (7) Cl.** ..... **14-02**

FIG. 1 is a perspective view of a raiser used in stacked gigabyte interface converter guide rail assembly for removable optoelectronic modules showing my new design;

(52) **U.S. Cl.** ..... **D14/447**

FIG. 2 is a front view thereof;

(58) **Field of Search** ..... D14/432, 433,

FIG. 3 is a rear view thereof;

D14/439, 447, 448; 385/134, 135, 901, 147

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

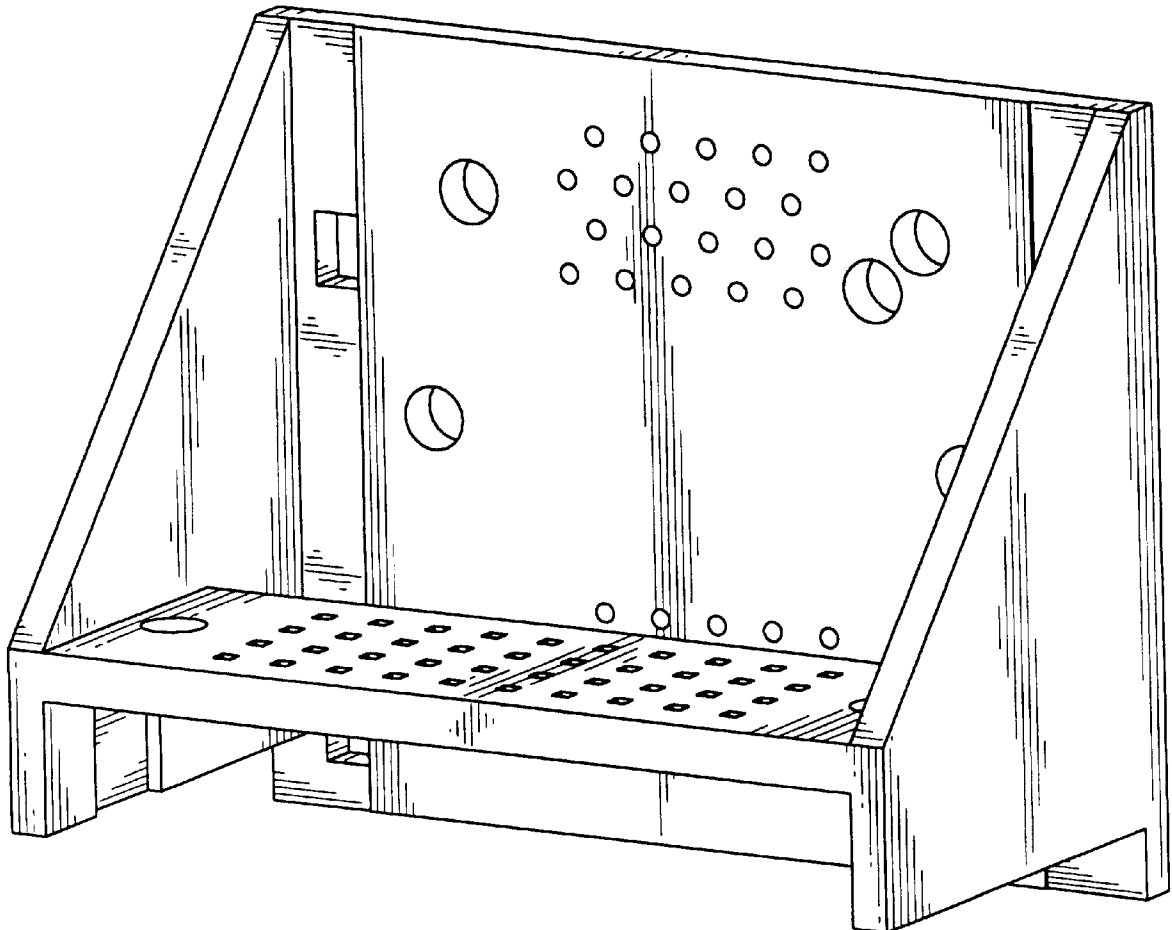
FIG. 7 is a bottom plan view thereof.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

4,752,110 A \* 6/1988 Blanchet et al. .... 385/135

**1 Claim, 7 Drawing Sheets**



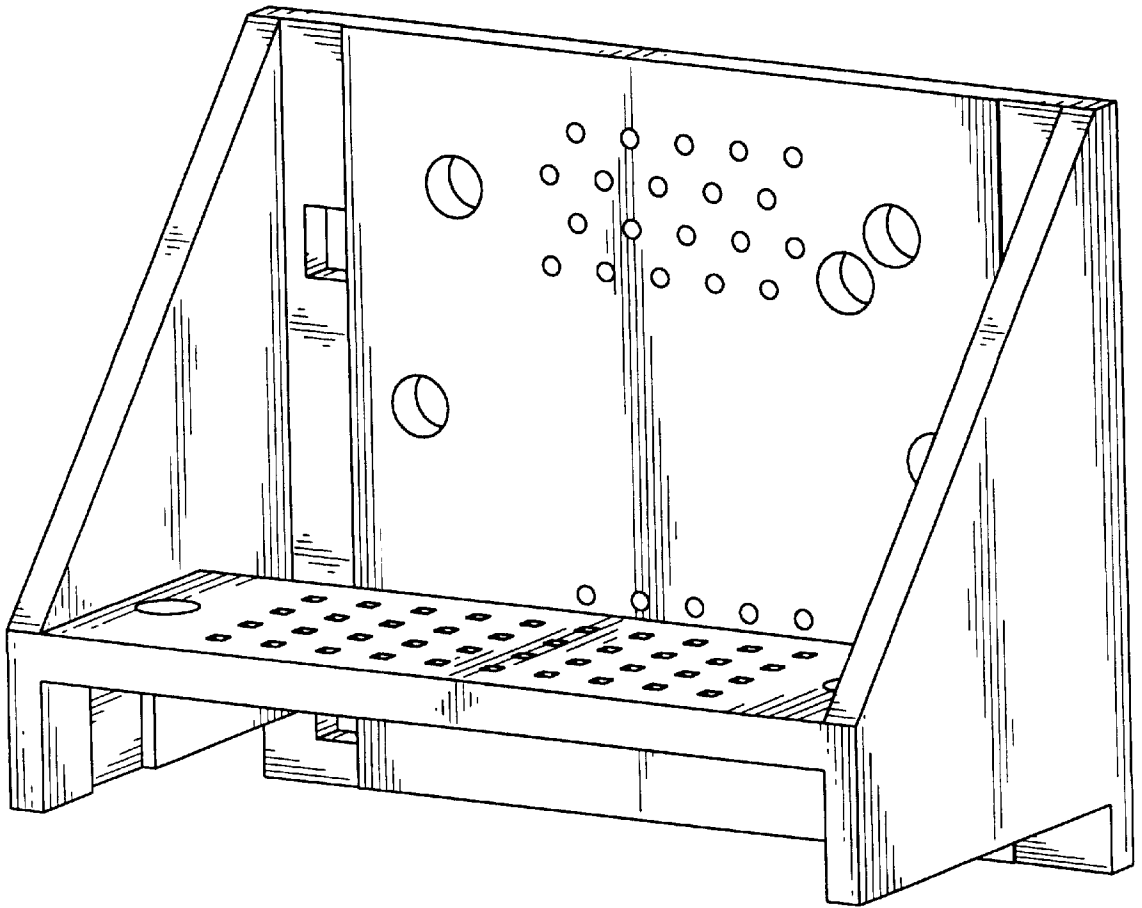


FIG. 1

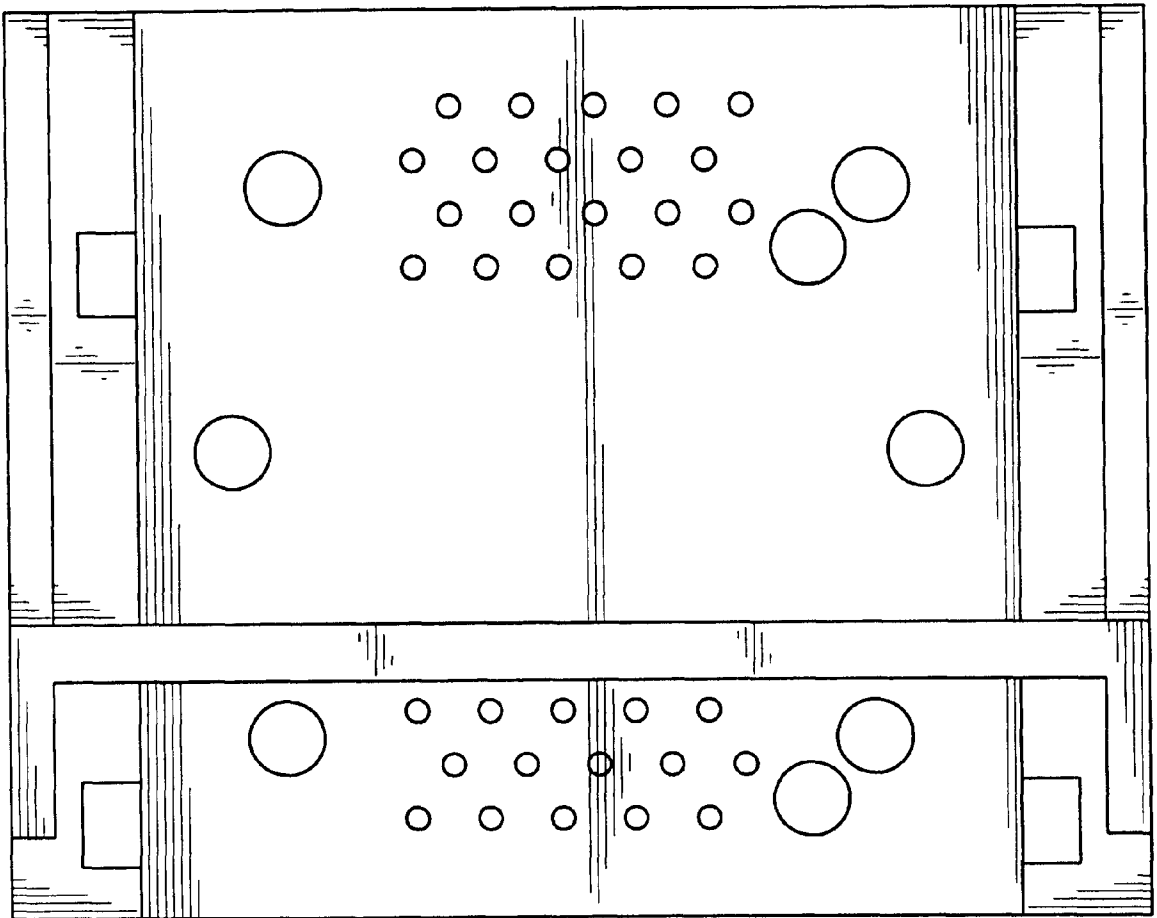


FIG. 2

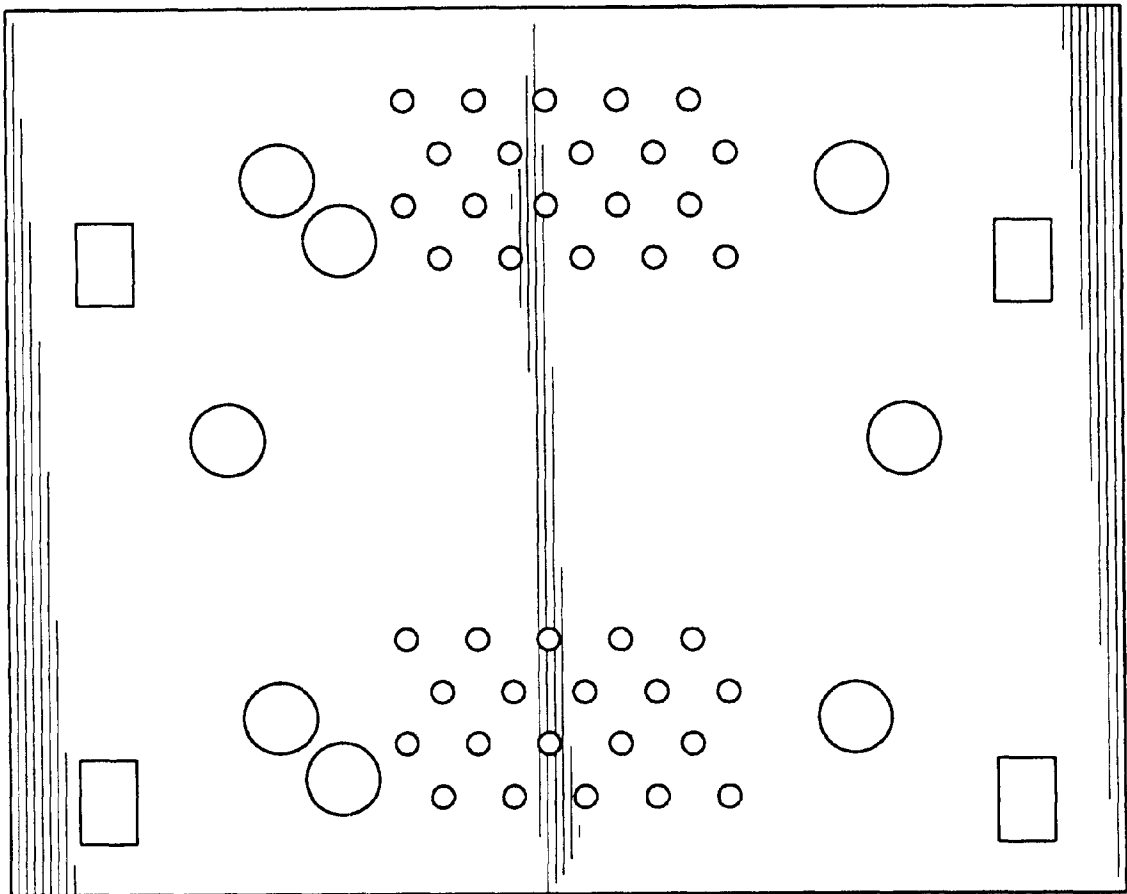


FIG. 3

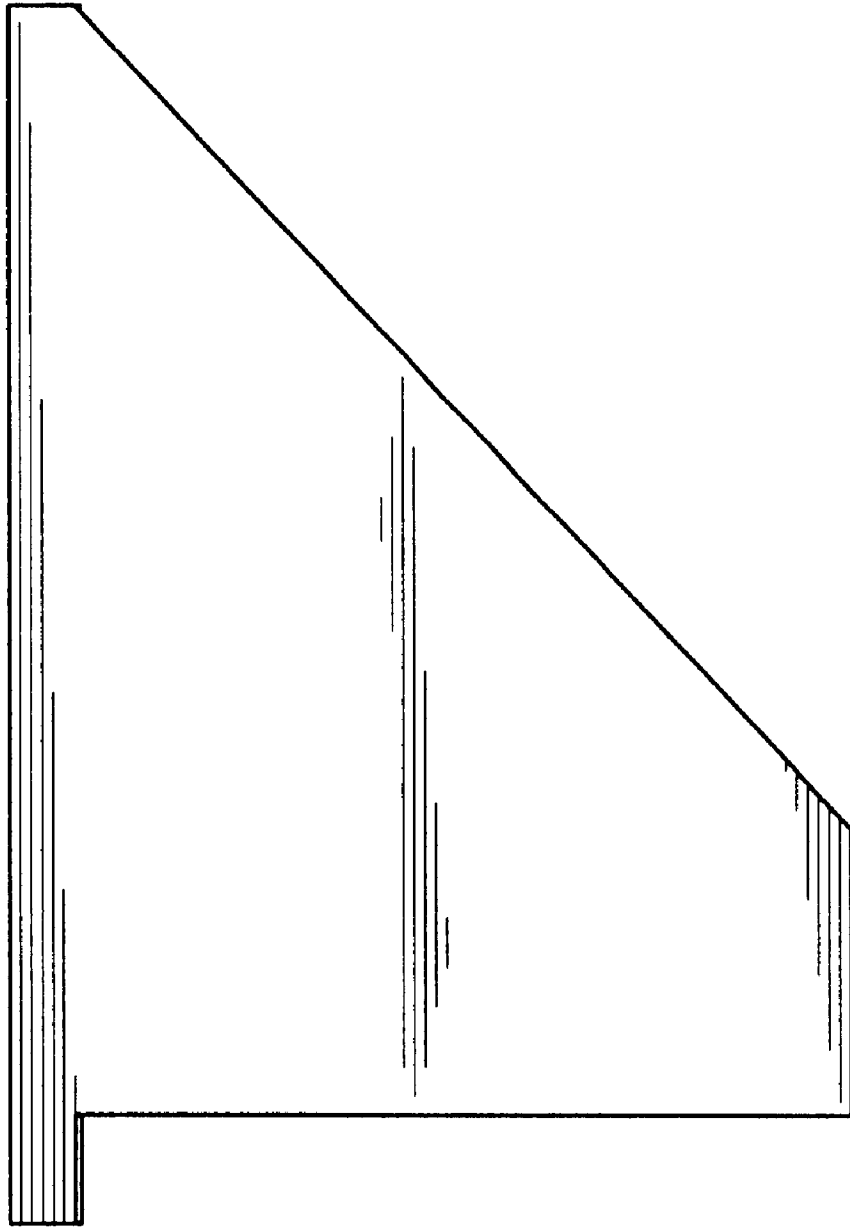


FIG. 4

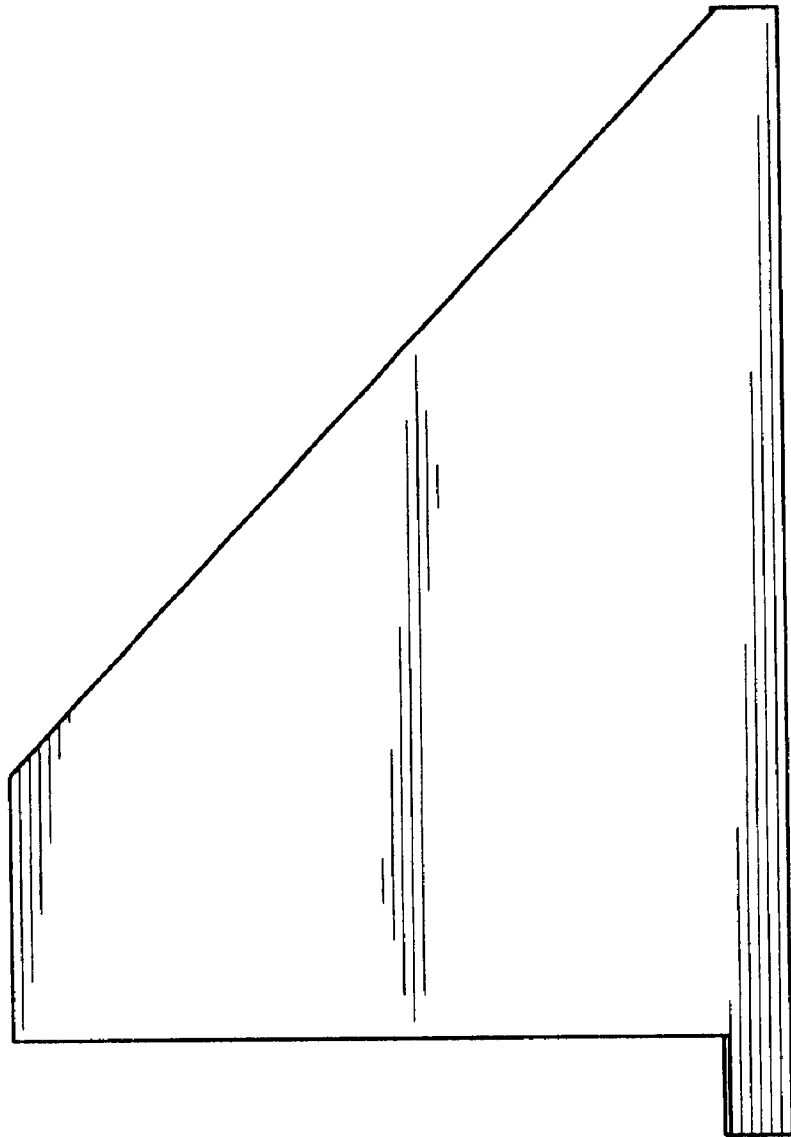


FIG. 5

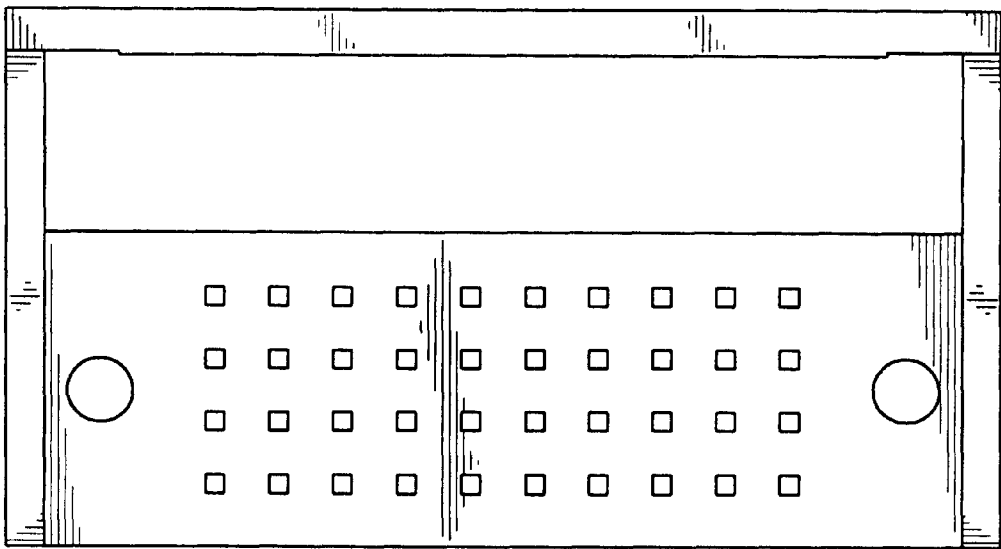


FIG. 6

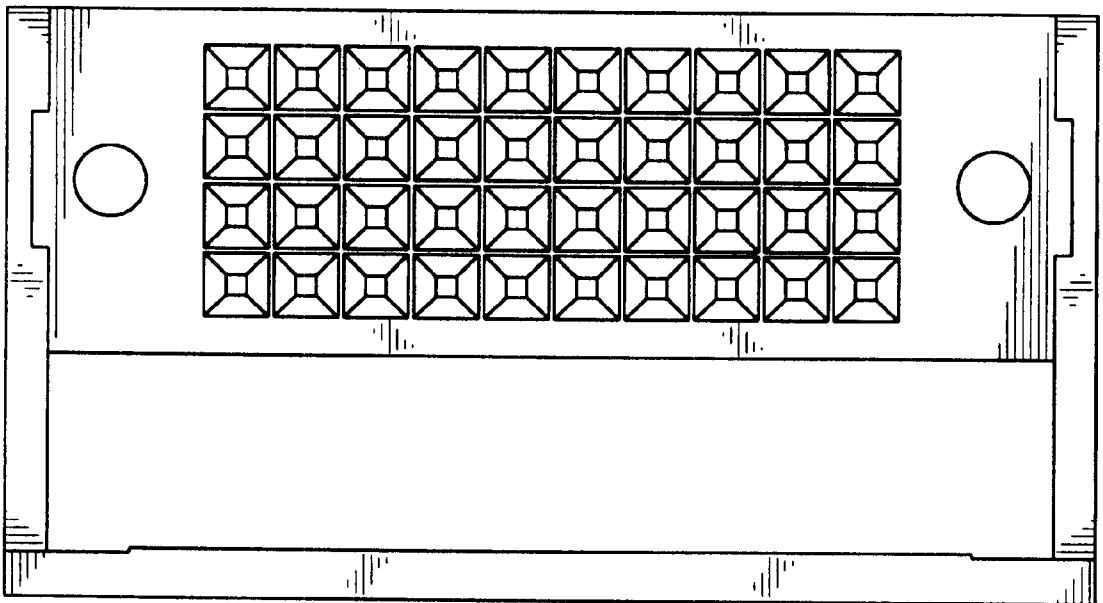


FIG. 7