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(54) **HIGH-EFFICIENCY MODULATING RF AMPLIFIER**

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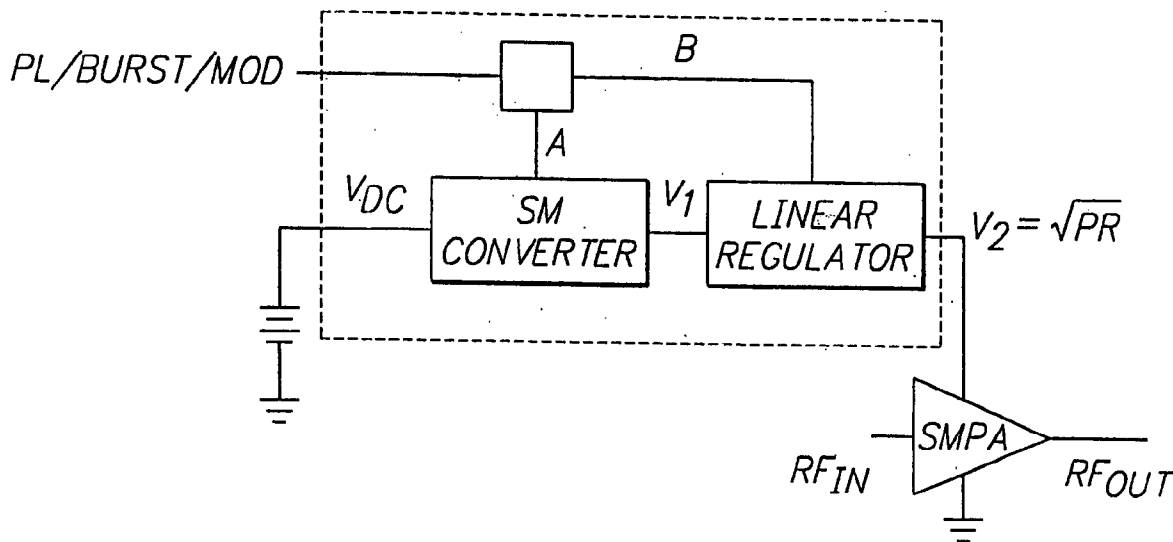
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(57) **ABSTRACT**

The present invention, generally speaking, provides for high-efficiency power control of a high-efficiency (e.g., hard-limiting or switch-mode) power amplifier in such a manner as to achieve a desired modulation. In one embodiment, the spread between a maximum frequency of the desired modulation and the operating frequency of a switch-mode DC-DC converter is reduced by following the switch-mode converter with an active linear regulator. The linear regulator is designed so as to control the operating voltage of the power amplifier with sufficient bandwidth to faithfully reproduce the desired amplitude modulation wave-form. The linear regulator is further designed to reject variations on its input voltage even while the output voltage is changed in response to an applied control signal. This rejection will occur even though the variations on the input voltage are of commensurate or even lower frequency than that of the controlled output variation. Amplitude modulation may be achieved by directly or effectively varying the operating voltage on the power amplifier while simultaneously achieving high efficiency in the conversion of primary DC power to the amplitude modulated output signal. High efficiency is enhanced by allowing the switch-mode DC-to-DC converter to also vary its output voltage such that the voltage drop across the linear regulator is kept at a low and relatively constant level. Time-division multiple access (TDMA) bursting capability may be combined with efficient amplitude modulation, with control of these functions being combined. In addition, the variation of average output power level in accordance with commands from a communications system may also be combined within the same structure.



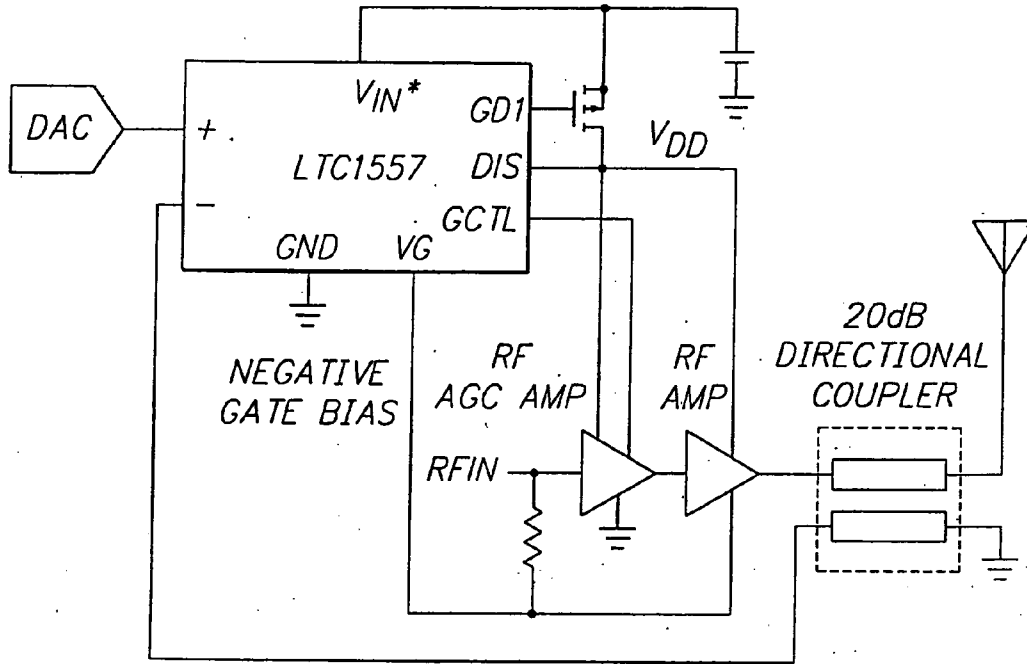


FIG. 1 (PRIOR ART)

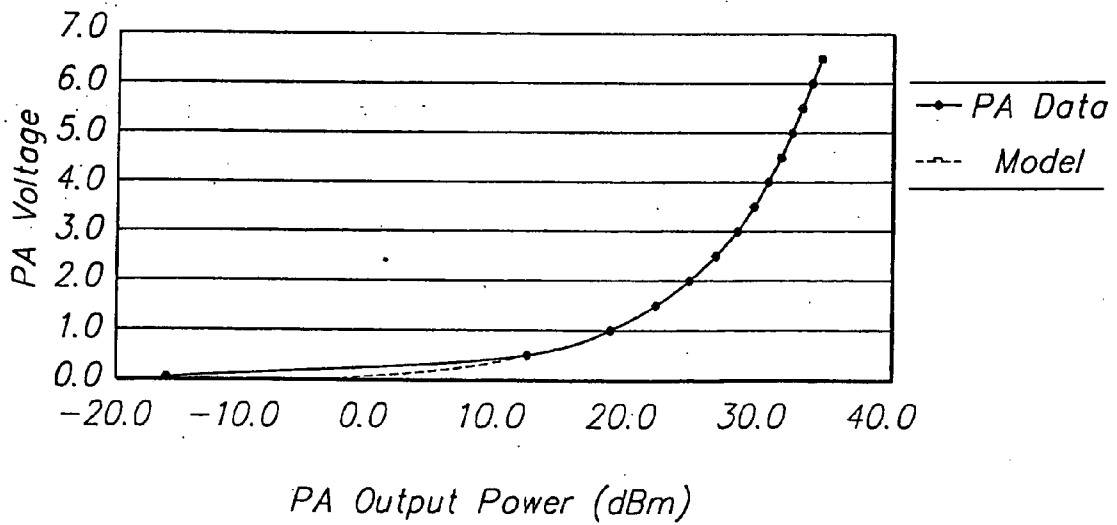


FIG. 4

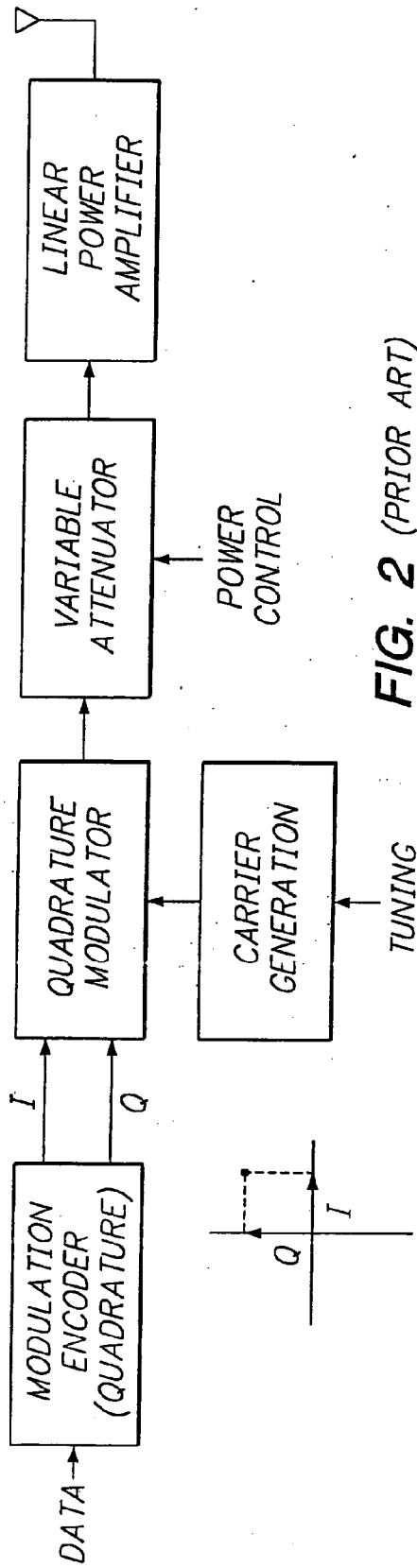


FIG. 2 (PRIOR ART)

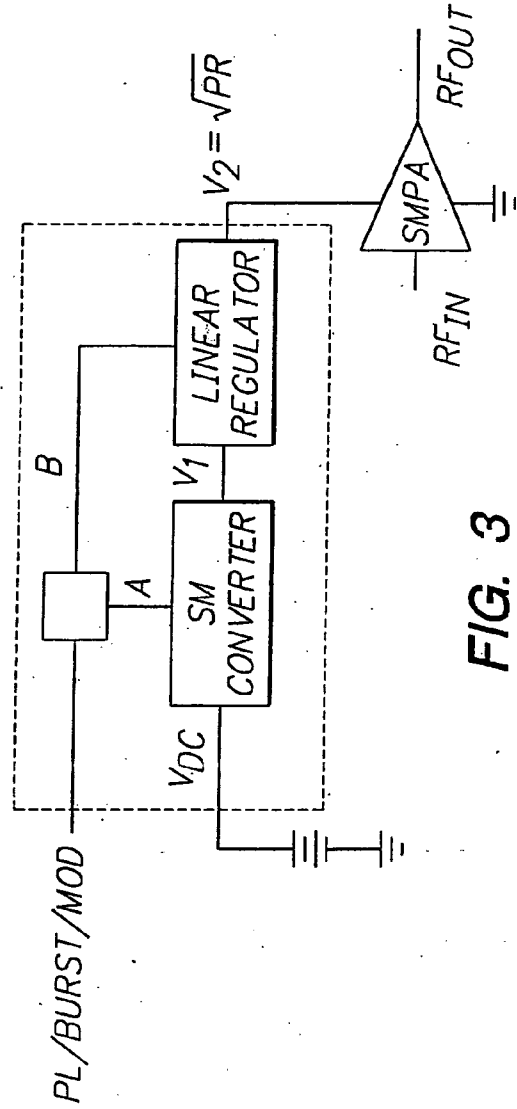


FIG. 3

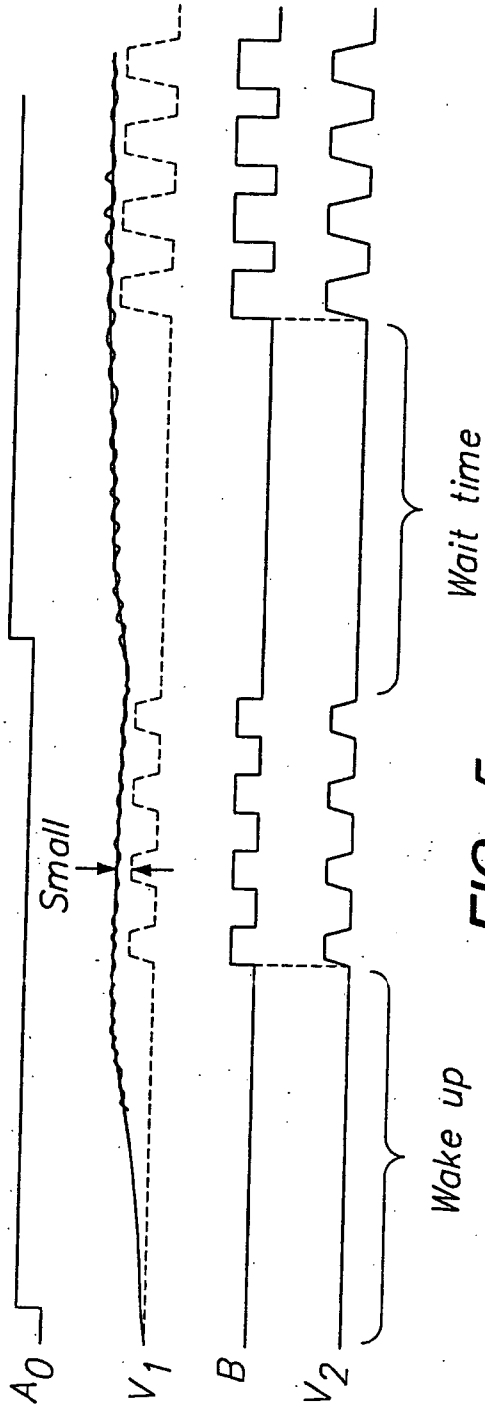


FIG. 5

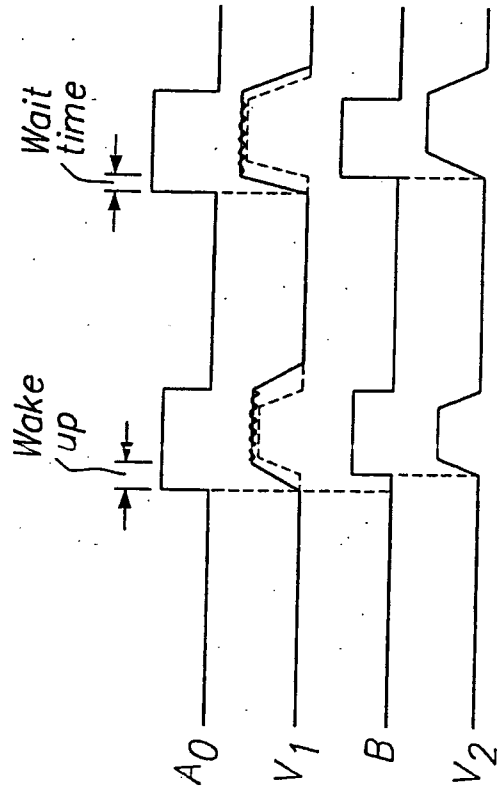


FIG. 6

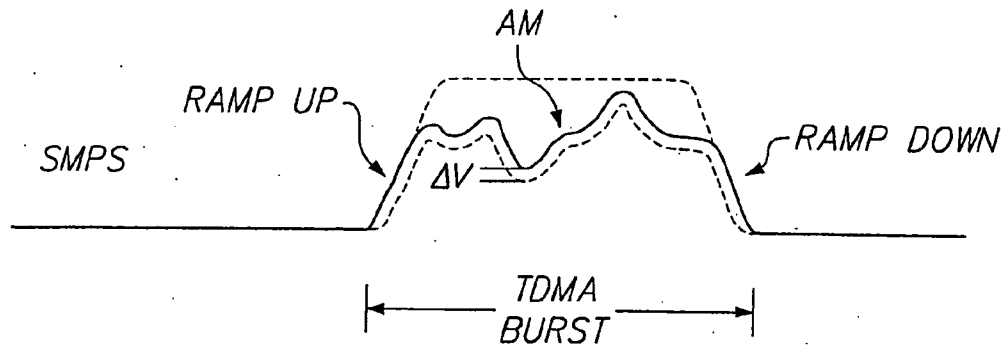


FIG. 7

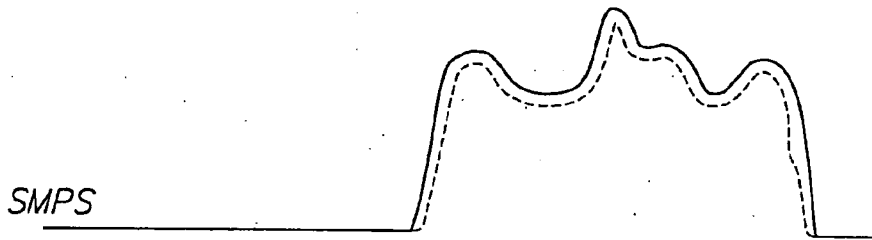


FIG. 8

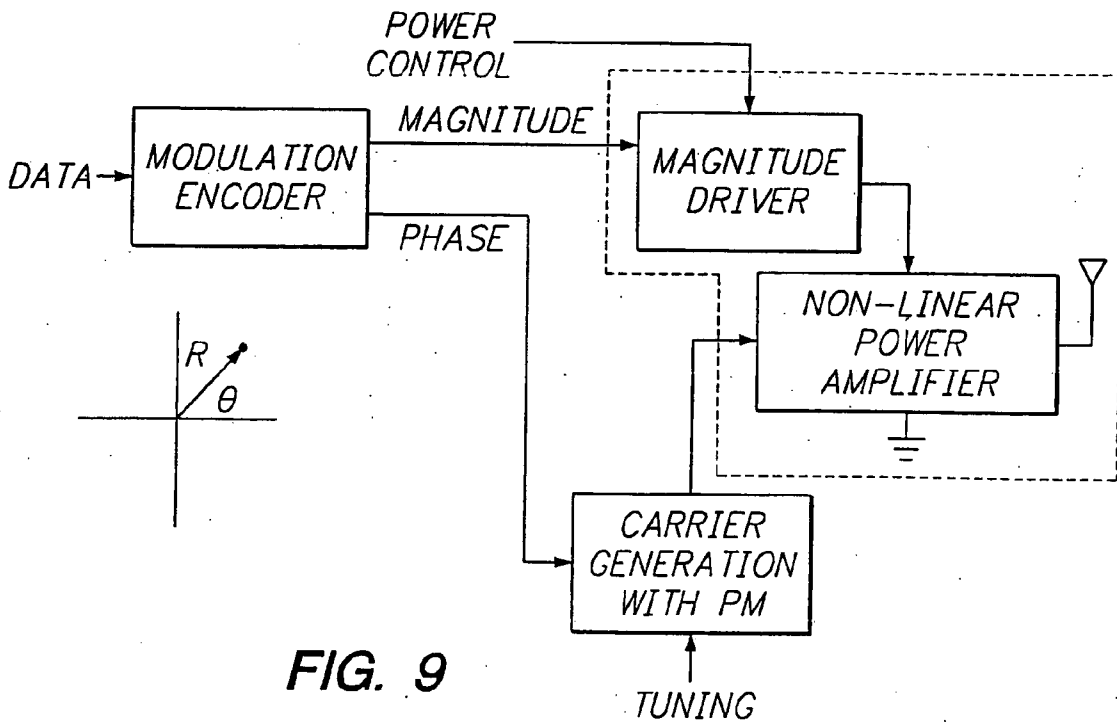


FIG. 9

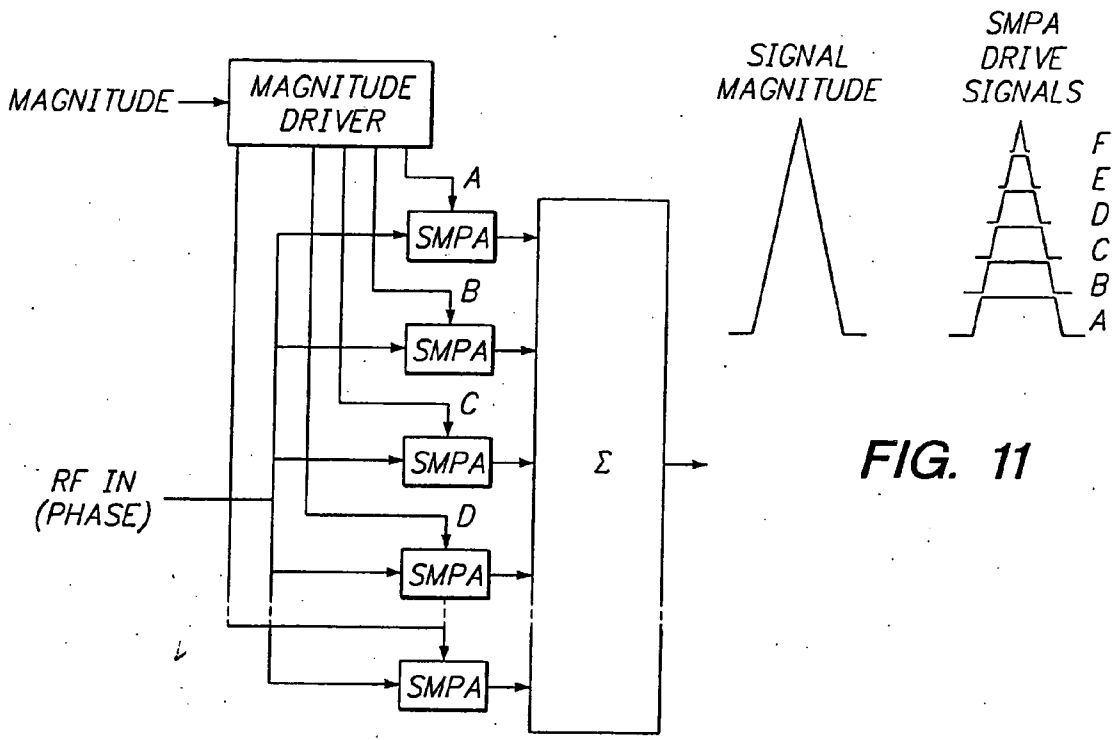


FIG. 10

FIG. 11

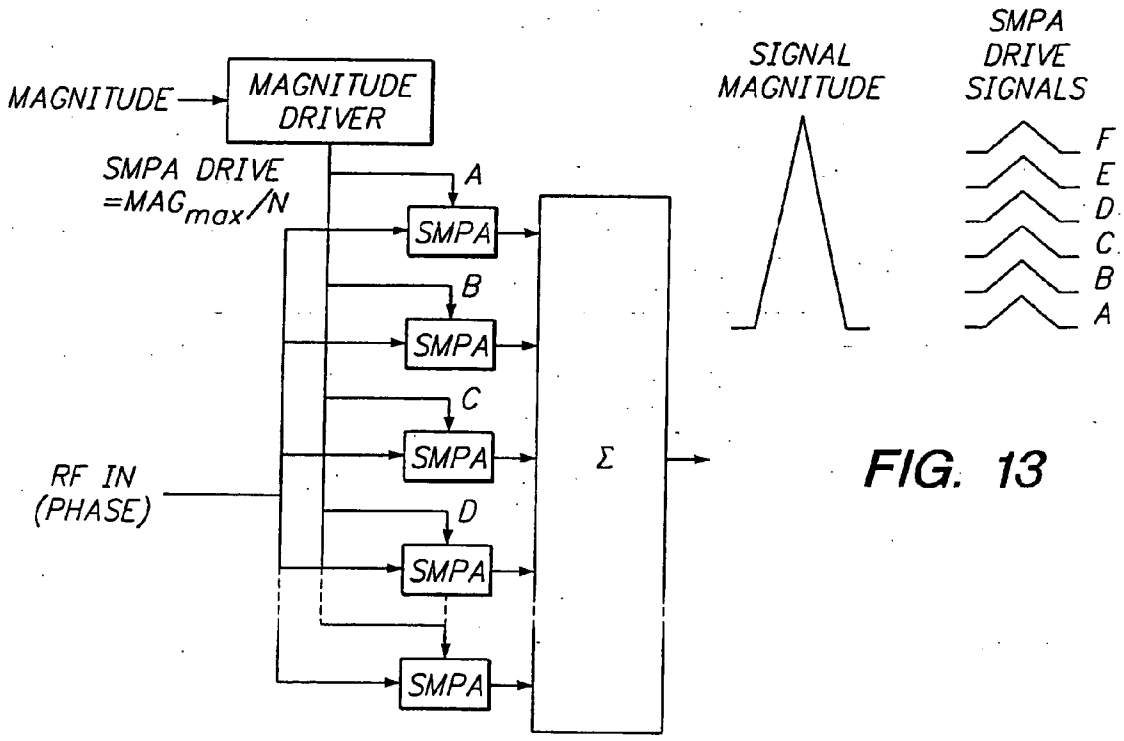


FIG. 12

FIG. 13

HIGH-EFFICIENCY MODULATING RF AMPLIFIER

BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] The present invention relates to RF amplifiers and signal modulation.

[0003] 2. State of the Art

[0004] Battery life is a significant concern in wireless communications devices such as cellular telephones, pagers, wireless modems, etc. Radio-frequency transmission, especially, consumes considerable power. A contributing factor to such power consumption is inefficient power amplifier operation. A typical RF power amplifier for wireless communications operates with only about 10% efficiency. Clearly, a low-cost technique for significantly boosting amplifier efficiency would satisfy an acute need.

[0005] Furthermore, most modern digital wireless communications devices operate on a packet basis. That is, the transmitted information is sent in a series of one or more short bursts, where the transmitter is active only during the burst times and inactive at all other times. It is therefore also desirable that control of burst activation and deactivation be controlled in an energy-efficient manner, further contributing to extended battery life.

[0006] Power amplifiers are classified into different groups: Class A, Class B, Class AB, etc. The different classes of power amplifiers usually signify different biasing conditions. In designing an RF power amplifier, there is usually a trade-off between linearity and efficiency. The different classes of amplifier operation offer designers ways to balance these two parameters.

[0007] Generally speaking, power amplifiers are divided into two different categories, linear and non-linear. Linear amplifiers (e.g. Class A amplifiers and Class B push-pull amplifiers), maintain high linearity, resulting in faithful reproduction of the input signal at their output since the output signal is linearly proportional to the input signal. In non-linear amplifiers (e.g. single-ended Class B and Class C amplifiers), the output signal is not directly proportional to the input signal. The resulting amplitude distortion on the output signal makes these amplifiers most applicable to signals without any amplitude modulation, which are also known as constant-envelope signals.

[0008] Amplifier output efficiency is defined as the ratio between the RF output power and the input (DC) power. A major source of power amplifier inefficiency is power dissipated in the transistor. A Class A amplifier is inefficient since current flows continuously through the device. Conventionally, efficiency is improved by trading-off linearity for increased efficiency. In Class B amplifiers, for example, biasing conditions are chosen such that the output signal is cut off during half of the cycle unless the opposing half is provided by a second transistor (push-pull). As a result, the waveform will be less linear. The output waveform may still be made sinusoidal using a tank circuit or other filter to filter out higher and lower frequency components.

[0009] Class C amplifiers conduct during less than 50% of the cycle, in order to further increase efficiency; i.e., if the output current conduction angle is less than 180 degrees, the

amplifier is referred to as Class C. This mode of operation can have a greater efficiency than Class A or Class B, but it typically creates more distortion than Class A or Class B amplifiers. In the case of a Class C amplifier, there is still some change in output amplitude when the input amplitude is varied. This is because the Class C amplifier operates as a constant current source—albeit one that is only on briefly—and not a switch.

[0010] The remaining classes of amplifiers vigorously attack the problem of power dissipation within the transistor, using the transistor merely as a switch. The underlying principle of such amplifiers is that a switch ideally dissipates no power, for there is either zero voltage across it or zero current through it. Since the switch's V-I product is therefore always zero, there is no dissipation in this device. A Class E power amplifier uses a single transistor, in contrast with a Class D power amplifier, which uses two transistors

[0011] In real life, however, switches are not ideal. (Switches have turn on/off time and on-resistance.) The associated dissipation degrades efficiency. The prior art has therefore sought for ways to modify so-called “switch-mode” amplifiers (in which the transistor is driven to act as a switch at the operating frequency to minimize the power dissipated while the transistor is conducting current) so that the switch voltage is zero for a non-zero interval of time about the instant of switching, thereby decreasing power dissipation. The Class E amplifier uses a reactive output network that provides enough degrees of freedom to shape the switch voltage to have both zero value and zero slope at switch turn-on, thus reducing switching losses. Class F amplifiers are still a further class of switch-mode amplifiers. Class F amplifiers generate a more square output waveform as compared to the usual sinewave. This “squaring-up” of the output waveform is achieved by encouraging the generation of odd-order harmonics (i.e., $\times 3$, $\times 5$, $\times 7$, etc.) and suppressing the even-order harmonics (i.e., $\times 2$, $\times 4$, etc.) in the output network.

[0012] An example of a known power amplifier for use in a cellular telephone is shown in **FIG. 1**. GSM cellular telephones, for example, must be capable of programming output power over a 30 dBm range. In addition, the transmitter turn-on and turn-off profiles must be accurately controlled to prevent spurious emissions. Power is controlled directly by the DSP (digital signal processor) of the cellular telephone, via a DAC (digital to analog converter). In the circuit of **FIG. 1**, a signal GCTL drives the gate of an external AGC amplifier that controls the RF level to the power amplifier. A portion of the output is fed back, via a directional coupler, for closed-loop operation. The amplifier in **FIG. 1** is not a switch-mode amplifier. Rather, the amplifier is at best a Class AB amplifier driven into saturation, and hence demonstrates relatively poor efficiency.

SURVEY OF PRIOR PATENTS

[0013] Control of the output power from an amplifier is consistently shown as requiring a feedback structure, as exemplified in U.S. Pat. Nos. 4,392,245; 4,992,753; 5,095,542; 5,193,223; 5,369,789; 5,410,272; 5,697,072 and 5,697,074. Other references, such as U.S. Pat. No. 5,276,912, teach the control of amplifier output power by changing the amplifier load circuit.

[0014] A related problem is the generation of modulated signals, e.g., amplitude modulated (AM) signals, quadrature

amplitude modulated signals (QAM), etc. A known IQ modulation structure is shown in **FIG. 2**. A data signal is applied to a quadrature modulation encoder that produces I and Q signals. The I and Q signals are applied to a quadrature modulator along with a carrier signal. The carrier signal is generated by a carrier generation block to which a tuning signal is applied.

[0015] Typically, an output signal of the quadrature modulator is then applied to a variable attenuator controlled in accordance with a power control signal. In other instances, power control is implemented by varying the gain of the amplifier. This is achieved by adjusting the bias on transistors within the linear amplifier, taking advantage of the effect where transistor transconductance varies with the applied bias conditions. Since amplifier gain is strongly related to the transistor transconductance, varying the transconductance effectively varies the amplifier gain. A resulting signal is then amplified by a linear power amplifier and applied to an antenna.

[0016] A method for producing accurate amplitude modulated signals using nonlinear Class C amplifiers, called "plate modulation," has been known for over 70 years as described in texts such as Terman's Radio Engineers Handbook (McGraw-Hill, 1943). In the typical plate-modulation technique, output current from the modulator amplifier is linearly added to the power supply current to the amplifying element (vacuum tube or transistor), such that the power supply current is increased and decreased from its average value in accordance with the amplitude modulation. This varying current causes the apparent power supply voltage on the amplifying element to vary, in accordance with the resistance (or conductance) characteristics of the amplifying element.

[0017] By using this direct control of output power, AM can be effected as long as the bandwidth of the varying operating voltage is sufficient. That is, these nonlinear amplifiers actually act as linear amplifiers with respect to the amplifier operating voltage. To the extent that this operating voltage can be varied with time while driving the nonlinear power amplifier, the output signal will be linearly amplitude modulated.

[0018] In AM signals, the amplitude of the signal is made substantially proportional to the magnitude of an information signal, such as voice. Information signals such as voice are not constant in nature, and so the resulting AM signals are continuously varying in output power. Methods of achieving amplitude modulation include the combination of a multitude of constant amplitude signals, as shown in U.S. Pat. Nos. 4,580,111; 4,804,931; 5,268,658 and 5,652,546. Amplitude modulation by using pulse-width modulation to vary the power supply of the power amplifier is shown in U.S. Pat. Nos. 4,896,372; 3,506,920; 3,588,744 and 3,413,570. However, the foregoing patents teach that the operating frequency of the switch-mode DC-DC converter must be significantly higher than the maximum modulation frequency.

[0019] U.S. Pat. No. 5,126,688 to Nakanishi et al. addresses the control of linear amplifiers using feedback control to set the actual amplifier output power, combined with periodic adjustment of the power amplifier operating voltage to improve the operating efficiency of the power amplifier. The primary drawback of this technique is the

requirement for an additional control circuit to sense the desired output power, to decide whether (or not) the power amplifier operating voltage should be changed to improve efficiency, and to effect any change if so decided. This additional control circuitry increases amplifier complexity and draws additional power beyond that of the amplifier itself, which directly reduces overall efficiency.

[0020] A further challenge has been to generate a high-power RF signal having desired modulation characteristics. This object is achieved in accordance with the teachings of U.S. Pat. No. 4,580,111 to Swanson by using a multitude of high efficiency amplifiers providing a fixed output power, which are enabled in sequence such that the desired total combined output power is a multiple of this fixed individual amplifier power. In this scheme, the smallest change in overall output power is essentially equal to the power of each of the multitude of high efficiency amplifiers. If finely graded output power resolution is required, then potentially a very large number of individual high efficiency amplifiers may be required. This clearly increases the overall complexity of the amplifier.

[0021] U.S. Pat. No. 5,321,799 performs polar modulation, but is restricted to full-response data signals and is not useful with high power, high-efficiency amplifiers. The patent teaches that amplitude variations on the modulated signal are applied through a digital multiplier following phase modulation and signal generation stages. The final analog signal is then developed using a digital-to-analog converter. As stated in the State of the Art section herein, signals with information already implemented in amplitude variations are not compatible with high-efficiency, nonlinear power amplifiers due to the possibly severe distortion of the signal amplitude variations.

[0022] Despite the teachings of the foregoing references, a number of problems remain to be solved, including the following: to achieve high-efficiency amplitude modulation of an RF signal by variation of the operating voltage using a switch mode converter without requiring high-frequency switch-mode operation (as compared to the modulation frequency); to unify power-level and burst control with modulation control; to enable high-efficiency modulation of any desired character (amplitude and/or phase); and to enable high-power operation (e.g., for base stations) without sacrificing power efficiency.

SUMMARY OF THE INVENTION

[0023] The present invention, generally speaking, provides for high-efficiency power control of a high-efficiency (e.g., hard-limiting or switch-mode) power amplifier in such a manner as to achieve a desired modulation. In one embodiment, the spread between a maximum frequency of the desired modulation and the operating frequency of a switch-mode DC-DC converter is reduced by following the switch-mode converter with an active linear regulator. The linear regulator is designed so as to control the operating voltage of the power amplifier with sufficient bandwidth to faithfully reproduce the desired amplitude modulation wave-form. The linear regulator is further designed to reject variations on its input voltage even while the output voltage is changed in response to an applied control signal. This rejection will occur even though the variations on the input voltage are of commensurate or even lower frequency than that of the

controlled output variation. Amplitude modulation may be achieved by directly or effectively varying the operating voltage on the power amplifier while simultaneously achieving high efficiency in the conversion of primary DC power to the amplitude modulated output signal. High efficiency is enhanced by allowing the switch-mode DC-to-DC converter to also vary its output voltage such that the voltage drop across the linear regulator is kept at a low and relatively constant level. Time-division multiple access (TDMA) bursting capability may be combined with efficient amplitude modulation, with control of these functions being combined. In addition, the variation of average output power level in accordance with commands from a communications system may also be combined within the same structure.

[0024] The high-efficiency amplitude modulation structure may be extended to any arbitrary modulation. Modulation is performed in polar form, i.e., in a quadrature-free manner.

[0025] Single high-efficiency stages may be combined together to form high-power, high-efficiency modulation structures.

BRIEF DESCRIPTION OF THE DRAWING

[0026] The present invention may be further understood from the following description in conjunction with the appended drawing. In the drawing:

[0027] FIG. 1 is a block diagram of a known power amplifier with output power controlled by varying the power supply voltage;

[0028] FIG. 2 is a block diagram of a known IQ modulation structure;

[0029] FIG. 3 is a block diagram of a power amplifier in accordance with an exemplary embodiment of the present invention;

[0030] FIG. 4 is a plot comparing saturated Class AB power amplifier output power versus operating voltage with the mathematical model $V=\sqrt{PR}$;

[0031] FIG. 5 is a waveform diagram illustrating operation of one embodiment of the invention;

[0032] FIG. 6 is a waveform diagram illustrating operation of another embodiment of the invention;

[0033] FIG. 7 is a waveform diagram illustrating bursted AM operation;

[0034] FIG. 8 is a waveform diagram illustrating bursted AM operation with power level control;

[0035] FIG. 9 is a block diagram of a polar modulation structure using a high-efficiency amplifier;

[0036] FIG. 10 is a block diagram of a first high power, high efficiency, amplitude modulating RF amplifier;

[0037] FIG. 11 is a waveform diagram illustrating operation of the amplifier of FIG. 10;

[0038] FIG. 12 is a block diagram of a second high power, high efficiency, amplitude modulating RF amplifier; and

[0039] FIG. 13 is a waveform diagram illustrating operation of the amplifier of FIG. 12.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0040] Referring now to FIG. 3, a block diagram is shown of a power amplifier that may be used in the present invention. A switch-mode (or saturated) nonlinear amplifier has applied to it a voltage produced by a power control stage. In an exemplary embodiment, the voltage V applied to the nonlinear amplifier is controlled substantially in accordance with the equation

$$V=\sqrt{PR}$$

where P is the desired power output level of the amplifier and R is the resistance of the amplifier. In the case of a switch-mode or saturated amplifier, the resistance R may be regarded as constant. The power control stage receives a DC input voltage, e.g., from a battery, and receives a power level control signal and outputs a voltage in accordance with the foregoing equation.

[0041] The efficacy of directly controlling output power of nonlinear amplifiers over a wide dynamic range by solely varying the operating voltage is demonstrated by FIG. 4, showing a plot comparing saturated Class AB power amplifier output power versus operating voltage with the mathematical model $V=\sqrt{PR}$.

[0042] Referring again to FIG. 3, a power control circuit in accordance with an exemplary embodiment of the invention is shown. A power control stage includes a switch-mode converter stage and a linear regulator stage connected in series. The switch-mode converter may be a Class D device, for example, or a switch-mode power supply (SMPS). The switch-mode converter efficiently steps down the DC voltage to a voltage that somewhat exceeds but that approximates the desired power-amplifier operating voltage level. That is, the switch-mode converter performs an efficient gross power level control. The switch-mode converter may or may not provide sufficiently fine control to define ramp portions of a desired power envelope.

[0043] The linear regulator performs a filtering function on the output of the switch-mode converter. That is, the linear regulator controls precise power-envelope modulation during a TDMA burst, for example. The linear regulator may or may not provide level control capabilities like those of the switch-mode converter.

[0044] Note that, depending on the speed of the switch-mode converter and the linear regulator, the power control stage may be used to perform power control and/or amplitude modulation. A control signal PL/BURST is input to a control block, which outputs appropriate analog or digital control signals for the switch-mode converter and the linear regulator. The control block may be realized as a ROM (read-only memory) and/or a DAC (digital to analog converter).

[0045] Referring to FIG. 5, a waveform diagram is shown, illustrating operation of one embodiment of the invention. The waveforms A and B represent analog control signals applied to the switch-mode converter and to the linear regulator, respectively. The waveforms V_1 and V_2 represent the output voltages of the switch-mode converter and to the linear regulator, respectively. Assume that the switch-mode converter has a relatively large time constant, i.e., that it ramps relatively slowly. When the control signal A is set to

a first non-zero power level, the voltage V_1 will then begin to ramp toward a commensurate voltage. Because of the switch-mode nature of the converter, the voltage V_1 may have a considerable amount of ripple. An amount of time required to reach that voltage defines the wakeup period. When that voltage is reached, the control signal B is raised and lowered to define a series of transmission bursts. When the control signal B is raised, the voltage V_2 ramps quickly up to a commensurate voltage, and when the control signal B is lowered, the voltage V_2 ramps quickly down. Following a series of bursts (in this example), the control signal A is raised in order to increase the RF power level of subsequent bursts. The control signal B remains low during a wait time. When the voltage V_1 has reached the specified level, the control signal B is then raised and lowered to define a further series of transmission bursts.

[0046] The voltage V_2 is shown in dotted lines superimposed on the voltage V_1 . Note that the voltage V_2 is less than the voltage V_1 by a small amount, greater than the negative peak ripple on the voltage V_1 . This small difference between the input voltage of the linear regulator V_1 and the output voltage of the linear regulator V_2 makes overall high-efficiency operation possible.

[0047] Referring to FIG. 6, in accordance with a different embodiment of the invention, the switch-mode converter is assumed to have a relatively short time constant; i.e., it ramps relatively quickly. Hence, when the control signal A is raised, the voltage V_1 ramps quickly to the commensurate voltage. The control signal B is then raised, and the voltage V_2 is ramped. The time difference between when the control signal A is raised on the control signal B is raised defines the wake up time, which may be very short, maximizing sleep time and power savings. The control signal B is then lowered at the conclusion of the transmission burst, after which the control signal A is lowered. Following the example of FIG. 5, in FIG. 6, when the control signal A is next raised, it defines a higher power level. Again, the voltage V_2 is superimposed in dotted lines on the voltage V_1 .

[0048] The same structure may be used to perform amplitude modulation in addition to power and burst control. Referring to FIG. 7, a waveform diagram is shown illustrating bursted AM operation. An output signal of the switch-mode converted is shown as a solid line. As a burst begins, the output signal of the switch-mode converter ramps up. Optionally, as shown in dashed line, the switch-mode converter may ramp up to a fixed level with the linear regulator effecting all of the amplitude modulation on the output signal. More preferably, from an efficiency standpoint, the switch-mode converter effects amplitude modulation, producing an output signal that, ignoring noise, is a small fixed offset ΔV above the desired output signal. The linear regulator removes the noise from the output signal of the switch-mode converter, effectively knocking down the signal by the amount ΔV . The output signal of the linear regulator is shown as a dotted line in FIG. 7. At the conclusion of the burst, the signals ramp down.

[0049] Full control of the output signal power level (average power of the signal) is retained. A succeeding burst, for example, might occur at a higher power level, as shown in FIG. 8. As compared to FIG. 7, in FIG. 8, all signals scale appropriately to realized a higher average power output.

[0050] Incorporation of amplitude modulation on a phase-modulated signal, though it complicates the signal genera-

tion method, is often desirable since such signals may, and often do, occupy less bandwidth than purely phase-modulated signals. Referring to FIG. 9, a block diagram is shown of a polar modulation structure using a high-efficiency amplifier of the type described thus far. This polar modulation structure is capable of effecting any desired modulation. A data signal is applied to a modulation encoder that produces magnitude and phase signals. The phase signal is applied to a phase-modulation-capable carrier generation block, to which a tuning signal is also applied. A resulting signal is the amplified by a nonlinear power amplifier of the type previously described. Meanwhile, the magnitude signal is applied to a magnitude driver. The magnitude driver also receives a power control signal. In response, the magnitude driver produces an operating voltage that is applied to the non-linear amplifier. The magnitude driver and the non-linear amplifier may be realized in the same manner as FIG. 3, described previously, as indicated in FIG. 9 by a dashed line.

[0051] The modulation structures described thus far are suitable for use in, among other applications, cellular telephone handsets. A similar need for high-efficiency RF signal generation exists in cellular telephone basestations. Basestations, however, operate at much higher power than handsets. The following structure may be used to achieve high-power, high-efficiency RF signal generation.

[0052] Referring to FIG. 10, a first high power, high efficiency, amplitude modulating RF amplifier includes multiple switch mode power amplifier (SMPA) blocks, each block being realized as shown in FIG. 3, for example. An RF signal to be amplified is input to all of the SMPA blocks in common. Separate control signals for each of the SMPA blocks are generated by a magnitude driver in response to a magnitude input signal. Output signals of the SMPA blocks are summed to form a single resultant output signal.

[0053] The manner of operation of the amplifier of FIG. 10 may be understood with reference to FIG. 11. On the left-hand side is shown an overall magnitude signal that is applied to the magnitude driver. On the right-hand side are shown SMPA drive signals output by the magnitude driver to be applied to the respective SMPAs. Note that the sum of the individual drive signals yields the overall magnitude signal.

[0054] An alternative embodiment of a high-power amplifier is shown in FIG. 12. In this embodiment, instead of generating individual drive signals for the respective SMPAs, a common drive signal is generated and applied in common to all of the SMPAs. At a given instant in time, the common drive signal is caused to have a value that is one Nth of an overall magnitude signal applied to the magnitude driver, where N is the number of SMPAs. The result is illustrated in FIG. 13. Once again, note that the sum of the individual drive signals yields the overall magnitude signal.

[0055] It will be appreciated by those of ordinary skill in the art that the invention can be embodied in other specific forms without departing from the spirit or essential character thereof. The presently disclosed embodiments are therefore considered in all respects to be illustrative and not restrictive. The scope of the invention is indicated by the appended claims rather than the foregoing description, and all changes which come within the meaning and range of equivalents thereof are intended to be embraced therein.

1-6. (canceled)

7. A wireless communication device comprising:

a modulation encoder block responsive to a data input signal for producing as output signals a magnitude signal and a phase signal each describing corresponding characteristics of a desired RF output signal;

a carrier generation block responsive to the phase signal and to a tuning input signal for producing an RF carrier signal having a frequency determined in accordance with the tuning input signal and having a phase modulation characteristic determined in accordance with the phase signal;

a magnitude driver block responsive to the magnitude signal and to a power level input signal for producing at least one magnitude drive signal having a drive strength determined in accordance with both the magnitude signal and the power level input signal; and

RF power amplifier circuitry having at least one stage and having the magnitude drive signal as a supply voltage, the RF carrier signal causing the one stage to be driven repeatedly between two states, a hard-on state and a hard-off state, without operating the stage in a linear operating region for an appreciable percentage of time, thereby producing an RF output signal;

wherein the RF power amplifier circuitry is controlled without continuous or frequent feedback adjustment of the RF output signal.

8. The wireless communication device of claim 7, wherein the magnitude driver block comprises:

a switch mode converter having a power input, a power output and a control input; and

a regulator having a power input, a power output and a control input, the power input of the regulator being coupled to the power output of the switch-mode converter; and

control circuitry responsive to the magnitude signal and the power level input signal for producing a first control signal coupled to the control input of the switch mode converter and a second control signal coupled to the control input of the regulator.

9. The wireless communication device of claim 8, further comprising a plurality of amplifier modules coupled in parallel, wherein the magnitude driver block is responsive to

an overall magnitude signal for generating one or more magnitude drive signals, a magnitude drive signal being applied to each of the amplifier modules, each amplifier module comprising RF power amplifier circuitry having at least one stage and having a respective magnitude drive signal as a supply voltage, the RF carrier signal causing the one stage to be driven repeatedly between two states, a hard-on state and a hard-off state, without operating the stage in a linear operating region for an appreciable percentage of time, thereby producing an RF output signal.

10. The wireless communication device of claim 9, wherein separate respective magnitude drive signals are generated for each of the RF power amplifiers.

11. The wireless communication device of claim 9, wherein a single magnitude drive signal is applied in common to all of the RF power amplifiers.

12. A method of generating a modulated RF signal in a wireless communication device, comprising:

responsive to a data input signal, producing as output signals a magnitude signal and a phase signal each describing corresponding characteristics of a desired RF output signal;

responsive to the phase signal and to a tuning input signal, producing an RF carrier signal having a frequency determined in accordance with the tuning input signal and having a phase modulation characteristic determined in accordance with the phase signal;

responsive to the magnitude signal and to a power level input signal, producing at least one magnitude drive signal having a drive strength determined in accordance with both the magnitude signal and the power level input signal; and

applying the magnitude drive signal as a supply source to RF power amplifier circuitry having at least one stage, the RF carrier signal causing the one stage to be driven repeatedly between two states, a hard-on state and a hard-off state, without operating the stage in a linear operating region for an appreciable percentage of time, thereby producing an RF output signal;

wherein the RF power amplifier circuitry is controlled without continuous or frequent feedback adjustment of the RF output signal.

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