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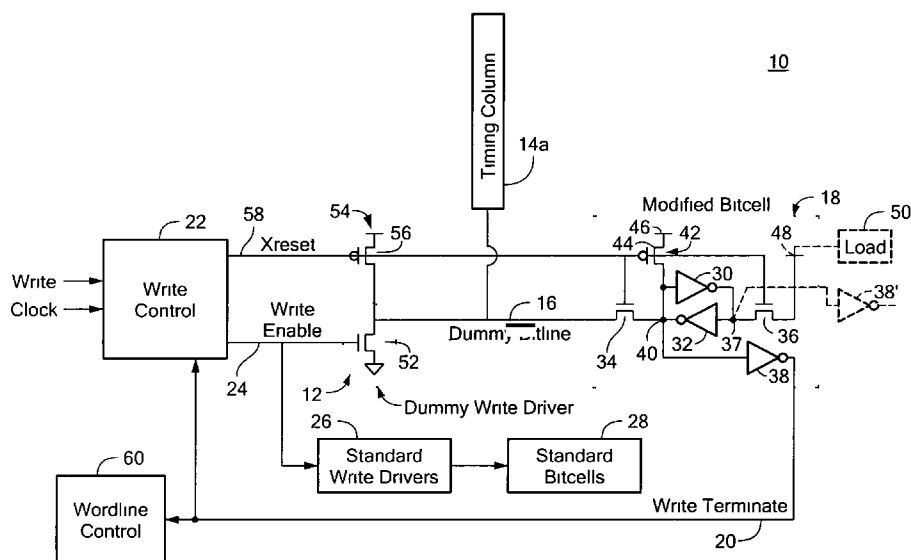
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(54) **Title:** MEMORY WRITE TIMING SYSTEM



(57) **Abstract:** A memory write timing system includes a modified memory bitcell (18) including a storage device (30, 32) and a write/read circuit (60) for writing/reading data to/from the storage device; and an output circuit (38) for detecting the current state of the storage device.

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A CLASSIFICATION OF SUBJECT MATTER
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B FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No
x	US 6,181,640 B1 (Kang) 30 January 2001 (30 01 2001), all document	1, 12, 13, 16, 18-21
x	US 5,657,330 (Matsumoto) 12 August 1997 (12 08 1997), all document	1, 7, 8, 12
x	US 6,181,640 B1 (Kang) 30 January 2001 (30 01 2001), all document	7

y	US 5,657,330 (Matsumoto) 12 August 1997 (12 08 1997), all document	

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