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(71) Applicant (for all designated States except US): CISCO TECHNOLOGY, INC. [US/US]; 170 West Tasman Drive, San Jose, CA 95134-1706 (US).

(72) Inventors; and

- (75) Inventors/Applicants (for US only): SRINIVASAN. Udayakumar [IN/US]; 231 Dixon Landing Road, No. 292, Milpitas, CA 95035 (US). KUMAR, Sampath, Hosahally [US/US]; 491 Galen Drive, San Jose, CA 95123 (US). MATTUR, Dattatri, N. [IN/US]; 614 San Conrado Terrace, Sunnyvale, CA 94085 (US). RAO, Madhukar [IN/US]; 1360 Tanaka Drive, San Jose, CA 95131 (US). BHORKAR, Abhay, Ujwal [IN/US]; 65 Rio Robles East, Unit 1204, San Jose, CA 95134 (US).
- (74) Agents: FORD, Stephen, S. et al.; Marger Johnson & Mc-Collom, P.C., 210 SW Morrison Street, Suite 400, Portland, OR 97204 (US).

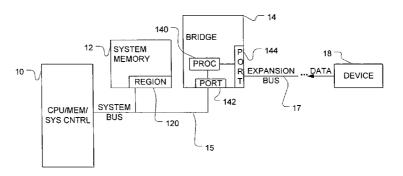
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(54) Title: INTELLIGENT PCI BRIDGING CONSISTING OF PREFETCHING ALL DATA PRIOR TO SENDING DATA TO REQUESTING DEVICE



(57) Abstract: A bridging device has at least two ports. The first port allows the device to communicate with devices on an expansion bus and at least one other port to allow the bridge to communicate with a system memory on a system bus or other devices on another expansion bus. The device is capable of identifying at least two regions in memory, a descriptor region and a data region. A descriptor provides information about segments of data in the data region. The bridge may detect descriptors read from the memory, extract information related to data associated with those descriptors and use this information to perform prefetching of data from the system memory.





INTERNATIONAL SEARCH REPORT

International application No.

PCT/US05/02342

A. CLASSIFICATION OF SUBJECT MATTER IPC(7): G06F 13/00 US CL: 710/300,307,315,313 According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED			
Minimum documentation searched (classification system followed by classification symbols) U.S.: 710/300,307,315,313			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Please See Continuation Sheet			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	or the state of th		Relevant to claim No.
Y,E	US 6,973,528 B2 (Bronson et al.) 06 December 2005, see entire document.		1-20
Y	US 6,463,483 B1 (Imperiali) 08 October 2002, see entire document.		1-20
Y,P	US 6,766,511 B1 (Berry et al.) 20 July 2004, see entire document.		2,10,13,17
Y	Y US 5,815,662 A (Ong) 29 September 1998, see entire document.		15
A US 5,649,161 A (Andrade et al.) 15 July 1997.			1-28
A	US 5,737,765 A (Shigeeda) 07 April 1998.		1-28
Further documents are listed in the continuation of Box C. See patent family annex.			
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10 February 2006 (10.02.2006)		Authorized officer	<u> </u>
Name and mailing address of the ISA/US		711	
Mail Stop PCT, Attn: ISA/US Commissioner for Patents		BRIAN JOHNSON Firmes A. Matthetas Telephone No. (571) 272-3595	
P.O. Box 1450 Alexandria Virginia 22313-1450		Telephone No. (571) 272-3595	
Alexandria, Virginia 22313-1450 Facsimile No. (571) 273-3201			

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PCT/US05/02342

Continuation of Item 4 of the first sheet: INTELLIGENT PCI BRIDGING CONSISTING OF PREFETCHING ALL DATA PRIOR TO SENDING DATA TO REQUESTING DEVICE
Continuation of B. FIELDS SEARCHED Item 3: USPAT;US-PGPUB;USOCR;EPO;JPO;DERWENT;IBM_TDB peripheral,component,interconnect,descriptor,determin\$3,table,hash,data,size,address,length,decoder,circuit