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(71) Applicant *(for all designated States except US)*: **QUALCOMM Incorporated** [US/US]; Attn: International Ip Administration, 5775 Morehouse Drive, San Diego, California 92121 (US).

(72) Inventors; and

(75) Inventors/Applicants *(for US only)*: **SHIRLEN, Martyn Ryan** [US/US]; 5775 Morehouse Drive, San Diego, California 92121 (US). **HOFMANN, Richard Gerard** [US/US]; 5775 Morehouse Drive, San Diego, California 92121 (US).

(74) Agent: **KAMARCHIK, Peter**; 5775 Morehouse Drive, San Diego, California 92121 (US).

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(54) **Title**: METHOD FOR SECURELY COMMUNICATING INFORMATION ABOUT THE LOCATION OF A COMPROMISED COMPUTING DEVICE

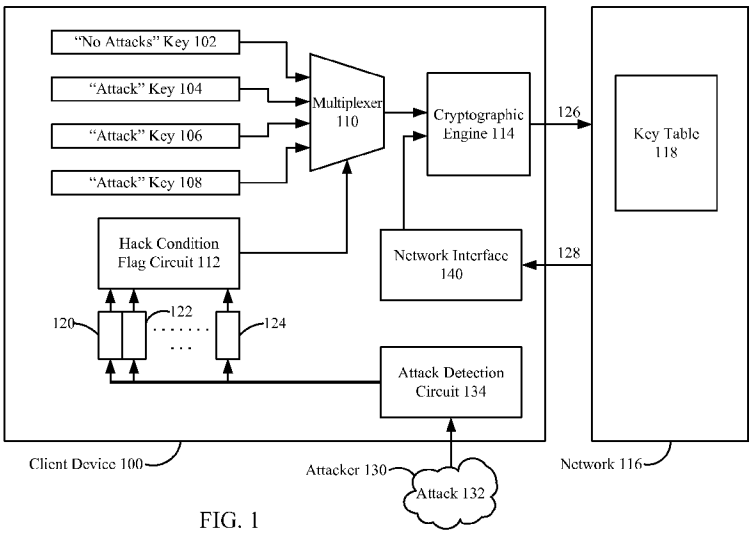

FIG. 1

(57) **Abstract**: A method for securely reporting location information after an attack on a computing device is presented. Such information may be reported to a requesting entity in a manner almost transparent to an attacker. Several exemplary embodiments of systems wherein the method may be used are presented.

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METHOD FOR SECURELY COMMUNICATING INFORMATION ABOUT THE LOCATION OF A COMPROMISED COMPUTING DEVICE

Cross-Reference to Related Application

[0001] This application is a continuation-in-part of prior Application No. 12/044,409, filed March 7, 2008.

Field of Disclosure

[0002] The present disclosure is generally related to security and specifically to securely communicating location information about a computing device that has been compromised or hacked.

Background

[0003] As computing devices have become more complex, they have also become more feature-rich. Devices such as cellular phones now contain sophisticated processors and are capable of performing such tasks as video and audio playback, electronic banking and secure information storage. Hardware, service, content and software providers all have vested interests in protecting their assets from unauthorized access or tampering. For example, a cellular phone provider may want to restrict access to certain "premium" phone features such as video or audio content. Given the large investment by such companies and the quantity and type of information stored in devices such as cellular phones, it is important to be able to prevent unauthorized copying, distribution or access to data.

[0004] There are a number of common methods used to gain unauthorized access to a computing device, including: using an improperly disabled or non-disabled test interface port such as a Joint Test Action Group (JTAG) port; purposefully operating the computing device outside its designed temperature or voltage tolerances; altering traces

or adding components to the printed circuit board to which the computing device is attached; and various types of software attacks. It is possible to provide both hardware and software for detecting and mitigating the effects of these and other types of attacks. It is advantageous to be able to differentiate between types of attacks to allow different responses by a system with which the computing device communicates. It is also advantageous to be able to provide notice that a device has been the subject of an attack without alerting the attacker to the fact that the attack has been detected.

SUMMARY OF THE DISCLOSURE

[0005] It is understood that other embodiments of the teachings herein will become apparent to those skilled in the art from the following detailed description, wherein various embodiments of the teachings are shown and described by way of illustration. As will be realized, the teachings herein are capable of other and different embodiments without departing from the spirit and scope of the teachings. Accordingly, the drawings and detailed description are to be regarded as illustrative in nature and not as restrictive.

BRIEF DESCRIPTION OF THE DRAWINGS

[0006] Various aspects of the teachings of the present disclosure are illustrated by way of example, and not by way of limitation, in the accompanying drawings, wherein:

[0007] FIG. 1 is a block diagram of an embodiment;

[0008] FIG. 2 is a block diagram of another embodiment; and

[0009] FIG. 3 is a flowchart showing the system design of an embodiment; and

[0010] FIG. 4 is a flowchart showing the detection of an attack and the reporting of a client device's location.

DETAILED DESCRIPTION

[0011] The detailed description set forth below, in connection with the appended drawings, is intended as a description of various exemplary embodiments of the teachings of the present disclosure and is not intended to represent the only embodiments in which such teachings may be practiced. The detailed description includes specific details for the purpose of providing a thorough understanding of the teachings by way of illustration and not limitation. It will be apparent to those skilled in the art that the teachings of the present disclosure may be practiced in a variety of ways. In some instances, well known structures and components are described at a high level in order to avoid obscuring the concepts of the present disclosure.

[0012] In one or more exemplary embodiments, the functions and blocks described may be implemented in hardware, software, firmware, or any combination thereof. If implemented in software, the functions may be stored on or transmitted over as one or more instructions or code on a computer-readable medium. Computer-readable media includes both computer storage media and communication media including any medium that facilitates transfer of a computer program from one place to another. A storage media may be any available media that can be accessed by a computer. By way of example, and not limitation, such computer-readable media can comprise RAM, ROM, EEPROM, CD-ROM or other optical disk storage, magnetic disk storage or other magnetic storage devices, or any other medium that can be used to carry or store desired program code in the form of instructions or data structures and that can be accessed by a computer. Also, any connection is properly termed a computer-readable medium. For example, if the software is transmitted from a website, server, or other remote source using a coaxial cable, fiber optic cable, twisted pair, digital subscriber line (DSL), or wireless technologies such as infrared, radio, and microwave, then the coaxial cable,

fiber optic cable, twisted pair, DSL, or wireless technologies such as infrared, radio, and microwave are included in the definition of medium. Disk and disc, as used herein, includes compact disc (CD), laser disc, optical disc, digital versatile disc (DVD), floppy disk and blu-ray disc where disks usually reproduce data magnetically, while discs reproduce data optically with lasers. Combinations of the above should also be included within the scope of computer-readable media.

[0013] FIG. 1 illustrates an exemplary embodiment of a Computing Device 100 incorporating an Attack Detection Block 134.

[0014] The Computing Device 100 is coupled to a Requesting Entity 116 via a Reverse Link 126 and a Forward Link 128. The Reverse Link 126 and Forward Link 128 may be a variety of connections including but not limited to Ethernet, wireless Ethernet or a cellular wireless network protocol such as CDMA or GSM. The Computing Device 100 receives communications from the Requesting Entity 116 via the Forward Link 128 through an Interface 140.

[0015] The Requesting Entity 116 forms a request in a Request Formation Block 152. The request contains the identity of the device the request is directed to. In lieu of an explicit identity the request may be directed to a group of devices or all devices which can receive the request. Additionally the request may set up a schedule for the requested devices to report or may implement any other reporting and scheduling mechanism as the needs of a particular implementation dictate.

[0016] The Request Formation Block 152 may be a dedicated circuit, a general-purpose processor, a software program or any other suitable processing mechanism. The request may include a non-deterministic value generated by an entropy source including but not limited to a look-up table or a thermal noise generator. The Requesting Entity 116 provides the request over the Forward Link 128. Depending on the level of security

desired, the request may be sent in the clear or may be mathematically transformed by methods including, but not limited to, masking or use of a cryptographic algorithm.

[0017] In one embodiment, the Computing Device 100 receives the request including a non-deterministic value at the Interface 140 from the Requesting Entity 116 over the Forward Link 128. The Interface 140 provides the non-deterministic value to a Cryptographic Engine 114. The Cryptographic Engine 114 is adapted to perform a mathematical transformation on information, thereby obscuring that information to a third-party observer. The mathematical transformation performed by the Cryptographic Engine 114 may be but is not limited to a cryptographic hash function (such as MD5, SHA-1 or SHA-3) or a cipher algorithm (such as triple-DES or AES ciphers). The Cryptographic Engine 114 may be implemented as a dedicated hardware block, a general-purpose processor capable of performing cryptographic computations or a software program contained in a computer-readable medium. The Cryptographic Engine 114 generates a transformed key by combining the response key provided by a Key Selection Block 110 with the non-deterministic value and performing the mathematical transformation on the combination of the response key and the non-deterministic value. Because the transformed key is based on the non-deterministic value and the response key, the identity of the response key used in the transformation will be virtually undecipherable to an attacker who can observe the transformed key. Determining the response key from the transformed key is a computationally difficult problem, which makes the response key more undecipherable to an attacker. Using a non-deterministic value as part of the request and response ensures that the transformed key will not always be the same even when reporting the same type of attack. The computing device then transmits the transformed key to the Requesting Entity 116 over the Reverse Link 126.

[0018] The Requesting Entity 116 computes a list of possible transformed key values based on each Programmed Hardware Key 102 – 108, which it may have stored previously or received from the Computing Device 100 and stores the possible transformed key values in a Key Table 118. The Requesting Entity 116 may compute the list of possible transformed key values prior to transmitting the request, in parallel with transmitting the request or after the Requesting Entity 116 has received the transformed key back from the Computing Device 100. The Requesting Entity 116 receives the transformed key from the Computing Device 100 over the Reverse Link 126 at a Comparison Block 150. The Comparison Block 150 may be a dedicated circuit, a general-purpose processor or a software program. In Comparison Block 150, the Requesting Entity 116 compares the transformed key to the possible transformed key values stored in the Key Table 118. The Requesting Entity 116 is thus able to determine whether or not the Computing Device 100 has been attacked by the particular transformed key received from the Computing Device 100. The Requesting Entity 116 is also able to gain information about the type of attack based on the particular transformed key received from the Computing Device 100.

[0019] When an Attacker 130 executes an Attack 132 on the Computing Device 100, the Attack 132 is detected by the Attack Detection Block 134. The Attack Detection Block 134 sets at least one of the plurality of Hack Condition Indicators 120 – 124 based on the type of attack detected, and may be adapted to select the "No Attacks" Key 102 as the default key. The Hack Condition Flag Block 112 controls the output of the Key Selection Block 110 in response to the states of the Hack Condition Indicators 120 – 124. Based on the states of the Hack Condition Indicators 120 - 124, the Hack Condition Flag Block 112 generates a control signal that enables the Key Selection Block 110 to select one of the plurality of Programmed Hardware Keys 102 – 108 as a

response key and provide it to the Cryptographic Engine 114. This response key embodies the identity of the Computing Device 100, whether or not an attack has been detected, and if an attack has been detected, the type of attack detected.

[0020] The Computing Device 100 contains a plurality of Programmed Hardware Keys 102 - 108. These include a "No Attacks" Key 102 and a plurality of "Attack" Keys 104 – 108 which are used to identify the type of attack and to authenticate the Computing Device 100 when challenged by the Requesting Entity 116. The "No Attacks" Key 102 and the plurality of "Attack" Keys 104 - 108 are coupled to Key Selection Block 110. A Hack Condition Flag Circuit 112 is coupled to a plurality of Hack Condition Indicators 120 – 124 and has an output which is coupled to the multiplexer 110. The Attack Detection Block 134 is coupled to the plurality of Hack Condition Indicators 120 – 124. The Cryptographic Engine 114 is responsive to an output of the Key Selection Block 110 and an output of the Interface 140. The Cryptographic Engine 114 transforms a value provided by the Requesting Entity 116 based on the output of the Key Selection Block 110. The value may contain other information also if so desired. The transformed key is then transmitted to the Requesting Entity 116 over the Reverse Link 126.

[0021] The "No Attacks" Key 102 is used when the Attack Detection Block 134 has not detected any attacks on the Computing Device 100. The plurality of "Attack" Keys 104 – 108 correspond to particular types of detected attacks. Each of the plurality of Programmed Hardware Keys 102 – 108 can both identify the Computing Device 100 and communicate the attack status of the Computing Device 100. The plurality of hardware keys 102 – 108 may be programmed in a variety of ways, including but not limited to electronic fusing at the time of production, non-volatile RAM programmed at

the time of production or non-volatile RAM programmed by the Requesting Entity 116 when the Computing Device 100 connects to the Requesting Entity 116.

[0022] Each of the Hack Condition Indicators 120 – 124 is correlated with one of the "Attack" Keys 104 – 108. In one embodiment, the Hack Condition Indicators 120 – 124 may contain volatile storage elements such as static RAM or latches. In another embodiment, the Hack Condition Indicators 120 – 124 may contain non-volatile storage elements such as hardware fuses that are permanently blown. Those skilled in the art will recognize that embodiments combining volatile and non-volatile storage elements are possible and that other types of volatile and non-volatile storage elements may also be used. Although in this particular embodiment only three attack keys and hack condition indicators are illustrated, those skilled in the art will recognize that there may be any number of such "attack" keys and indicators, and they may correspond to any type of detectable attack or to an unknown attack.

[0023] A factor to consider in choosing whether to use volatile or non-volatile storage elements for particular hack condition indicators is the perceived seriousness of a particular kind of attack. Types of attacks that are perceived as less serious could be indicated using volatile storage elements, while types of attacks that are considered more serious could be indicated using non-volatile storage elements. In an exemplary embodiment, attacks targeting the security of the device such as attacks on the physical package, attempts to use a test interface such as a JTAG interface or a number of authentication failures above a predetermined threshold might be considered more serious and be indicated by blown hardware fuses while attacks which gain access to restricted features such as video or audio playback might be considered less serious and be indicated by setting static RAM bits that re-set when the cellular phone is power-cycled. Those skilled in the art will realize that for different types of computing devices,

different factors including but not limited to data sensitivity and potential financial loss from an attack may be relevant.

[0024] In one exemplary embodiment, the Computing Device 100 could be incorporated into a cellular phone. The Requesting Entity 116 could be the cellular network with which the cellular phone communicates. The "attack" keys could represent common attacks on cellular phone devices, including attempting to access a JTAG interface, taking the device outside its normal temperature or voltage ranges, attempting to execute untrusted code on the cellular phone's processor, attempting to gain access to features the user has not paid for, or operating the phone on an unauthorized network. The network provider could then take actions based on the type of attack such as, but not limited to denying the compromised phone access to the network, disabling certain software or features the user has not paid for, logging the location of the compromised phone, or logging information about the type of phone compromised.

[0025] In another exemplary embodiment, the Computing Device 100 could be coupled with an engine control computer of a vehicle. The Requesting Entity 116 could be maintained by the vehicle's manufacturer or a third party. In this case, the "attack" keys would represent conditions such as modified engine management software, speed above a certain threshold, whether the vehicle has been reported stolen, or long mileage intervals between required maintenance checks. The vehicle manufacturer could use that information to determine when warranty conditions had been violated or to provide more accurate information about vehicle usage to their service personnel.

[0026] FIG. 2 illustrates another embodiment of a Computing Device 200 which incorporates an Attack Detection Block 204. The Computing Device 200 is coupled to a Requesting Entity 202 via Reverse Link 212 and Forward Link 214. The Reverse Link 212 and Forward Link 214 may be a variety of connections including but not limited to

ethernet, wireless ethernet or a cellular wireless network protocol. The Computing Device 200 receives communications from the Requesting Entity 202 via the Forward Link 214 at an Interface 240. The Computing Device 200 contains a Programmed Hardware Key 206 which is used to authenticate the Computing Device 200 when challenged by the Requesting Entity 202. The Attack Detection Block 204 is coupled to a plurality of Hack Condition Indicators 218 – 222. The Programmed Hardware Key 206 and the Hack Condition Indicators 218 – 222 are coupled to a Key Generation Block 208. The Key Generation Block 208 and the Interface 240 are coupled to a Cryptographic Engine 210.

[0027] When an Attacker 230 makes an Attack 232 on the Computing Device 200, the Attack 232 is detected by the Attack Detection Block 204. In response to an Attack 232, the Attack Detection Block 204 sets one or more Hack Condition Indicators 218 – 222. The Attack Detection Block 204 may be configured to detect attacks such as but not limited to JTAG port attacks, voltage or temperature attacks, malicious software attacks, unauthorized use, attempts to access sensitive information or denial of service attacks. The Hack Condition Indicators 218 – 222 may be volatile storage elements such as static RAM or latches, or they may be non-volatile storage elements such as non-volatile RAM or hardware fuses. Those skilled in the art will recognize that embodiments combining volatile and non-volatile storage elements may also be used. Although in this particular embodiment only three hack condition indicators are illustrated, those skilled in the art will recognize that there may be any number of such "attack" indicators, and they may correspond to any type of detectable attack or to an unknown attack.

[0028] The Key Generation Block 208 combines the Programmed Hardware Key 206 and the Hack Condition Indicators 218 – 222 into a response key that communicates the

identity of the Computing Device 200 and information about any attacks against the Computing Device 200 to the Requesting Entity 202. For example, this may be accomplished by appending the Hack Condition Indicators 218 – 222 to the Programmed Hardware Key or by generating an encoding based on the state of the Hack Condition Indicators 218 – 222 and combining that encoding with the Programmed Hardware Key. Those skilled in the art will recognize that many different methods of combining the Programmed Hardware Key 206 and the Hack Condition Indicators 218 – 222 that preserve all the information contained in each exist, and the methods herein are presented by way of illustration and not limitation. After the Key Generation Block 208 has combined the Programmed Hardware Key 206 and the Hack Condition Indicators 218 – 222 to generate a response key, the Key Generation Block 208 provides the response key to the Cryptographic Engine 210.

[0029] The Requesting Entity 202 forms a request in a Request Formation Block 252. The Request Formation Block 252 may be a dedicated circuit, a general-purpose processor or a software program. The request may include a non-deterministic value generated by an entropy source including but not limited to a look-up table or a thermal noise generator. The Requesting Entity 202 provides the request over the Forward Link 214. Depending on the level of security desired, the request may be sent in the clear or may be mathematically transformed by methods including, but not limited to, masking or use of a cryptographic algorithm.

[0030] The Computing Device 200 receives the request including a non-deterministic value from the Requesting Entity 202 over the Forward Link 214 at the Interface 240. The Interface 240 provides the non-deterministic value to the Cryptographic Engine 210. The Cryptographic Engine 210 may be a dedicated hardware block, a general-purpose processor capable of performing cryptographic computations or a software

program contained in a computer-readable medium. The Cryptographic Engine 210 then generates a transformed key by combining the response key with the non-deterministic value received by the Computing Device 200 from the Requesting Entity 202 and mathematically transforming the combination. The Cryptographic Engine 210 may use mathematical transformations including, but not limited to cryptographic hash functions or cipher algorithms. The Computing Device 200 provides the transformed key to the Requesting Entity 202 over the Reverse Link 212.

[0031] The Requesting Entity 202 computes a list of possible values based on each possible value of the transformed key and stores the values in a Key Table 216. The Requesting Entity 202 may compute the list of possible values prior to transmitting the random value, in parallel with transmitting the random value or after the Requesting Entity 202 has received the transformed key back from the Computing Device 200. The Requesting Entity 202 receives the transformed key from the Computing Device 200 over the Reverse Link 212 at a Comparison Block 250. The Comparison Block 250 may be a dedicated circuit, a general-purpose processor or a software program. In the Comparison Block 250, the Requesting Entity 202 compares the transformed key to the values stored in the Key Table 216. The Requesting Entity 202 is thus able to determine whether or not the Computing Device 200 has been attacked from the particular transformed key received from the Computing Device 200. The Requesting Entity 202 is also able to gain information about the type of attack based on the particular transformed key received from the Computing Device 200.

[0032] FIG. 3 is an exemplary flow diagram illustrating how the Computing Device 100 may respond to a challenge from the Requesting Entity 116. Beginning in block 302, the Requesting Entity 116 generates a request including a non-deterministic value. In block 304, the Requesting Entity 116 computes all possible values of a cryptographic

hash function with which the Computing Device 100 could respond and stores those values in the Key Table 118. The Requesting Entity 116 may compute these values before transmitting the request including the non-deterministic value to the Computing Device 100, in parallel with transmitting the request including the non-deterministic value to the Computing Device 100 or after the Requesting Entity 116 has received the transformed key back from the Computing Device 100.

[0033] In block 320, the Requesting Entity 116 transmits the request including the non-deterministic value to the Computing Device 100 over the Forward Link 128. In block 322, the Computing Device 100 receives the request including the non-deterministic value at the Interface 140. In decision block 324, the Computing Device 100 evaluates whether it has detected any attacks upon itself. If an attack has not occurred, block 326 is reached. In block 326, the Computing Device 100 computes a value of the cryptographic hash function based on the non-deterministic value received from the Requesting Entity 116 and the "no attacks" key 102. If an attack has occurred, block 328 is reached. At block 328, the Computing Device 100 computes the value of the cryptographic hash function based on the non-deterministic value received from the Requesting Entity 116 and one of the plurality of "attack" keys 104 – 108. Next, in block 330 the Computing Device 100 transmits the value of the cryptographic hash function back to the Requesting Entity 116 over the Reverse Link 126. In block 332, the Requesting Entity 116 receives the value of the cryptographic hash function from the Computing Device 100.

[0034] In block 306, the Requesting Entity 116 compares the value of the cryptographic hash function received from the Computing Device 100 against all the possible values of the cryptographic hash function computed by the Requesting Entity 116 in block 304. Depending on which of the values from block 304 matches the value received in block

332, the Requesting Entity 116 can determine if any and what type of attack occurred and can take action if necessary. If no attacks have been detected, block 310 is reached and the challenge and response is ended. If some type of attack has been detected, in block 312 the Requesting Entity 116 can take action based on the type of attack. Responses to an attack could include denying the compromised computing device access to the network, disabling certain software or features, logging the location of the compromised computing device, or logging information about the type of computing device compromised. Those skilled in the art will realize that many different responses are possible, and those discussed here in the context of the exemplary embodiment are for purposes of illustration and not limitation.

[0035] FIG. 4 is an exemplary flow diagram illustrating how the Requesting Entity 116 can respond to attacks of varying seriousness. In block 410, the Requesting Entity 116 determines that the Computing Device 100 has been attacked. In block 412, the Requesting Entity 116 determines the seriousness of the attack. The seriousness of the attack can be based on factors including but not limited to the particular feature of the Computing Device 100 that has been compromised, whether the attack was hardware or software based, or whether any of the Computing Device 100 user's personal information stored on the device has been compromised. If the attack is determined not to be serious, block 414 is reached in which the Requesting Entity 116 may take action, including but not limited to logging the attack and continuing with normal operation. If the attack is determined to be serious, block 416 is reached in which the Requesting Entity 116 provides a location request to the Computing Device 100.

[0036] In block 418, the Computing Device 100 receives the location request from the Requesting Entity 116. In block 420, the Computing Device 100 formulates a response based on its location and at least a portion of a programmed hardware key. The

response may be formulating by methods including but not limited to performing a mathematical transformation such as a one-way hash on the portion of the programmed hardware key and at least a portion of the location information. The location of the Computing Device 100 may be determined, for example, by use of a GPS receiver integrated into the Computing Device 100, virtual GPS or signal triangulation. Those skilled in the art will recognize that other mathematical transformations and methods of determining the location of the Computing Device 100 may be used. The response may be sent in the clear or alternatively it may be encrypted. Once the Computing Device 100 has formulated the response, in block 422 it provides the response back to the Requesting Entity 116.

[0037] In block 424, the Requesting Entity 116 receives the response from the Computing Device 100. In block 426, the Requesting Entity 116 can process the location information received in the response including decryption if necessary and take further actions if desired. Such actions may include but are not limited to generating a log, sending an alert or remotely deactivating the Computing Device 100.

[0038] While the teachings of the present disclosure are disclosed in the context of unauthorized access to a consumer computing device, it will be recognized that a wide variety of implementations may be employed by persons of ordinary skill in the art consistent with the teachings herein and the claims which follow below.

CLAIMS

WHAT IS CLAIMED IS:

1. A method of securely communicating location information from a computing device, comprising:
   a. receiving a location request from a requesting entity;
   b. acquiring location information at the computing device;
   c. forming a response based on the location information and at least a portion of a hardware key; and
   d. providing the response to the requesting entity.
2. The method of claim 1, wherein forming the response comprises mathematically transforming the portion of a hardware key and at least a portion of the location information.
3. The method of claim 2, wherein the mathematical transformation is a one-way hash function.
4. A method of securely receiving information about a location of a computing device at a requesting entity, comprising:
   a. forming a location request at the requesting entity;
   b. providing the location request to the computing device;
   c. receiving a response from the computing device based on the location of the computing device and at least a portion of a hardware key; and
   d. determining the location of the computing device from the response.
5. The method of claim 4, wherein the location request at the requesting entity is formed only if the requesting entity determines that the computing device has been attacked.

6. The method of claim 5, wherein the location request at the requesting entity is formed only if the requesting entity determines that an attack on the computing device is of a serious nature.

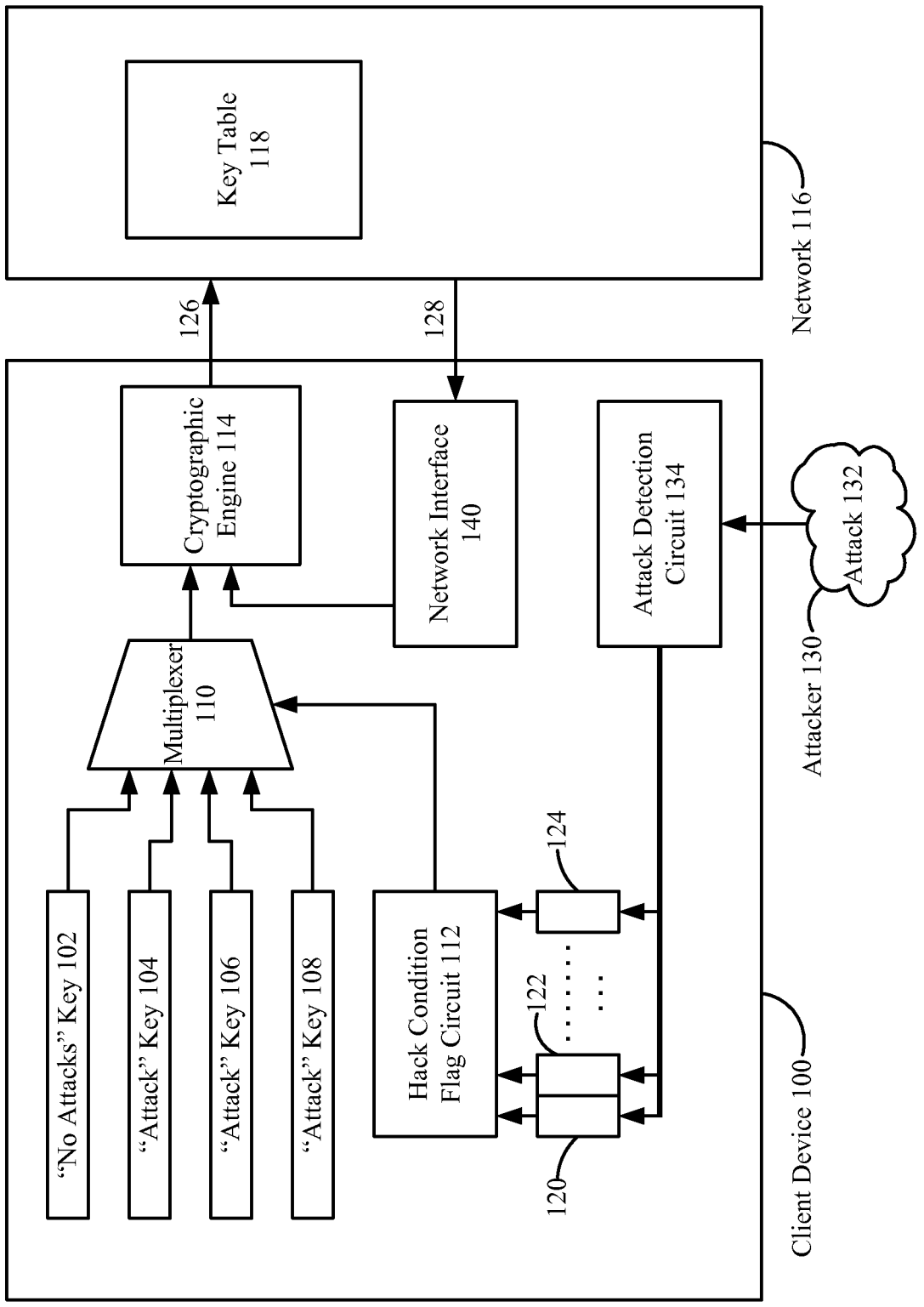
Key Table 118
Network 116
126
128
Cryptographic Engine 114
Network Interface 140
Attack Detection Circuit 134
Attack 132
Attacker 130
Multiplexer 110
"No Attacks" Key 102
"Attack" Key 104
"Attack" Key 106
"Attack" Key 108
Hack Condition Flag Circuit 112
124
122
120
Client Device 100

FIG. 1

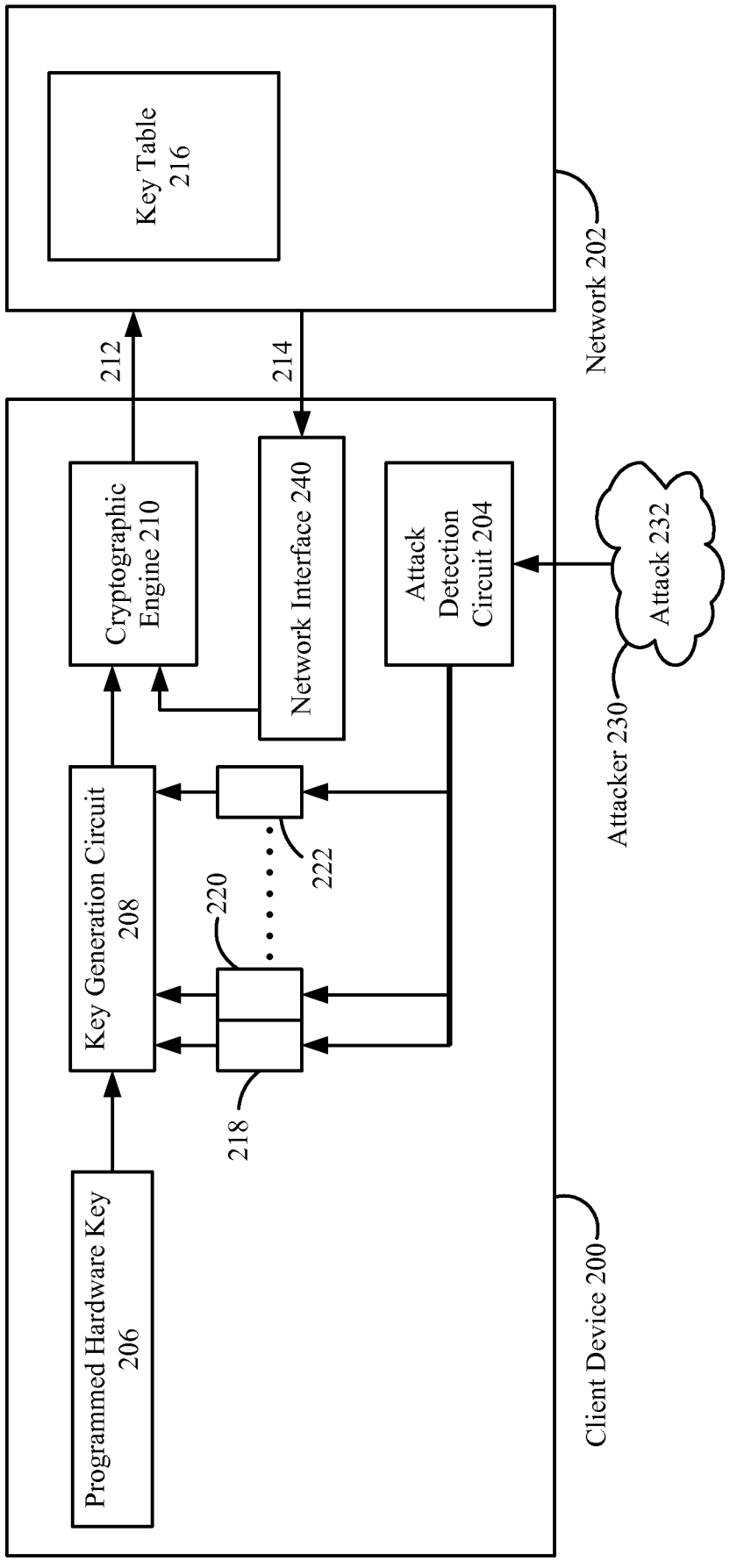
Key Table
216
Network 202
212
214
Cryptographic
Engine 210
Network Interface 240
Attack
Detection
Circuit 204
Attack 232
Attacker 230
Key Generation Circuit
208
220
222
218
Programmed Hardware Key
206
Client Device 200

FIG. 2

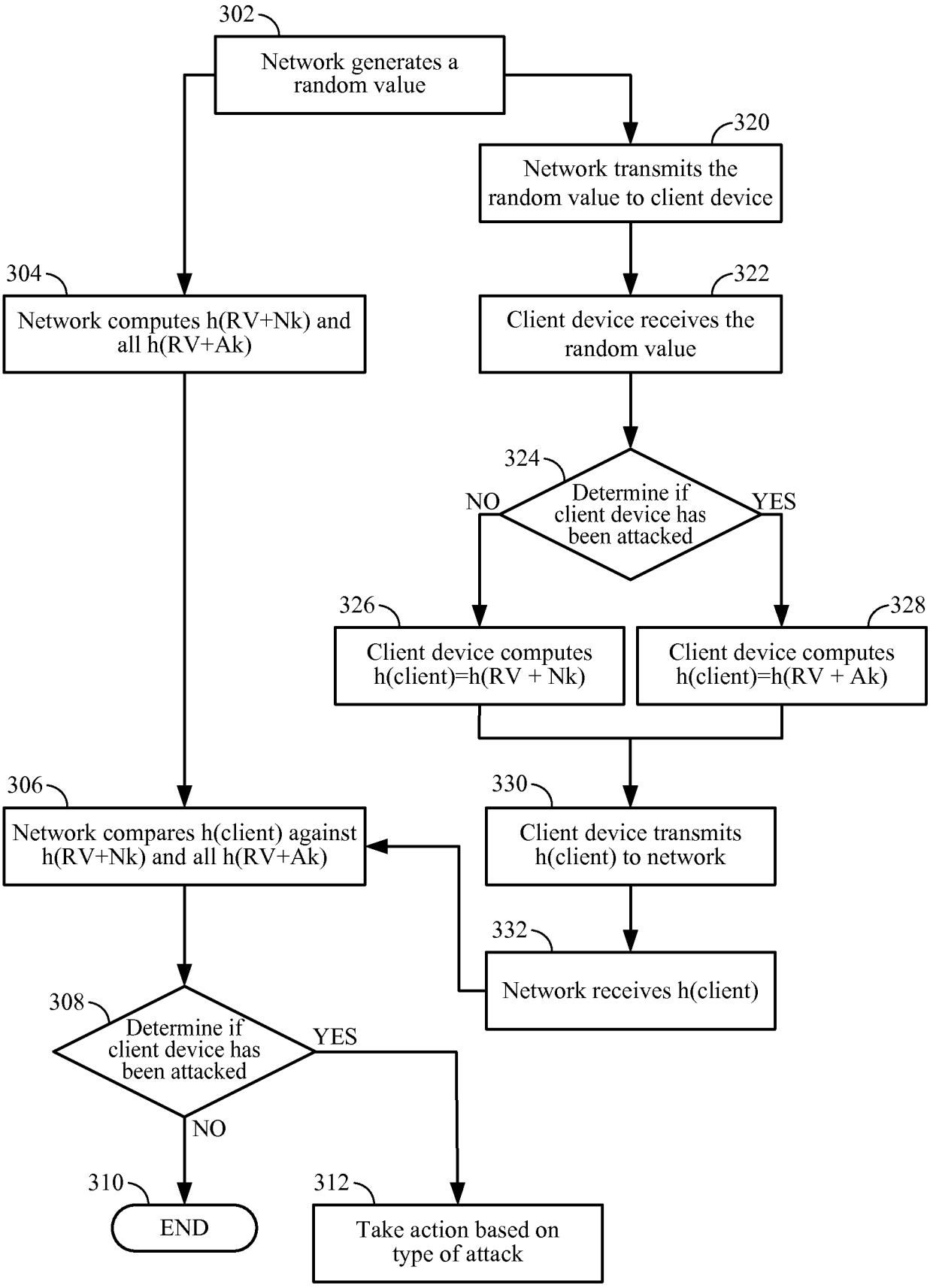
302
Network generates a random value
320
Network transmits the random value to client device
304
Network computes h(RV+Nk) and all h(RV+Ak)
322
Client device receives the random value
324
Determine if client device has been attacked
NO
YES
326
Client device computes h(client)=h(RV + Nk)
328
Client device computes h(client)=h(RV + Ak)
306
Network compares h(client) against h(RV+Nk) and all h(RV+Ak)
330
Client device transmits h(client) to network
332
Network receives h(client)
308
Determine if client device has been attacked
YES
NO
310
END
312
Take action based on type of attack

FIG. 3

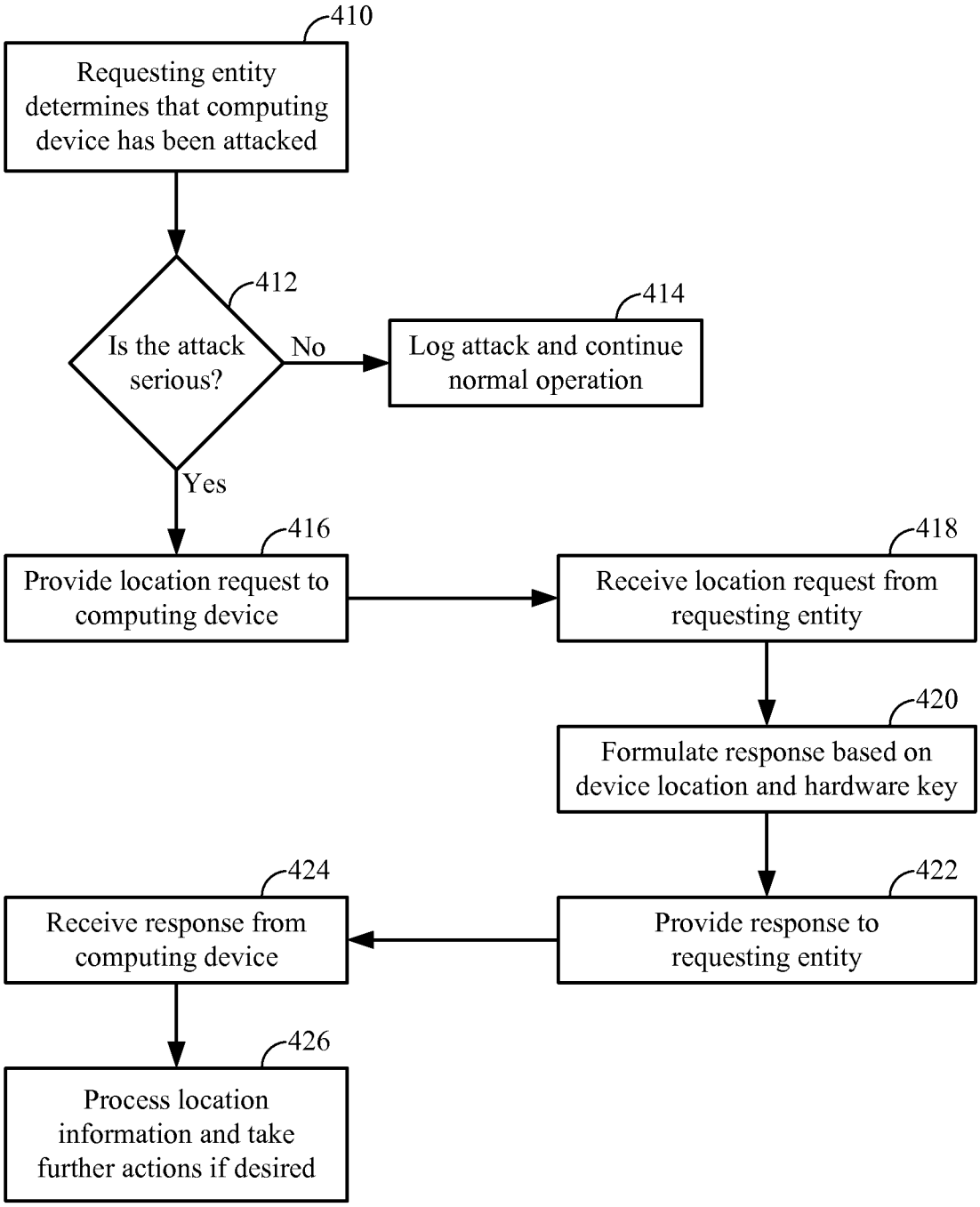
410
Requesting entity determines that computing device has been attacked
412
Is the attack serious?
No
414
Log attack and continue normal operation
Yes
416
Provide location request to computing device
418
Receive location request from requesting entity
420
Formulate response based on device location and hardware key
422
Provide response to requesting entity
424
Receive response from computing device
426
Process location information and take further actions if desired

FIG. 4

INTERNATIONAL SEARCH REPORT

International application No
PCT/US2009/056074

A. CLASSIFICATION OF SUBJECT MATTER
INV. G06F21/00 G06F21/02

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)
EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 748 084 A (ISIKOFF JEREMY M [US]) 5 May 1998 (1998-05-05) abstract; figures 1,3 column 1, line 10 - column 2, line 25 column 3, line 5 - column 5, line 11 column 8, line 62 - column 9, line 14 column 9, line 57 - column 10, line 31	1-6
A	GB 2 377 788 A (WIZARD MOBILE SOLUTIONS LTD [GB]) 22 January 2003 (2003-01-22) the whole document	1-6
A	WO 01/93531 A2 (INVICTA NETWORKS INC [US]) 6 December 2001 (2001-12-06) the whole document	1-6

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
20 November 2009	27/11/2009

Name and mailing address of the ISA/	Authorized officer
European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Powell, David

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No
PCT/US2009/056074

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5748084 A	05-05-1998	NONE	
GB 2377788 A	22-01-2003	CN 101082887 A	05-12-2007
		GB 2377776 A	22-01-2003
		GB 2379834 A	19-03-2003
WO 0193531 A2	06-12-2001	AU 6520701 A	11-12-2001