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[Continued on next page]

(54) Title: INDUCTIVE ISOLATION OF CAPACITIVE LOAD IN AMPLITUDE LIMITERS

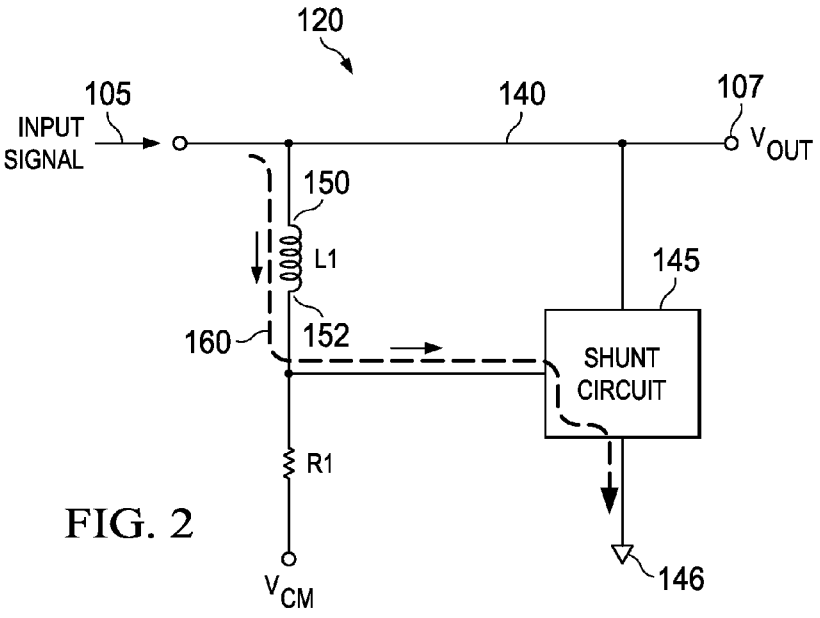

FIG. 2

(57) Abstract: An amplitude limiter circuit 120 includes an inductor LI and a shunt circuit 145. The inductor has a first terminal 150 connected to an input node. The shunt circuit is connected to a second terminal 152 of the inductor and also is connected to a low impedance node 146. If an overvoltage condition forms on the input node, the shunt circuit forms an overvoltage current path 160 from the input node, through the inductor, through the shunt circuit and to low impedance node.

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TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

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— *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

PatSearch (RUPTO internal), USPTO, PAJ, Esp@cenet, DWPI, EAPATIS, PATENTSCOPE, Information Retrieval System of FIPS

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	SU 374701 A1 (BATALIN E. S. et al.) 20.03.1973	1-18
A	US 8188682 B2 (MAXIM INTEGRATED PRODUCTS, INC.) 29.05.2012	1-18
A	US 4750061 A (CANON KABUSHIKI KAISHA) 07.06.1988	1-18

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

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26 August 2014 (26.08.2014)	18 September 2014 (18.09.2014)
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