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(54) Title: RAPID PULSE ELECTROHYDRAULIC (EH) SHOCKWAVE GENERATOR APPARATUS WITH IMPROVED ELECTRODE LIFETIME

(57) Abstract: Apparatuses, capacitor arrays, and methods for generating therapeutic compressed acoustic waves (e.g., shock waves). In the apparatuses and at least some of the methods, a plurality of electrodes can be disposed in a chamber that is defined by a housing and configured to be filled with liquid, and a plurality of capacitors can be electrically connected to the electrodes and can be carried by (e.g., physically coupled to) the housing. Voltage pulses can be applied simultaneously to the plurality of electrodes (e.g., to begin to vaporize and ionize portions of the liquid to provide at least one inter-electrode conductive path between the plurality of electrodes) and to the capacitors to charge the plurality of capacitors). The plurality of capacitors can be configured to, upon reaching a threshold charge, discharge to the plurality of electrodes (e.g., to generate one or more arcs along the one or more inter-electrode conductive paths to vaporize additional portions of the liquid and generate one or more acoustic shock waves). In the capacitor arrays, a plurality of capacitors can be coupled to the one or more circuit boards with a first portion of the capacitors arranged in a first pattern defined by a plurality of capacitor sets, a second portion of the plurality of capacitors can be arranged in a second pattern defined by a plurality of capacitor sets, with the sets defining the first pattern connected in parallel, the sets defining the second pattern connected in parallel, and the circuit board(s) can be configured to be coupled to an electrode such that the electrode is in electrical communication with the capacitors and is fixed in at least two degrees of freedom relative to the one or more circuit boards.



RAPID PULSE ELECTROHYDRAULIC (EH) SHOCKWAVE GENERATOR
APPARATUS WITH IMPROVED ELECTRODE LIFETIME

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the benefit of U.S. Provisional application No. 62/365,099 filed July 21, 2016, the content of which is incorporated into the present application by reference.

FIELD OF INVENTION

[0002] The present invention relates generally to therapeutic uses for shock waves or shockwaves. More particularly, but not by way of limitation, the present invention relates to an apparatus for generating therapeutic shock waves or shockwaves (shock waves with therapeutic uses) with improved electrode lifetime.

DESCRIPTION OF RELATED ART

[0003] Acoustic shockwaves have been used for certain therapies for a number of years. "Shock wave" or "shockwave" is generally used to refer to an acoustic phenomenon (e.g., resulting from an explosion or lightning) that creates a sudden and intense change in pressure. These intense pressure changes can produce strong waves of energy that can travel through elastic media such as air, water, human soft tissue, or certain solid substances such as bone, and/or can induce an inelastic response in such elastic media. Methods for creating shock waves for therapeutic uses include: (1) electrohydraulic, or spark gap (EH); (2) electromagnetic, or EMSE; and (3) piezoelectric. Each is based upon its own unique physical principles.

A. Devices and Systems for Shockwave Generation

[0004] US Patent Application 13/574,228 (a national-stage application of PCT/US2011/021692, which published as WO 2011/091020), by one of the present inventors, discloses a device for producing shock waves at a high pulse rate using a transducer. That device includes an acoustic-wave generator configured to emit acoustic waves having at least one frequency between 1 MHz and 1000 MHz; a shockwave housing coupled to the acoustic-wave generator; and a shockwave medium disposed in the shockwave housing; where the apparatus is configured such that if the acoustic-wave generator emits

acoustic waves then at least some portion of the acoustic waves will travel through the shockwave medium and form shock waves. That device can be actuated to form shock waves configured to cause particles within a patient to rupture one or more cells of the patient, and the shock waves can be directed to cells of a patient such that the shock waves cause particles to rupture one or more of the cells. This acoustic-transducer device can produce high powered shockwaves at high frequencies or pulse rates.

[0005] Additionally, US Patent Application 13/798712, also by the present inventors, discloses apparatuses and methods for electrohydraulic generation of shockwaves at a rate of 10 Hz and 5 MHz comprising: a housing defining a chamber and a shockwave outlet; a liquid disposed in the chamber; a plurality of electrodes (e.g., in a spark head or module) configured to be disposed in the chamber to define one or more spark gaps; and a pulse-generation system configured to apply voltage pulses to the electrodes at a rate of between 10 Hz and 5 MHz.

[0006] Other systems for producing shockwaves can include an electrohydraulic (EH) wave generator. EH systems can generally deliver similar levels of energy as other methods, but may be configured to deliver that energy over a broader area, and therefore deliver a greater amount of shock wave energy to targeted tissue over a shorter period of time. EH systems generally incorporate an electrode (i.e., a spark plug) to initiate a shock wave. In EH systems, high energy shock waves are generated when electricity is applied to an electrode immersed in treated water contained in an enclosure. When the electrical charge is fired, a small amount of water is vaporized at the tip of the electrode and the rapid, nearly instantaneous, expansion of the vaporized water creates a shock wave that propagates outward through the liquid water. In some embodiments, the water is contained in an ellipsoid enclosure. In these embodiments, the shock wave may ricochet from the sides of the ellipsoid enclosure and converge at a focal point that coincides with the location of the area to be treated.

[0007] For example, U.S. Patent No 7,189,209 (the '209 Patent) describes a method of treating pathological conditions associated with bone and musculoskeletal environments and soft tissues by applying acoustic shock waves. The '209 Patent describes that shockwaves induce localized trauma and cellular apoptosis therein, including micro-fractures, as well as to induce osteoblastic responses such as cellular recruitment, stimulate formation of molecular bone, cartilage, tendon, fascia, and soft tissue morphogens and growth factors, and to induce vascular neoangiogenesis. The '209 Patent claims several specific

implementations of its method. For instance, the '209 Patent claims a method of treating a diabetic foot ulcer or a pressure sore, comprising: locating a site or suspected site of the diabetic foot ulcer or pressure sore in a human patient; generating acoustic shock waves; focusing the acoustic shock waves throughout the located site; and applying more than 500 to about 2500 acoustic shock waves per treatment to the located site to induce micro-injury and increased vascularization thereby inducing or accelerating healing. The '209 Patent discloses a frequency range of approximately 0.5-4 Hz, and application of about 300 to 2500 or about 500 to 8,000 acoustic shock waves per treatment site, which can result in a treatment duration for each treatment site and/or a "total time per treatment" for all sites that is inconveniently large. For example, the '209 Patent discloses total times per treatment for different examples ranging from 20 minutes to 3 hours.

[0008] U.S. Patent 5,529,572 (the '572 Patent) includes another example of the use of electro-hydraulically generated shockwaves to produce therapeutic effect on tissues. The '572 Patent describes a method of increasing the density and strength of bone (to treat osteoporosis), comprising subjecting said bone to substantially planar, collimated compressional shock waves having a substantially constant intensity as a function of distance from a shock wave source, and wherein said collimated shock waves are applied to the bone at an intensity of 50-500 atmospheres. The '572 Patent describes the application of unfocussed shock waves to produce dynamic repetitive loading of the bone to increase mean bone density, and thereby strengthen bone against fracture. As described in the '572 Patent, "the unfocussed shock waves preferably are applied over a relatively large surface of the bone to be treated, for example to cover an area of from 10 to 150 cm². The intensity of the shock waves may be from 50-500 atmospheres. Each shock wave is of duration of a few microseconds, as in a conventional lithotripter, and is preferably applied at a frequency of 1-10 shock waves per second for a period of 5-30 minutes in each treatment. The number of treatments depends on the particular patient."

[0009] U.S. Patent Application No. 10/415, 293 (the '293 Application), which is also published as US 2004/0006288, discloses another embodiment of the use of EH-generated shockwaves to provide a therapeutic effect on tissues. The '293 Application discloses a device, system, and method for the generation of therapeutic acoustic shock waves for at least partially separating a deposit from a vascular structure. The '293 Application describes that the device can produce shockwaves at a pulse rate of about 50 to about 500 pulses per minute

(i.e., 0.83 to 8.33 Hz) with a number of pulses per treatment site (in terms of per length of vascular unit being treated) from about 100 to about 5,000 per 1 cm².

B. Shockwave Rate

[0010] Prior art literature has indicated that faster pulse rates using EH systems to provide shockwaves can lead to tissue damage. For example, in one study (Delius, Jordan, & et al, 1988) [2], the effect of shock waves on normal canine kidneys was examined in groups of dogs whose kidneys were exposed to 3000 shockwaves. The groups differed only in the rate of shockwave administration which was 100 Hz and 1 Hz, respectively. Autopsy was performed 24 to 30 hours later. Macroscopically and histologically, significantly more hemorrhages occurred in kidney parenchyma if shockwaves were administered at a rate of 100 Hz (vs 1 Hz). The results showed that kidney damage is dependent on the rate of shockwave administration.

[0011] In another study (Madbouly & et al, 2005) [7], slow shockwave lithotripsy rate (SWL) was associated with a significantly higher success rate at a lower number of total shockwaves compared to the fast shockwave lithotripsy rate. In this paper, the authors discussed how human studies have also shown a decrease in the incidence of SWL induced renal injury or need for anesthesia when slower rates of test SWL were used.

[0012] In yet another study (Gillitzer & et al, 2009) [5], slowing the delivery rate from 60 to 30 shockwaves per minute also provides a dramatic protective effect on the integrity of renal vasculature in a porcine model. These findings support potential strategies of reduced pulse rate frequency to improve safety and efficacy in extracorporeal shockwave lithotripsy.

[0013] Soft tissues may transition from elastic to viscous behavior for pulse rates (PRs) between 1 Hz and 10 Hz. As a result, potential damage to tissue from shockwaves at PRs between 1 Hz and 10 Hz is unpredictable when typical lithotripsy power levels are used. Perhaps as a result, the prior art teaches slower PRs and large total times per treatment (TTPT). For example, currently known EH shockwave systems generally deliver PRs of less than 10 Hz and require large total times per treatment (TTPT) (e.g., TTPT periods of minutes or even hours for even a single treatment site). When, as may be typical, a treatment requires repositioning of a device at multiple treatment sites, the TTPT becomes large and potentially impractical for many patients and treatment needs.

[0014] While long treatment times may be acceptable for extracorporeal shockwave lithotripsy, the use of shockwaves to provide non-lithotripsy therapeutic effects on tissue in the medical setting is less than optimal if not impractical. For example, the cost of treatment often increases with the time needed to administer a treatment (e.g., due to the labor, facilities and other resource costs allocated to the administration of the treatment). Furthermore, in addition to costs, at some point the duration of providing treatment to the patient becomes unbearable for the patient receiving, and healthcare staff providing, the treatment.

SUMMARY

[0015] This disclosure includes embodiments of apparatuses and methods for electrohydraulic generation of rapid acoustic pulses that have improved electrode lifetime. In certain embodiments, this improved electrode lifetime is achieved by utilizing a two stage pulse discharge approach to shock wave generation. According to these embodiments, in the first stage, the pulse-generation system is configured to simultaneously apply voltage pulses to the plurality of electrodes in the electrode chamber such that portions of the liquid contained therein are vaporized to provide an inter-electrode conductive path; and, to apply voltage pulses to a plurality of capacitors located adjacent to said electrodes to charge said plurality of capacitors. In the second stage, the charged plurality of capacitors discharge to the electrodes to generate a short inter-electrode arc, through the established inter-electrode conductive path, resulting in an acoustic shock wave. The short inter-electrode arc minimizes electrode erosion leading to improved electrode lifetime.

[0016] The improved lifetime of the electrodes is the result of the fast discharge of the capacitors located adjacent to the electrodes within the chamber. The pulse-generation system is configured to simultaneously apply voltage pulses to the plurality of electrodes in the electrode chamber such that portions of the liquid are vaporized to provide an inter-electrode conductive path; and, to apply voltage pulses to the plurality of capacitors located adjacent to said electrodes to charge said plurality of capacitors. In one embodiment, the plurality of capacitors comprises at least 10 planar capacitors in parallel wherein each capacitor has a capacitance of no greater than 100 nanofarad. In one embodiment, the plurality of planar capacitors is placed on a plurality of stacked circuit boards adjacent to the electrodes and wherein the plurality of planar capacitors is placed on opposing sides of each stackable circuit board in a low-inductance pattern. Locating these capacitors adjacent to the electrodes

enables the arc to discharge completely and quickly. Once the capacitors are discharged, the inter-electrode arc ends, which minimizes electrode erosion.

[0017] Some embodiments of the present apparatuses (e.g., for generating therapeutic shock waves) comprise: a housing defining a chamber and a shockwave outlet; a liquid disposed in the chamber; a plurality of electrodes configured to be disposed in the chamber to define one or more spark gaps; a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes; and a pulse-generation system configured to be coupled to the plurality of electrodes such that: (i) the housing is movable relative to the pulse-generation system, and (ii) the pulse-generation system is in electrical communication with the plurality of electrodes and the plurality of capacitors; where the pulse-generation system is configured to apply voltage pulses simultaneously to: the plurality of electrodes (e.g., to begin to vaporize and ionize portions of the liquid to provide at least one inter-electrode conductive path between the plurality of electrodes, and the plurality of capacitors to charge the plurality of capacitors); and where the plurality of capacitors are configured to, upon reaching a threshold charge, discharge to the plurality of electrodes to generate one or more arcs along the one or more inter-electrode conductive paths to vaporize additional portions of the liquid and generate one or more acoustic shock waves.

[0018] In some embodiments of the present apparatuses, the pulse-generation system is configured to provide an inter-electrode conductive path by applying voltage to charge the plurality of capacitors during the period that the pulse generation system applies voltage to the plurality of electrodes.

[0019] Some embodiments of the present apparatuses (e.g., for generating therapeutic shock waves) comprise: a housing defining a chamber and a shockwave outlet, the chamber being configured to be filled with a liquid; a plurality of electrodes configured to be disposed in the chamber to define one or more spark gaps; a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes; and a pulse-generation system configured to be coupled to the plurality of electrodes such that: (i) the housing is movable relative to the pulse-generation system, and (ii) the pulse-generation system is in electrical communication with the plurality of electrodes and the plurality of capacitors; where the pulse-generation system is configured to apply voltage pulses simultaneously to: the plurality of electrodes (e.g., to begin to vaporize and ionize portions of the liquid to provide at least one inter-electrode conductive path between the plurality of electrodes), and the plurality of capacitors to charge the plurality of capacitors; where the

plurality of capacitors are configured to, upon reaching a threshold charge, discharge to the plurality of electrodes to generate one or more arcs along the one or more inter-electrode conductive paths to vaporize additional portions of the liquid and generate one or more acoustic shock waves.

5 **[0020]** Some embodiments of the present apparatuses (e.g., for generating therapeutic shock waves) comprise: a housing defining a chamber and a shockwave outlet, the chamber being configured to be filled with a liquid; a plurality of electrodes configured to be disposed in the chamber to define one or more spark gaps; a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes; and where the
10 plurality of electrodes is configured to be coupled to a pulse-generation system such that: (i) the housing is movable relative to the pulse-generation system, and (ii) the pulse-generation system is in electrical communication with the plurality of electrodes and the plurality of capacitors such that the plurality of electrodes and the plurality of capacitors can simultaneously receive voltage pulses from the pulse-generation system; and where the
15 plurality of capacitors are configured to, upon reaching a threshold charge, discharge to the plurality of electrodes.

[0021] In some embodiments of the present apparatuses, each of the plurality of capacitors is planar. In some embodiments, the plurality of capacitors are arranged in a circuit having an overall inductance of between 2 nH and 200 nH. In some embodiments, the
20 plurality of capacitors comprises between 2 and 20 sets of capacitors with the sets of capacitors connected in parallel. In some embodiments, each set of capacitors comprises fewer than 50 capacitors. In some embodiments, each set of capacitors comprises 10 or more capacitors in series.

[0022] In some embodiments of the present apparatuses, each capacitor has a
25 capacitance of no greater than 100 nanofarad.

[0023] In some embodiments of the present apparatuses, the plurality of capacitors is coupled to a plurality of stackable circuit boards. In some embodiments, the plurality of capacitors are arranged in a plurality of circular patterns. In some embodiments, the plurality of stackable circuit boards comprises a first stackable circuit board, and a second stackable
30 circuit board coupled to the first stackable circuit board. In some embodiments, a first portion of the plurality of capacitors is coupled to the first stackable circuit board, and a second portion of the plurality of capacitors is coupled to the second stackable circuit board.

In some embodiments, the first portion of the plurality of capacitors is disposed on a first side of a first stackable circuit board, and the second portion of the plurality of capacitors is disposed on a second side of a second stackable circuit board, and the second side of the second circuit board is opposite the first side of the first stackable circuit board. In some
5 embodiments, the first stackable circuit board and the second stackable circuit board are circular. In some embodiments, a first portion of the plurality of capacitors is coupled to the first stackable circuit board and a second portion of the plurality of capacitors is coupled to the second stackable circuit board. In some embodiments, the first portion of the plurality of capacitors is coupled to the first stackable circuit board in a circular pattern; and the second
10 portion of the plurality of capacitors is coupled to the second stackable circuit board in a circular pattern. In some embodiments, each set of capacitors comprises 10 or more capacitors in series. In some embodiments, the first stackable circuit board further comprises an outer edge and a center, the second stackable circuit board further comprises an outer edge and a center; and the first portion of the plurality of capacitors is configured to cause current
15 to flow from the outer edge of the first stackable circuit board towards the center of the first stackable circuit board, and the second portion of the plurality of capacitors is configured to cause current to flow from the outer edge of the second stackable circuit board towards the center of the second stackable circuit board. In some embodiments, the first stackable circuit board is electrically coupled to the second stackable circuit board by connectors disposed
20 along the outer edges of the stackable circuit boards. In some embodiments, the plurality of stackable circuit boards each have a thickness of between 0.02 inches and 0.2 inches.

[0024] In some embodiments of the present apparatuses, the plurality of capacitors each have length of between 2 mm and 4 mm, and a width of between 1 mm and 3 mm.

[0025] In some embodiments of the present apparatuses, the plurality of capacitors
25 comprises at least 100 capacitors.

[0026] Some embodiments of the present capacitor-array apparatus (e.g., for use in generating therapeutic shock waves) comprise: one or more circuit boards; and a plurality of capacitors coupled to the one or more circuit boards; where a first portion of the capacitors is arranged in a first pattern defined by a plurality of capacitor sets, a second portion of the
30 plurality of capacitors is arranged in a second pattern defined by a plurality of capacitor sets, each capacitor set comprises two or more of the capacitors connected in series; the capacitor sets defining the first pattern are connected in parallel, and the capacitor sets defining the second pattern are connected in parallel; and where the one or more circuit boards are

configured to be coupled to an electrode such that the electrode is in electrical communication with the capacitors and is fixed in at least two degrees of freedom relative to the one or more circuit boards.

[0027] In some embodiments of the present capacitor-array apparatuses, the plurality of capacitors are planar. In some embodiments, the plurality of capacitors are arranged in a circuit having an overall inductance of between 2 nH and 200 nH. In some embodiments, the plurality of capacitors comprises between 2 and 20 sets of capacitors with the sets of capacitors connected in parallel. In some embodiments, each set of capacitors comprises fewer than 50 capacitors.

[0028] In some embodiments of the present capacitor-array apparatuses, each set of capacitors comprises 10 or more capacitors in series.

[0029] In some embodiments of the present capacitor-array apparatuses, each capacitor has a capacitance of no greater than 100 nanofarads.

[0030] In some embodiments of the present capacitor-array apparatuses, the one or more circuit boards comprises a plurality of stackable circuit boards. In some embodiments, the first and second patterns are circular. In some embodiments, the plurality of stackable circuit boards comprises a first stackable circuit board, and a second stackable circuit board coupled to the first stackable circuit board. In some embodiments, the first portion of the capacitors is coupled to the first stackable circuit board, and the second portion of the capacitors is coupled to the second stackable circuit board. In some embodiments, the first portion of the capacitors is disposed on a first side of a first stackable circuit board, and the second portion of the plurality of capacitors is disposed on a second side of a second stackable circuit board, and the second side of the second circuit board is opposite the first side of the first stackable circuit board. In some embodiments of the present capacitor-array apparatuses, the first portion of the plurality of capacitors is coupled to the first stackable circuit board in a circular pattern; and the second portion of the plurality of capacitors is coupled to the second stackable circuit board in a circular pattern. In some embodiments, each set of capacitors further comprises 10 or more capacitors connected in parallel. In some embodiments, the first stackable circuit board further comprises an outer edge and a center, the second stackable circuit board further comprises an outer edge and a center; and the first portion of the plurality of capacitors is configured to cause current to flow from the outer edge of the first stackable circuit board towards the center of the first stackable circuit board,

and the second portion of the plurality of capacitors is configured to cause current to flow from the outer edge of the second stackable circuit board towards the center of the second stackable circuit board. In some embodiments, the first stackable circuit board is electrically coupled to the second stackable circuit board by connectors disposed along the outer edges of the stackable circuit boards. In some embodiments, the plurality of stackable circuit boards each have a thickness of between 0.02 inches and 0.2 inches.

[0031] In some embodiments of the present capacitor-array apparatuses, the plurality of capacitors each have length of between 2 mm and 4 mm, and a width of between 1 mm and 3 mm.

[0032] In some embodiments of the present capacitor-array apparatuses, the plurality of capacitors comprises at least 100 capacitors.

[0033] Some embodiments of the present methods (e.g., of producing a compressed acoustic wave using an apparatus for generating therapeutic shock waves), comprising: applying voltage pulses to a plurality of electrodes in a chamber defined by a housing and filled with liquid such that portions of the liquid begin to vaporize and ionize to provide an inter-electrode conductive path; applying voltage to a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes to charge the plurality of capacitors; and upon the plurality of capacitors reaching a threshold charge, discharging the plurality of capacitors to the electrodes to generate an inter-electrode arc along the established inter-electrode conductive path and thereby generate of at least one acoustic shock wave. In some embodiments, the voltage pulses applied to the plurality of electrodes is between 500 V and 10,000 volts (V). In some embodiments, the voltage pulses applied to the plurality of capacitors is between 500 V and 10,000 V.

[0034] The term “coupled” is defined as connected, although not necessarily directly, and not necessarily mechanically; two items that are “coupled” may be unitary with each other. The terms “a” and “an” are defined as one or more unless this disclosure explicitly requires otherwise. The term “substantially” is defined as largely but not necessarily wholly what is specified (and includes what is specified; e.g., substantially 90 degrees includes 90 degrees and substantially parallel includes parallel), as understood by a person of ordinary skill in the art. In any disclosed embodiment, the terms “substantially,” “approximately,” and “about” may be substituted with “within [a percentage] of” what is specified, where the

percentage includes .1, 1, 5, and 10 percent. In the disclosed embodiment, the term “adjacent” is generally defined located in the same discrete chamber, housing, or module.

[0035] The terms “comprise” (and any form of comprise, such as “comprises” and “comprising”), “have” (and any form of have, such as “has” and “having”), “include” (and any form of include, such as “includes” and “including”) and “contain” (and any form of contain, such as “contains” and “containing”) are open-ended linking verbs. As a result, a system or apparatus that “comprises,” “has,” “includes” or “contains” one or more elements possesses those one or more elements, but is not limited to possessing only those elements. Likewise, a method that “comprises,” “has,” “includes” or “contains” one or more steps possesses those one or more steps, but is not limited to possessing only those one or more steps.

[0036] Further, a structure (e.g., a component of an apparatus) that is configured in a certain way is configured in at least that way, but it can also be configured in other ways than those specifically described.

[0037] Any embodiment of any of the present systems, apparatuses, and methods can consist of or consist essentially of – rather than comprise/include/contain/have – any of the described steps, elements, and/or features. Thus, in any of the claims, the term “consisting of” or “consisting essentially of” can be substituted for any of the open-ended linking verbs recited above, in order to change the scope of a given claim from what it would otherwise be using the open-ended linking verb.

[0038] Details associated with the embodiments described above and others are presented below.

BRIEF DESCRIPTION OF THE DRAWINGS

[0039] The following drawings illustrate by way of example and not limitation. For the sake of brevity and clarity, every feature of a given structure is not always labeled in every figure in which that structure appears. Identical reference numbers do not necessarily indicate an identical structure. Rather, the same reference number may be used to indicate a similar feature or a feature with similar functionality, as may non-identical reference numbers. The figures are drawn to scale (unless otherwise noted), meaning the sizes of the depicted elements are accurate relative to each other for at least the embodiment depicted in the figures.

[0040] FIG. 1 is a graph illustrating an acoustic wave from prior art electrohydraulic systems.

[0041] FIG. 2A depicts stage 1 of a pulse generation system: inter-electrode saline heating and initial vaporization.

5 [0042] FIG. 2B depicts stage 2 of a pulse generation system: inter-electrode vapor ionization.

[0043] FIG. 2C depicts stage 3 of a pulse generation system: inter-electrode arc formation.

10 [0044] FIG. 2D depicts stage 4 of a pulse generation system: inter-electrode intense arc.

[0045] FIG. 3 depicts a schematic diagram of one embodiment of an electrohydraulic shock wave generation system for use in or with some embodiments of the present systems.

[0046] FIG. 4A-4E depict various views of one embodiment of a stackable circuit board comprising a plurality of capacitors.

15 [0047] FIGS. 5A-5E depict various views of a second embodiment of a capacitor array affixed to a pair of coupled stackable circuit boards.

[0048] FIGS. 6A-6D depict various views of a capacitor array affixed to a pair of coupled stackable circuit boards and coupling components.

20 [0049] FIGS. 7A-7C depict various views of a capacitor array affixed to a pair of stackable circuit boards coupled to a pair of electrodes in a shock wave generation chamber.

[0050] FIGS. 8A and 8B depict the reduced electrode wear resulting from the use of one embodiment of the present apparatus when compared to prior art systems.

25 [0051] FIG. 9 depicts a graph illustrating a comparison of compressed acoustic wave from an embodiment of the present apparatus and an acoustic wave from a prior art apparatus.

[0052] FIG. 10 depicts an exploded perspective view of a further prototyped embodiment of the present probes having a spark head or module.

[0053] FIGS. 11A and 11B depict parts of the assembly of the probe of FIG. 10.

[0054] FIGS. 12A and 12B depict perspective and side cross-sectional views, respectively of the probe of FIG. 10.

[0055] FIG. 12C depicts an enlarged side cross-sectional view of a spark gap of the probe of FIG. 10.

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DESCRIPTION OF ILLUSTRATIVE EMBODIMENTS

[0056] Certain embodiments of the present systems and apparatuses are configured to generate high-frequency shock waves while having an improved electrode lifetime. In some embodiments, the generated EH shock waves can be used in medical and/or aesthetic therapeutic applications (e.g., when directed at and/or delivered to target tissue of a patient).

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Examples of medical and/or aesthetic therapeutic applications in which the present systems can be used are disclosed in: (1) U.S. Patent Application No. 13/574,228, published as US 2013/0046207; (2) U.S. Patent Application No. 13/547,995, published as, published as US 2013/0018287; and (3) U.S. Patent Application No. 13/798,710, published as US 2014/0257144, each of which are incorporated here in their entireties.

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[0057] In one embodiment, the apparatus for electrohydraulic generation of shockwaves comprises: a housing defining a chamber and a shockwave outlet; a liquid disposed in the chamber; a plurality of electrodes (e.g., in the spark head or module) configured to be disposed in the chamber to define one or more spark gaps; and a pulse generation system configured to apply voltage pulses to the electrodes at a rate of between 10 Hz and 5 MHz. The rate of voltage pulses may be at rates of 25 Hz, 50 Hz, 75 Hz, 100 Hz, 150 Hz, 200 Hz, 250 Hz, 300 Hz, 400 Hz, 500 Hz, 600 Hz, 700 Hz, 800 Hz, 900 Hz, 1 KHz, 5 KHz, 10 KHz, 25 KHz, 50 KHz, 100 KHz, 200 KHz, 300 KHz, 400 KHz, 500 KHz, 600 KHz, 700 KHz, 800 KHz, 900 KHz, 1 MHz, 2 MHz, 3 MHz, and 4 MHz.

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A. *Prior Art Systems*

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[0058] Referring now to the drawings, FIG. 1 depicts a typical pulse discharge from prior art electrohydraulic systems which produce a broad frequency spectrum acoustic wave (typically in the range of 16 Hz to 30 MHz) consisting of a large compressive pulse wave **100**, followed by a small tensile wave **102**. The compressive pulse wave **100** consists of two parts: a fast rise acoustic front **104** (also referred to as a shock wave front) followed by a long compressive acoustic tail **106**. The fast acoustic front **104** occurs on a time scale of

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nanoseconds whereas the long compressive acoustic tail **106** occurs on a time scale of microseconds.

[0059] Such prior art electrohydraulic systems create a pulse discharge event between two electrodes that takes place in four stages: (1) inter-electrode saline heating and initial vaporization; (2) vapor ionization; (3) inter-electrode arc formation; and (4) intense arc.

[0060] **FIG. 2A** depicts Stage 1 of the prior art pulse discharge event: inter-electrode saline heating and initial vaporization. During this stage of the pulse, a chamber **200** is filled with saline **202**. Next, a pulse-generation system applies voltage directly to the electrodes **204**, **206** to produce an inter-electrode conductive path **208**. Specifically, current **210** is conducted through the bulk amount of saline **202** from one electrode **204** to another **206**. This results in the saline **202** being heated resulting in portions of the saline **202** being vaporized at initial bubble nucleation sites located on the surface tips of the electrodes **204**, **206**. Because the electrical conductivity of saline increases with temperature, during this stage the electrode current rises as the temperature of the saline increases. At this stage there is no electrode damage during the saline heating and initial vaporization. The current is approximately evenly distributed across the surface tips of the electrodes **204**, **206** and the temperature of the saline is low (up to approximately 100° C) while overall impedance is high (approximately 50 Ω for 1% saline).

[0061] **FIG. 2B** depicts Stage 2 of the prior art pulse discharge event: inter-electrode vapor ionization, which overlaps with Stage 1 as depicted in **FIG. 2A**. During this stage of the pulse, current **210** is still being primarily conducted through the bulk amount of saline **202** from one electrode **204** to another **206**. Saline **202** continues to vaporize and expand from the initial bubble nucleation sites. Once the saline **202** vaporizes and its density is low enough, the increased free paths of the electrons allow them to acquire the energy sufficient for collisional ionization, and avalanche plasma discharges **212** are formed. As with Stage 1, negligible damage to the electrode occurs during this stage. Ion bombardment can cause electrode material removal through sputtering, but rates are extremely low when compared to Stages 3 and 4 of the pulse discharge event. Overall impedance is high (approximately 50 Ω for 1% saline).

[0062] **FIG. 2C** depicts Stage 3 of the prior art pulse discharge event: inter-electrode arc formation. During this stage of the pulse, multiple events happen almost simultaneously. The discharge through the saline vapor plasma layer causes cathode and anode spots to form

on the surfaces of the electrodes. These tiny, intense jets of electrode material and electrons supply the conductive material necessary to form a full arc **214**. The jets emanating from the cathode and anode spots begin to connect and transition to the intense arc of Stage 4. The net current across the electrodes **204**, **206** begins to spike as the initial arc **214** causes rapid and complete saline vaporization and arc spread. Overall impedance begins to drop from approximately 50 Ω to 0.1 Ω .

[0063] FIG. 2D depicts Stage 4 of the prior art pulse discharge event: inter-electrode intense arc., The intense arc mode **216** is very bright and appears to cover the anode and cathode, and fill the electrode gap **218**. Another anode and cathode spots are present and are continuously ejecting electrode material into the gap **218** which supplies the feeder material for the low-impedance arc. The intense arc mode **216** produced by prior art pulse-generation systems is characterized by severe erosion at the anode and cathode [1]. The arc voltage is low and the current is high, due to the low overall impedance (approximately 0.1 Ω). Anode erosion is typically more severe than cathode erosion because the anode spots tend to be fewer and more intense, while the cathode spots are more numerous and distributed [1].

[0064] The severe erosion of the electrodes **204**, **206** using prior art electrohydraulic systems limits the lifetime of the electrodes in those systems. Because many applications for electrohydraulic systems require large numbers or fast rates of pulses to be effective, the prior art approaches for generating these acoustic waves result in a lowering the limited lifetime of the electrodes **204**, **206** requiring either frequent electrode replacement or the use of an expensive, complicated electrode feeder system. Due to the limited electrode lifetime, these requirements have constrained electrohydraulic systems' commercial usefulness.

B. Improved Systems, Components, and Methods

[0065] Certain embodiments of the present apparatuses and methods are configured to electrohydraulically generate shockwaves while providing improved electrode lifetime. Certain embodiments achieve improved electrode lifetime by utilizing a two stage pulse discharge approach to shockwave generation. In some embodiments, in the first stage, the pulse-generation system is configured to simultaneously: (1) apply voltage pulses to a plurality of electrodes in an electrode chamber such that a portion of a liquid contained within the chamber are vaporized to provide an inter-electrode conductive path; and (2) apply voltage pulses to charge a plurality of capacitors located adjacent to the plurality of electrodes. In such embodiments, in the second stage, the charged plurality of capacitors

discharge to generate short inter-electrode arc through the established inter-electrode conductive path resulting in an acoustic shockwave. A shorter inter-electrode arc can minimize electrode erosion, and thereby lead to improved electrode lifetime.

[0066] In electrohydraulic shockwave generation, high capacitance may be required to obtain the required peak pulse current with the desired waveform at the electrodes. In some of the present embodiments, large capacitors may be disposed close to the electrodes may be able to provide the high voltage pulse to the electrodes necessary to produce a short inter-electrode arc. However, the use of repeated large voltage and current phase discharges required to generate pulse shockwaves may cause damage to large capacitors, which may in turn lead to shockwave generator failure. The capacitor damage sustained in these prior art systems is theorized to be secondary to the piezoelectric effect of the capacitor plates leading to mechanical failure. This problem can limit the ability to produce a commercially viable rapid pulse shockwave generator that has an electrode lifetime of acceptable length.

[0067] In some of the present embodiments, a plurality of small capacitors in parallel, arranged (e.g., in a low-inductance pattern) adjacent to the electrodes (e.g., in or on a handheld housing in which the electrodes are disposed) can be used to produce a short inter-electrode arc. In this embodiment, a plurality of small capacitors in parallel, arranged in a low-inductance pattern adjacent to electrodes is able to provide the repeated and rapid large voltage and current pulse discharges required to generate rapid pulse shockwaves without damage to the capacitors. The piezoelectric effect on the materials for each small capacitor is limited when used within the plurality of small capacitors in parallel to generate rapid pulse shockwaves. As a result, in such embodiments, catastrophic capacitor mechanical failure is avoided, thereby improving the commercial viability of rapid pulse shockwave generators.

[0068] In some of the present embodiments, a plurality of small capacitors in parallel may be placed in a plurality of stacked circuit boards so as to condense the area required for the capacitors. Additionally, placing the plurality of small capacitors on opposing sides of each stackable circuit board results not only in further reduction of surface area required for the capacitors, but also a reduction of the inductance caused by the use of the plurality of capacitors.

[0069] **FIG. 3** depicts a representative schematic of one embodiment of the disclosed electrohydraulic apparatus. In the embodiment shown, a pulse-generation system **300** is coupled to a head **302** by a cable **304**. The head **302** includes a plurality of electrodes **306**

configured to define one or more spark gap **308**, and a plurality of capacitors **310** (e.g., with the electrodes and capacitors carried by a housing). As described below, the capacitors may, for example, be configured in a low-inductance pattern. In some such embodiments, the housing or body of the head **302** defines a housing within which the plurality of electrodes **306** is disposed (e.g., with a portion of each electrode extending into the chamber), and the plurality of capacitors **310** is carried by the housing (and/or may be disposed in a chamber **312**). The chamber **312** is configured to be filled with a liquid. In the embodiment shown, pulse-generation system **300** comprises a high voltage power supply **314**, a capacitor **316**, a primary switch **318**, a current probe **320**, a resistor **322**, an inductor **324**, and a voltage probe **326**. The high voltage power supply **314** may for example, be configured to supply 3000 volts (V). The pulse-generation system **300** is configured to apply voltage pulses to the plurality of electrodes **306** such that portion of the liquid disposed in the chamber **312** are vaporized to provide an inter-electrode conductive path. The pulse-generation system **300** is also configured to (e.g., simultaneously) apply voltage to the plurality of capacitors **310** within the chamber. Once charged, the plurality of capacitors **310** can discharge within the established inter-electrode conductive path to produce a short inter-electrode discharge arc. This discharge arc then results in the formation of a shockwave.

[0070] In some embodiments, such as the one shown in **FIGS. 4A-4E**, at least a portion of the plurality of capacitors **310** is coupled to a stackable circuit board **400** in a circular, low inductance pattern on both the top side **408** and the bottom side **406** of the stackable circuit board **400**. **FIG. 4A** depicts a bottom-up view of one embodiment of a stackable circuit board **400** having a plurality of capacitors **310** coupled to the bottom side **406** of the stackable circuit board **400**. In the embodiment shown, the stackable circuit board **400** is circular, having an outer edge **402** and an center aperture **404**. Surrounding the center aperture **404**, the stackable circuit board **400** has a plurality of additional apertures **410** and a plurality of pins **412**. In this embodiment, fourteen (14) pins **412** are coupled to the stackable circuit board **400**. Other embodiments may include 5, 6, 7, 8, 9, 10, 11, 12, 13, 15, 16, 17, 18, 19, 20 or more pins **412** surrounding the center aperture **404**. Pins **412** may, for example, be pogo pins or other connectors configured to establish, at least temporarily, an electrical connection between multiple circuit boards. Additionally, in the embodiment shown, the stackable circuit board **400** has a plurality of board-to-board connectors **414** running around its outer edge **402**. Connectors **414** may be arranged in single row as shown, or in two rows, and facilitate electrically coupling the stackable circuit board **400** with additional circuit

boards. Connectors **414** may, for example, be configured to operate across a range of temperatures between -55° C and 125° C.

[0071] In the embodiment shown, capacitors **310** are coupled to stackable circuit boards **400** in a low inductance pattern. As shown, a low inductance pattern of capacitors may comprise a plurality of sets of capacitors, each set of capacitor comprising a plurality of individual capacitors. In the low inductance pattern, the sets of capacitors are arranged such that each set is in parallel with each other set. According to one embodiment, as shown in **FIGS. 4A-4E**, each set of capacitors is coupled to the stackable circuit board **400** such that one capacitor is coupled to the board **400** near the center aperture **404** and a plurality of additional capacitors are coupled to the board **400** such that they are in electrical communication with one another and extend radially away from the center aperture **404** towards the outer edge **402**. This portion of capacitors from the set is further configured such that they are in electrical communication with an additional portion of capacitors situated on the opposing side of the board (or another board, as shown in **FIGS. 6A-6D**). This additional portion of capacitors is similarly configured such that they extend in series from the edge of the board **402** towards the center aperture **404**. According to the embodiment described, the overall configuration of capacitors is such that multiple sets of capacitors, each with a portion of the overall plurality of capacitors, extend from the center aperture **404** outward to the center edge **402**, continue to the opposite side of the board (or to another board), then extend from the edge of the board **402** back towards the center aperture **404**. Capacitors **310**, when so configured, may cause current to flow from the outer edge **402** of the stackable circuit board **400** towards the center aperture **404** or from the center aperture **404** of the stackable circuit board **400** towards the outer edge **402**. Such a configuration has been shown to result in reduced inductance across the entire capacitor array. For example, in some such embodiments, certain sets of the capacitors are configured to cause current to flow radially inward, and others of the sets of capacitors are configured to cause current to flow radially outward, resulting in “counter flows” of current that tend to cancel out or otherwise (e.g., via destructive interference), inductance during use. In some embodiments, portions of the capacitors are coupled to each of a plurality of stackable circuit boards, which may include 2, 3, 4, 5, or more individual boards. Portions of the plurality of capacitors may be coupled to either side—or both sides—of any of the stackable circuit boards. As shown, a stackable circuit board **400** may be circular in shape, and may have a carve out **416** extending inward from outer edge **402** toward the center aperture.

[0072] In one embodiment, at least ten (10) planar capacitors in parallel, each having a capacitance of no greater than 100 nanoFarads (nF), are able to provide the repeated large voltage pulse discharges required to generate rapid pulse shockwaves without damage to the capacitors. In other embodiments, a minimum of 15, 20, 25, 30, 35, 40 45, or 50 planar capacitors may be used in parallel. Additionally, according to other embodiments, each capacitor may have a maximum capacitance of 95 nF, 90 nF, 85 nF, 80 nF, 75 nF, 70 nF, 65 nF, 60 nF, 55 nF, or 50 nF. In one embodiment, the capacitors each have a length of between 2 mm and 4 mm, and a width of between 1 mm and 3 mm.

[0073] In embodiments in which the capacitors are arranged in sets of capacitors, plurality of capacitors may be arranged in between 2 and 20 sets of capacitors, with the sets connected in parallel (e.g., and the capacitors within each set connected in series). Alternatively, the plurality of capacitors may comprise 2, 5, 10, or 15 sets of capacitors. In some embodiments, each set of capacitors comprises fewer than 50 capacitors, but may alternatively comprise 5, 10, 15, 20, 25, 30, 35, 40, or 45 capacitors per set. In some embodiments, the plurality of capacitors comprises at least 100 capacitors. In some embodiments, the plurality of capacitors are arranged in a circuit having an overall inductance of between 2 nH and 200 nH.

[0074] FIGS. 5A-5E depict perspective, cross-sectional, top, and side views of one embodiment of the present assemblies of stackable circuit boards including a capacitor array for use in shockwave pulse generating apparatuses. FIG. 5A depicts a perspective view of one embodiment of the present stackable circuit board assemblies; FIG. 5B depicts another perspective view of the assembly; FIG. 5C depicts a side cross-sectional view of the assembly; FIG. 5D depicts a top view of the assembly; and FIG. 5E depicts a side view of the assembly. As shown, in this assembly, circuit board 400 is coupled to the second stackable circuit board 500 via connectors 414 such that capacitors 310 of circuit board 400 are electrically connected to second stackable circuit board 500 via the connectors (414). Circuit board 400 is also mechanically coupled to circuit board 500 via a central hub assembly 502. According to this embodiment, Circuit board 500 provides the low-inductance return path from the central pin to the outermost row of capacitors 310.

[0075] FIGS. 6A-6D depict perspective, cross-sectional, and exploded perspective views of another embodiment of the present capacitor array for use in the rapid therapeutic shockwave generation apparatuses and methods. FIG. 6A depicts a perspective view of the capacitor array; FIG. 6B depicts a second perspective view of the capacitor array; FIG. 6C

depicts a cross-sectional view of the capacitor array; and **FIG. 6D** depicts an exploded view of the capacitor array. In this embodiment, the plurality of capacitors **310** is placed on a first stacked circuit board **400** and a second stacked circuit board **500**, adjacent to a plurality of electrodes wherein the plurality of small capacitors **310** is placed on opposing sides of each stackable circuit board **400**, **500** in a low-inductance pattern. The circuit boards **400**, **500** are both electrically coupled to each other via board-to-board connectors **414** and mechanically coupled to each other via a central mechanical assembly **502**.

[0076] In the embodiment shown, locating the plurality of capacitors **310** near the electrodes enables the arc to be discharged completely and quickly. Once the capacitors **310** within the chamber head (as illustrated by the embodiment depicted in **FIG. 3**) are discharged, the inter-electrode arc ends, minimizing electrode erosion.

[0077] In some embodiments, the improved lifetime of the electrodes is the result of the discharge of the plurality of capacitors **310** near the electrodes. Locating the plurality of capacitors **310** near the electrodes in a low inductance pattern provides the capacitor/electrode setup with an overall low inductance. As a result, the plurality of capacitors **310** within the chamber is able to be discharge completely and quickly.

[0078] As shown, the central mechanical assembly **502** comprises a contact ring **600**, a ring adapter **602**, a spacer **604**, a replacement pin socket **606**, a center pin **608**, and a plurality of nuts **610**. The ring adapter **602** may have a plurality of teeth **612** that are configured to be inserted into apertures in the second stackable circuit board **500** such that the teeth **612** prevent the second stackable circuit board **500** from rotating independent from the ring adapter **602**.

[0079] In the embodiment shown, the capacitors may be configured to cause current to flow from the center of the second stackable circuit board **500** towards its outer edge, through the board-to-board connectors **414** to the outer edge of the first stackable circuit board **400** and from there to the center of the first stackable circuit board **400**. Each stackable circuit board **400**, **500** may have a thickness of between 0.02 and 0.2 inches. Alternatively, the boards **400**, **500** may have thicknesses of between 0.03 and 0.125 inches, or between 0.04 and 0.1 inches.

[0080] **FIGS. 7A-7C** depict cross-sectional and side views of one embodiment of the disclosed capacitor array coupled shockwave generation chamber. According to the embodiment as shown in **FIG. 7A**, the capacitor array **700** is coupled to plurality of

electrodes comprising a proximal electrode 702 and a distal electrode 704. In this embodiment, both the proximal electrode 702 and the distal electrode 704 are disposed in a chamber 706, which is configured to be filled with liquid. In at least one embodiment, the chamber 706 is configured to be filled with saline. In yet another embodiment, the chamber 706 is filled with saline. The electrodes 702, 704 are configured to have a short gap between them defining the discharge location 708. The capacitor array 700, along with the coupled electrodes 702, 704 and chamber 706, is configured to perform the two stage discharge approach to shockwave generation. In the first stage, the pulse-generation system is configured to simultaneously: (1) apply voltage pulses to a plurality of electrodes 702, 704 in an electrode chamber 706 such that a portion of a liquid contained within the chamber 706 is vaporized to provide an inter-electrode conductive path in the discharge location 708; and (2) apply voltage pulses to charge a plurality of capacitors located adjacent to the plurality of electrodes 702, 704 in the capacitor array 700. According to this embodiment, in the second stage, the charged plurality of capacitors discharge to generate short inter-electrode arc through the established inter-electrode conductive path in the discharge location 708 resulting in an acoustic shockwave.

[0081] In some embodiments, using a two stage pulse discharge approach to generating shock waves results in a short inter-electrode arc times that minimizes electrode erosion, leading to improved electrode lifetime. Electrohydraulic systems that use a single stage pulse discharge approach (for example, where the pulse generation system applies voltage pulses directly to the electrodes to sequentially form the inter-electrode conductive path, and then generate the inter-electrode arc) suffer from long discharge arc times, and therefore significant electrode erosion. This significant electrode erosion leads to an electrohydraulic shockwave apparatus with short electrode lifetime, increasing the time and expenses necessary for maintenance.

[0082] For example, FIGS. 8A and 8B depict photos comparing an electrode used by a prior art system compared to an electrode implementing the disclosed system. FIG. 8A depicts one embodiment of an electrode run with a prior art pulsed power supply using the single stage approach. In contrast, FIG. 8B depicts an electrode run with one embodiment of the two-stage pulsed generation system disclosed herein. As can be seen by comparing FIGS. 8A and 8B, the electrode run using the prior art pulsed power supply (FIG. 8A) showed significant erosion after less than 100 pulses. Large cratering indicates bulk electrode melt due to extended severe arc duration resulting from the single stage prior art system. Contrary

to the electrode implementing the prior art system, the electrode run with the two-stage pulse generation system (**FIG. 8B**) demonstrated only minimal erosion after 6,200 pulses. The electrode implementing the two-stage system had a wear rate reduction of 15X when compared to that of implementing the prior art system. For example, at equivalent pulse rates, the electrodes depicted in **FIG. 8A** coupled to a prior art pulse-generation system exhibited a wear rate of approximately 3,750 micro-inches per minute, whereas the electrodes depicted in **FIG. 8B** coupled to one of the present, inventive two-stage pulse-generation approaches (including a pulse-generation system, and a housing-carried capacitor array), exhibited a wear rate of only 250 micro-inches per minute.

[0083] Additionally, according to one embodiment, apparatuses and method for electrohydraulic generation of shockwaves using the two-stage approach disclosed herein generate acoustic waves that are “compressed” when compared to those waves generated by prior art systems. **FIG. 9** depicts a graph illustrating the pressure over time of an acoustic wave generated by both the prior art system **900** as well as an acoustic wave generated by the proposed two-stage approach **902**. As can be seen from **FIG. 9**, in comparison to the prior art system, the acoustic wave generated by the two-stage approach has a faster rise acoustic front **904** than that of the prior art approach. More importantly, the long acoustic tail **906** is significantly compressed as a result of the fast capacitor discharge time into an already established inter-electrode conductive path. Finally, the two-stage approach puts more energy into the acoustical pulse and less total energy into the arc when compared to the prior art approach. Less total energy into the arc directly leads to improved electrode life.

[0084] Furthermore, the compressed acoustic waves depicted in **FIG. 9** are less painful and damaging when applied to tissue. The typical pulse discharge from prior art electrohydraulic systems produce a broad frequency spectrum acoustic wave, typically in the range of 16 Hz to 30 MHz. The long compressive tail **906** of the acoustic wave is composed of the lower frequency spectrum of the acoustic wave. These low frequency components, at the acoustic pressures that are typically used, are the main source of large cavitation bubbles. These large cavitation bubbles, when generated in tissue, result in pain and tissue damage. Due to the short capacitor discharge and the resulting fast arc, the long compressive tail **906** of the acoustic wave is compressed. As a result, large cavitation bubbles secondary to a long tail are minimized.

[0085] In one embodiment, the present shockwave generating systems and apparatuses incorporate the probes depicted in **FIGS. 10-12C**. In this embodiment, probe

1000 comprises: a housing **1002** defining a chamber **1004** and a shockwave outlet **1006**; a liquid disposed in chamber **1004**; a plurality of electrodes **306** (e.g. in spark head or module **1008**) configured to be disposed in the chamber to define one or more spark gaps; and is configured to be coupled to a pulse generation system (**300**) configured to apply voltage pulses to the electrodes at a rate of between 10 Hz and 5 MHz.

[0086] In the embodiment shown, spark head **1008** includes a sidewall or body **1010** and a plurality of electrodes **306** that defined a spark gap. In this embodiment, probe **1000** is configured to permit liquid to be circulated through chamber **1004** via liquid connectors or ports **1012** and **1014**, one of which is coupled to the spark head **1008** and the other of which is coupled to housing **1002**, as shown. In this embodiment, housing **1002** is configured to receive spark head **1008**, as shown, such that housing **1002** and housing **1010** cooperate to define chamber **1004** (e.g., such that spark head **1008** and housing **1002** include a complementary parabolic surfaces that cooperate to define the chamber). In this embodiment, housing **1002** and spark head **1008** includes a channel **1016** (e.g., along a central longitudinal axis of spark head **1008**) extending between liquid connector **1012** and chamber **1004** and aligned with the spark gap between electrodes **306** such that circulating water will flow in close proximity and/or through the spark gap. In the embodiment shown, housing **1002** includes a channel **1018** extending between liquid connector **1014** and chamber **1004**. In this embodiment, housing **1010** includes a groove **1020** configured to receive a resilient gasket or O-ring **1022** to seal the interface between spark head **1008** and housing **1002**, and housing **1002** includes a groove **1024** configured to receive a resilient gasket or O-ring **1026** to seal the interface between housing **1002** and cap member **1028** when cap member **1028** is secured to housing **1002** by ring **1030** and restraining collar **1032**.

[0087] In the embodiment shown, electrodes **306** each includes a flat bar portion **1034** and a perpendicular cylindrical portion **1036** (e.g., comprising tungsten for durability) in electrical communication (e.g., unitary with) bar portion **1034** such that cylindrical portion **1036** can extend through a corresponding opening **1038** in spark head **1008** into chamber **1004**, as shown. In some embodiments, part of the sides of cylindrical portion **1036** can be covered with an electrically insulative and/or resilient material (e.g., shrink wrap) such as, for example, to seal the interface between portion **1036** and housing **1010**. In this embodiment, housing **1010** also includes longitudinal grooves **1038** configured to receive bar portions **1034** of electrodes **306**. In the embodiment shown, housing **1002** also includes set screws **1040** positioned to align with cylindrical portions **1036** of electrodes **306** when spark head

1008 is disposed in housing **1000**, such that set screws **1040** can be tightened to press cylindrical portions **1036** inward to adjust the spark gap between the cylindrical portions of electrodes **306**. In some embodiments, spark head **1008** is permanently adhered to housing **1002**; however, in other embodiments, spark head **1008** may be removable from housing **1002** such as, for example, to permit replacement of electrodes **306** individually or as part of a new or replacement spark head **1008**.

[0088] The above specification and examples provide a description of the structure and use of exemplary embodiments. Although certain embodiments have been described above with a certain degree of particularity, or with reference to one or more individual embodiments, those skilled in the art could make numerous alterations to the disclosed embodiments without departing from the scope of this invention. As such, the various illustrative embodiments of the present devices are not intended to be limited to the particular forms disclosed. Rather, they include all modifications and alternatives within the scope of the claims, and embodiments other than the one shown may include some or all of the features of the depicted embodiment. For example, components may be combined as a unitary structure. Further, where appropriate, aspects of any of the described examples may be combined with aspects of any of the other described examples to form further examples with comparable or different properties and addressing the same or different problems. Similarly, the benefits and advantages described above may relate to one embodiment or may relate to several embodiments.

[0089] The claims are not intended to include, and should not be interpreted to include, means-plus- or step-plus-function limitations, unless such a limitation is explicitly recited in a given claim using the phrase(s) “means for” or “step for,” respectively.

References

The following references, to the extent that they provide exemplary procedural or other details supplementary to those set forth herein, are specifically incorporated herein by reference.

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- 15 [6] U.S. Patent No. 4,005,314 entitled “Short pulse generator” by M. Zinn
- [7] German Patent No. DE 3150430 C1 entitled “Circuit for generating an underwater discharge” by G. Heine, *et al.*
- [8] U.S. Patent No. 3,604,641 entitled “Apparatus for hydraulic crushing” by B.R. Donoghue, *et al.*

CLAIMS

1. An apparatus for generating therapeutic shock waves, comprising:
 - a housing defining a chamber and a shockwave outlet;
 - a liquid disposed in the chamber;
 - a plurality of electrodes configured to be disposed in the chamber to define one or more spark gaps;
 - a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes; and
 - a pulse-generation system configured to be coupled to the plurality of electrodes such that: (i) the housing is movable relative to the pulse-generation system, and (ii) the pulse-generation system is in electrical communication with the plurality of electrodes and the plurality of capacitors;where the pulse-generation system is configured to apply voltage pulses simultaneously to:
 - the plurality of electrodes to begin to vaporize and ionize portions of the liquid to provide at least one inter-electrode conductive path between the plurality of electrodes, and
 - the plurality of capacitors to charge the plurality of capacitors;where the plurality of capacitors are configured to, upon reaching a threshold charge, discharge to the plurality of electrodes to generate one or more arcs along the one or more inter-electrode conductive paths to vaporize additional portions of the liquid and generate one or more acoustic shock waves.
2. An apparatus for generating therapeutic shock waves, comprising:
 - a housing defining a chamber and a shockwave outlet, the chamber being configured to be filled with a liquid;
 - a plurality of electrodes configured to be disposed in the chamber to define one or more spark gaps;
 - a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes; and
 - a pulse-generation system configured to be coupled to the plurality of electrodes such that: (i) the housing is movable relative to the pulse-generation system, and

- (ii) the pulse-generation system is in electrical communication with the plurality of electrodes and the plurality of capacitors;
where the pulse-generation system is configured to apply voltage pulses simultaneously to:
the plurality of electrodes to begin to vaporize and ionize portions of the liquid to provide at least one inter-electrode conductive path between the plurality of electrodes, and
the plurality of capacitors to charge the plurality of capacitors;
where the plurality of capacitors are configured to, upon reaching a threshold charge, discharge to the plurality of electrodes to generate one or more arcs along the one or more inter-electrode conductive paths to vaporize additional portions of the liquid and generate one or more acoustic shock waves.
3. An apparatus for generating therapeutic shock waves, comprising:
a housing defining a chamber and a shockwave outlet, the chamber being configured to be filled with a liquid;
a plurality of electrodes configured to be disposed in the chamber to define one or more spark gaps;
a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes; and
where the plurality of electrodes is configured to be coupled to a pulse-generation system such that: (i) the housing is movable relative to the pulse-generation system, and (ii) the pulse-generation system is in electrical communication with the plurality of electrodes and the plurality of capacitors such that the plurality of electrodes and the plurality of capacitors can simultaneously receive voltage pulses from the pulse-generation system; and
where the plurality of capacitors are configured to, upon reaching a threshold charge, discharge to the plurality of electrodes.
4. The apparatus of any of claims 1, 2, or 3, where each of the plurality of capacitors is planar.
5. The apparatus of claim 4, where the plurality of capacitors are arranged in a circuit having an overall inductance of between 2 nH and 200 nH.

6. The apparatus of claim 5, where the plurality of capacitors comprises between 2 and 20 sets of capacitors with the sets of capacitors connected in parallel.
7. The apparatus of claim 6, where each set of capacitors comprises fewer than 50 capacitors.
8. The apparatus of claim 6, where each set of capacitors comprises 10 or more capacitors in series.
9. The apparatus of claim 1, 2, or 3, where each capacitor has a capacitance of no greater than 100 nanofarad.
10. The apparatus of claim 1, 2, or 3, where the plurality of capacitors is coupled to a plurality of stackable circuit boards.
11. The apparatus of claim 10, where the plurality of capacitors are arranged in a plurality of circular patterns.
12. The apparatus of claim 10, where the plurality of stackable circuit boards comprises a first stackable circuit board, and a second stackable circuit board coupled to the first stackable circuit board.
13. The apparatus of claim 12, where a first portion of the plurality of capacitors is coupled to the first stackable circuit board, and a second portion of the plurality of capacitors is coupled to the second stackable circuit board.
14. The apparatus of claim 13, where the first portion of the plurality of capacitors is disposed on a first side of a first stackable circuit board, and the second portion of the plurality of capacitors is disposed on a second side of a second stackable circuit board, and the second side of the second circuit board is opposite the first side of the first stackable circuit board.
15. The apparatus of claim 14, where the first stackable circuit board and the second stackable circuit board are circular.
16. The apparatus of claim 15, where a first portion of the plurality of capacitors is coupled to the first stackable circuit board and a second portion of the plurality of capacitors is coupled to the second stackable circuit board.

17. The apparatus of claim 16, where:
the first portion of the plurality of capacitors is coupled to the first stackable circuit board in a circular pattern; and
the second portion of the plurality of capacitors is coupled to the second stackable circuit board in a circular pattern.
18. The apparatus of claim 17, where each set of capacitors comprises 10 or more capacitors in series.
19. The apparatus of claim 17, where the first stackable circuit board further comprises an outer edge and a center, the second stackable circuit board further comprises an outer edge and a center; and the first portion of the plurality of capacitors is configured to cause current to flow from the outer edge of the first stackable circuit board towards the center of the first stackable circuit board, and the second portion of the plurality of capacitors is configured to cause current to flow from the outer edge of the second stackable circuit board towards the center of the second stackable circuit board.
20. The apparatus of claim 19, where the first stackable circuit board is electrically coupled to the second stackable circuit board by connectors disposed along the outer edges of the stackable circuit boards.
21. The apparatus of claim 10, where the plurality of stackable circuit boards each have a thickness of between 0.02 inches and 0.2 inches.
22. The apparatus of claim 1 or 2, where the pulse-generation system is configured to provide an inter-electrode conductive path by applying voltage to charge the plurality of capacitors during the period that the pulse generation system applies voltage to the plurality of electrodes.
23. The apparatus of claim 1, 2, or 3, where the plurality of capacitors each have length of between 2 mm and 4 mm, and a width of between 1 mm and 3 mm.
24. The apparatus of claim 1, 2, or 3, where the plurality of capacitors comprises at least 100 capacitors.

25. A capacitor-array apparatus for use in generating therapeutic shock waves, comprising:

one or more circuit boards; and

a plurality of capacitors coupled to the one or more circuit boards;

where a first portion of the capacitors is arranged in a first pattern defined by a plurality of capacitor sets, a second portion of the plurality of capacitors is arranged in a second pattern defined by a plurality of capacitor sets, each capacitor set comprises two or more of the capacitors connected in series; the capacitor sets defining the first pattern are connected in parallel, and the capacitor sets defining the second pattern are connected in parallel; and

where the one or more circuit boards are configured to be coupled to an electrode such that the electrode is in electrical communication with the capacitors and is fixed in at least two degrees of freedom relative to the one or more circuit boards.

26. The apparatus of claim 25, where the plurality of capacitors are planar.

27. The apparatus of claim 25, where the plurality of capacitors are arranged in a circuit having an overall inductance of between 2 nH and 200 nH.

28. The apparatus of claim 26, where the plurality of capacitors comprises between 2 and 20 sets of capacitors with the sets of capacitors connected in parallel.

29. The apparatus of claim 28, where each set of capacitors comprises fewer than 50 capacitors.

30. The apparatus of claim 25, where each set of capacitors comprises 10 or more capacitors in series.

31. The apparatus of claim 25, where each capacitor has a capacitance of no greater than 100 nanofarads.

32. The apparatus of claim 25, where the one or more circuit boards comprises a plurality of stackable circuit boards.

33. The apparatus of claim 32, where the first and second patterns are circular.

34. The apparatus of claim 32 where the plurality of stackable circuit boards comprises a first stackable circuit board, and a second stackable circuit board coupled to the first stackable circuit board.
35. The apparatus of claim 34, where the first portion of the capacitors is coupled to the first stackable circuit board, and the second portion of the capacitors is coupled to the second stackable circuit board.
36. The apparatus of claim 35, where the first portion of the capacitors is disposed on a first side of a first stackable circuit board, and the second portion of the plurality of capacitors is disposed on a second side of a second stackable circuit board, and the second side of the second circuit board is opposite the first side of the first stackable circuit board.
37. The apparatus of claim 35, where:
the first portion of the plurality of capacitors is coupled to the first stackable circuit board in a circular pattern; and
the second portion of the plurality of capacitors is coupled to the second stackable circuit board in a circular pattern.
38. The apparatus of claim 37, where each set of capacitors further comprises 10 or more capacitors connected in parallel.
39. The apparatus of claim 37, where the first stackable circuit board further comprises an outer edge and a center, the second stackable circuit board further comprises an outer edge and a center; and the first portion of the plurality of capacitors is configured to cause current to flow from the outer edge of the first stackable circuit board towards the center of the first stackable circuit board, and the second portion of the plurality of capacitors is configured to cause current to flow from the outer edge of the second stackable circuit board towards the center of the second stackable circuit board.
40. The apparatus of claim 39, where the first stackable circuit board is electrically coupled to the second stackable circuit board by connectors disposed along the outer edges of the stackable circuit boards.
41. The apparatus of claim 32, where the plurality of stackable circuit boards each have a thickness of between 0.02 inches and 0.2 inches.

42. The apparatus of claim 25, where the plurality of capacitors each have length of between 2 mm and 4 mm, and a width of between 1 mm and 3 mm.
43. The apparatus of claim 25, where the plurality of capacitors comprises at least 100 capacitors.
44. A method of producing a compressed acoustic wave using an apparatus for generating therapeutic shock waves, the method comprising:
- applying voltage pulses to a plurality of electrodes in a chamber defined by a housing and filled with liquid such that portions of the liquid begin to vaporize and ionize to provide an inter-electrode conductive path;
 - applying voltage to a plurality of capacitors carried by the housing and in electrical communication with the plurality of electrodes to charge the plurality of capacitors.;
 - upon the plurality of capacitors reaching a threshold charge, discharging the plurality of capacitors to the electrodes to generate an inter-electrode arc along the established inter-electrode conductive path and thereby generate of at least one acoustic shock wave.
45. The method of claim 44, where the voltage pulses applied to the plurality of electrodes is between 500 V and 10,000 V.
46. The method of claim 44, where the voltage pulses applied to the plurality of capacitors is between 500 V and 10,000 V.

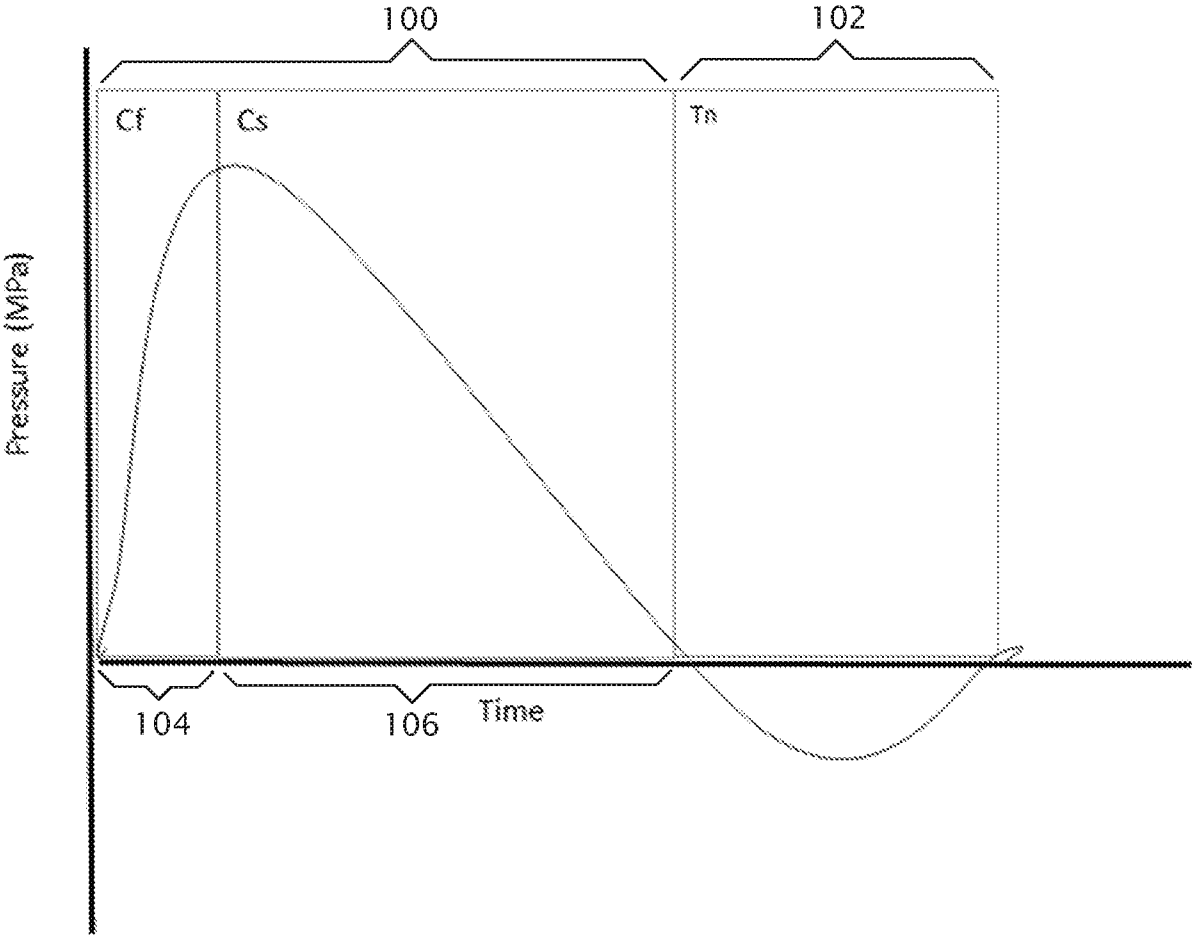


FIG. 1

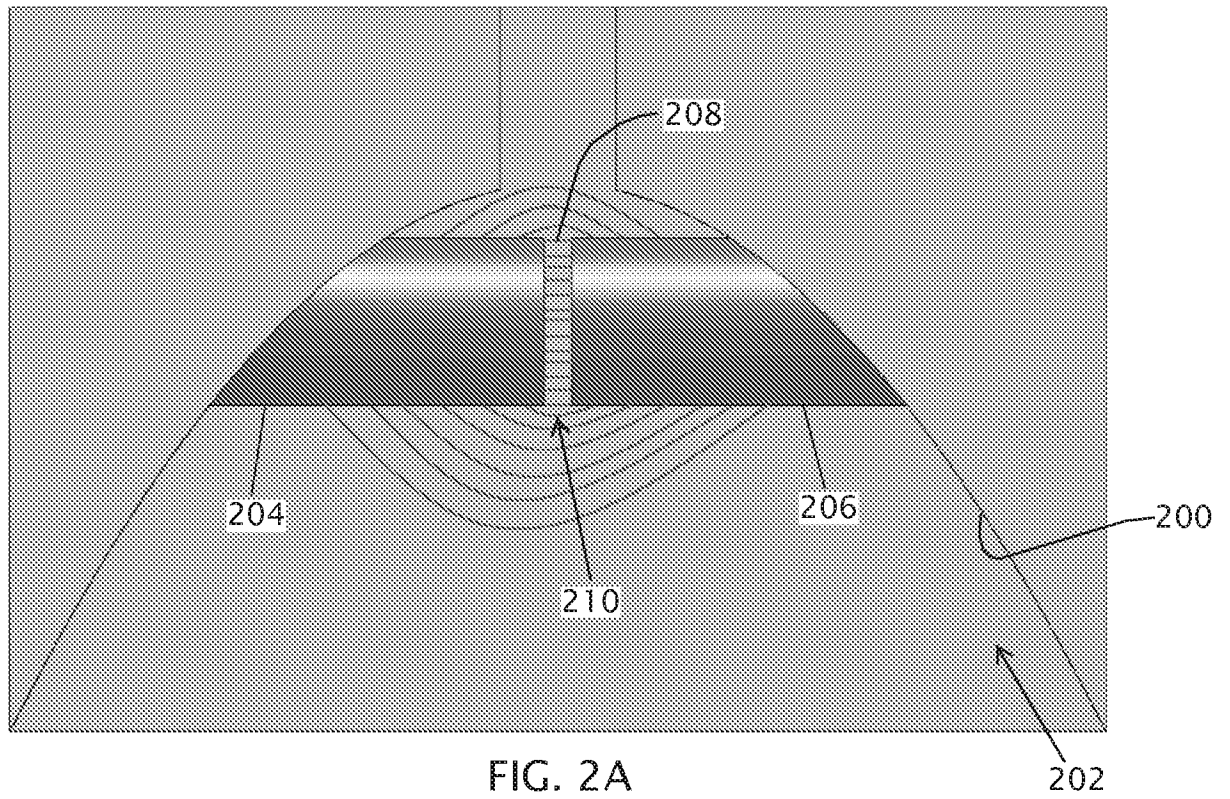


FIG. 2A

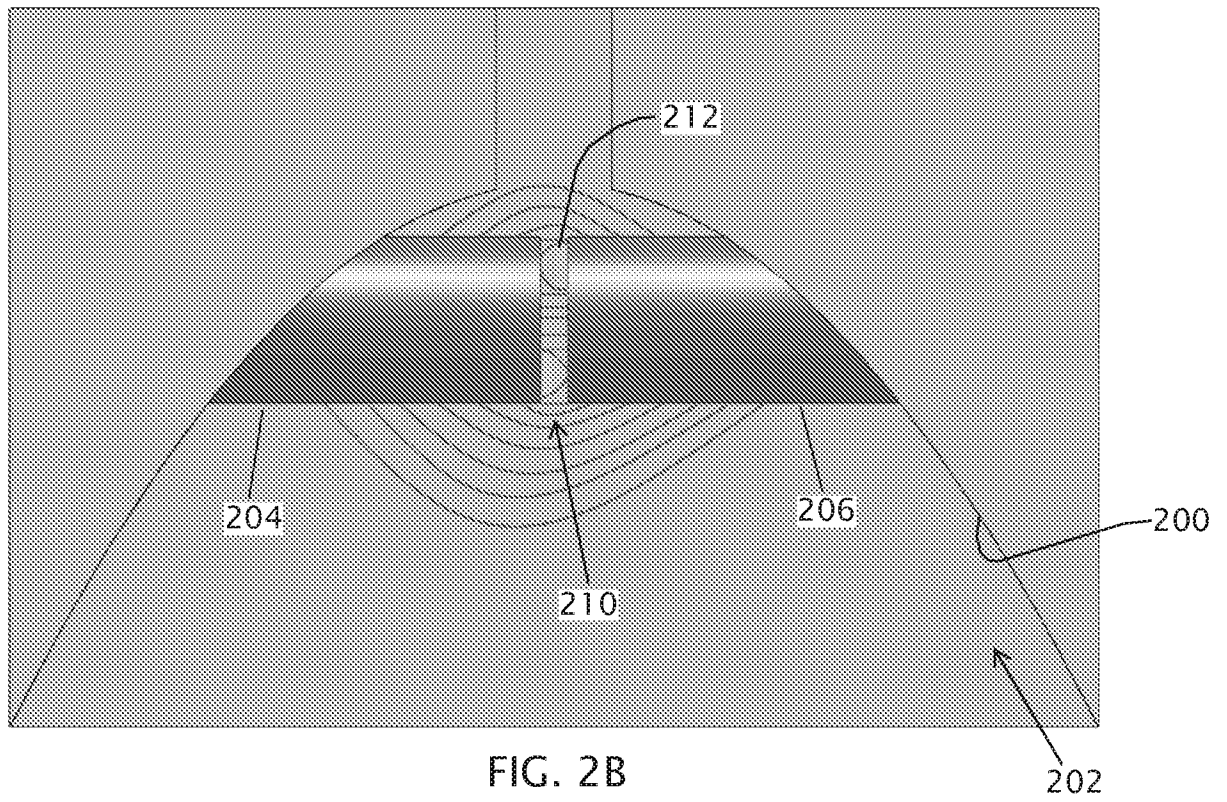
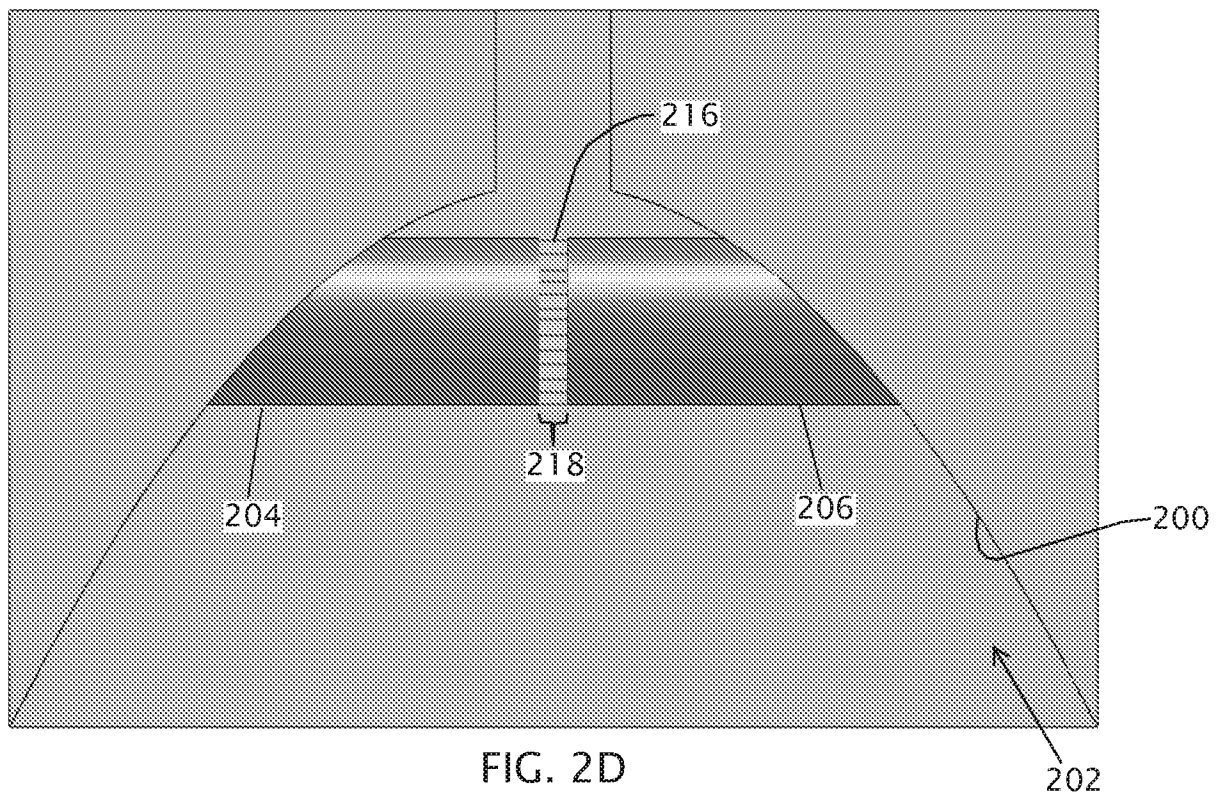
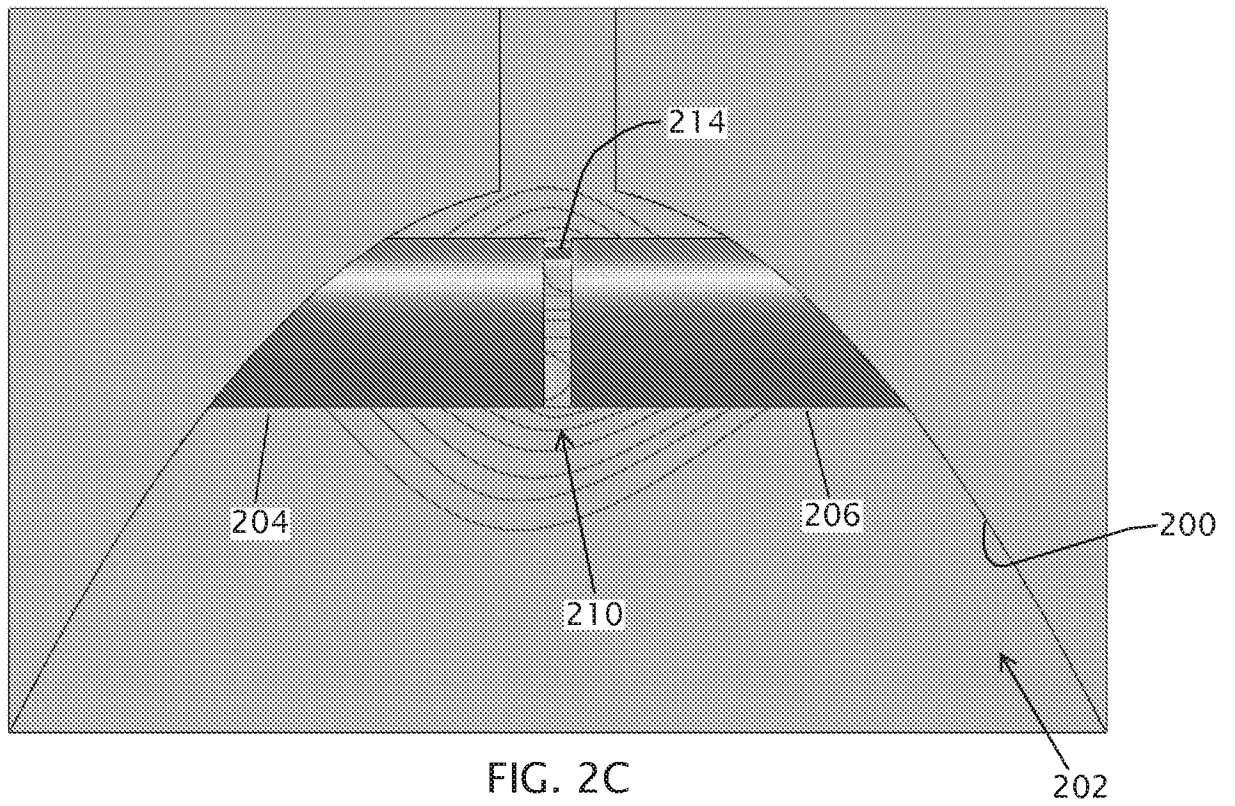


FIG. 2B



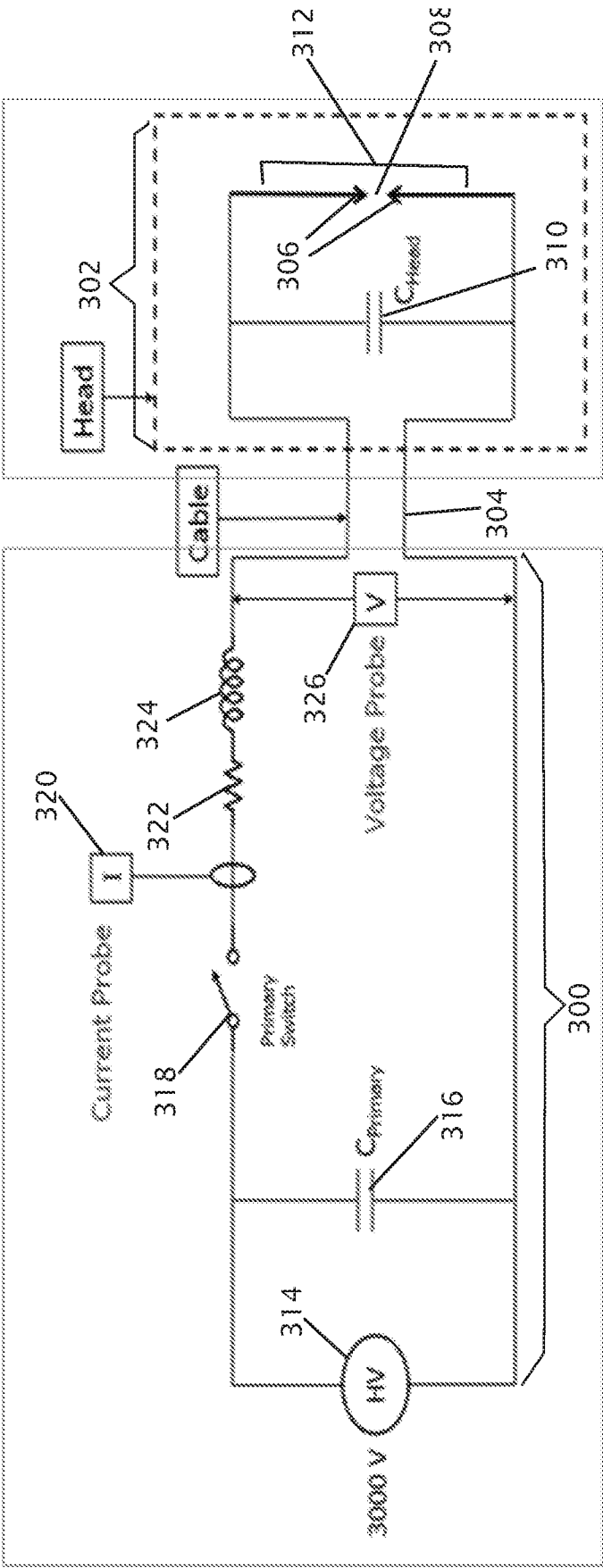


FIG. 3

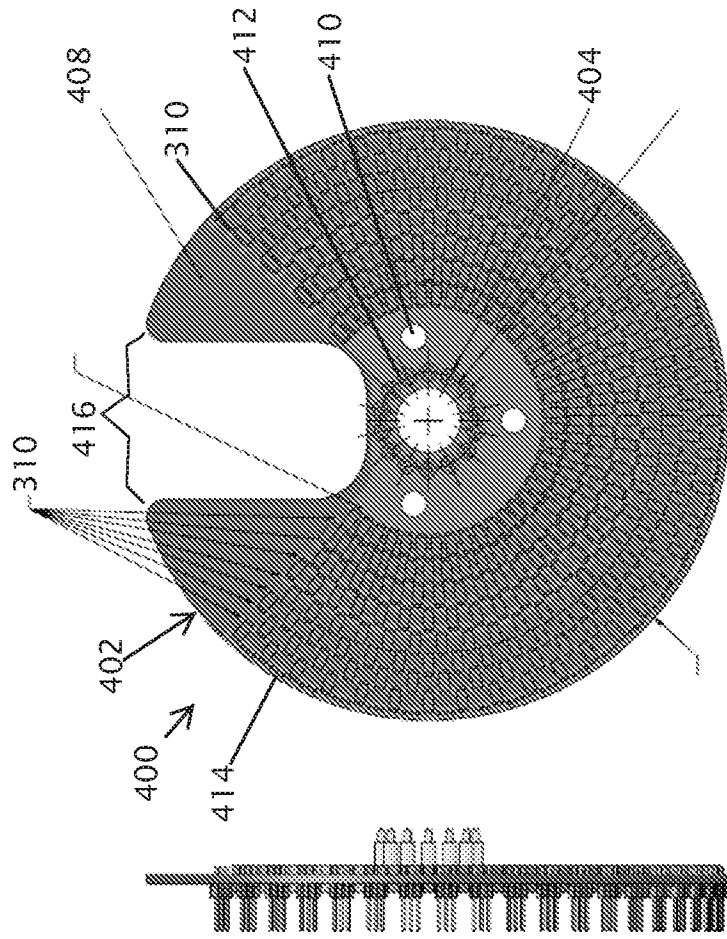


FIG. 4C

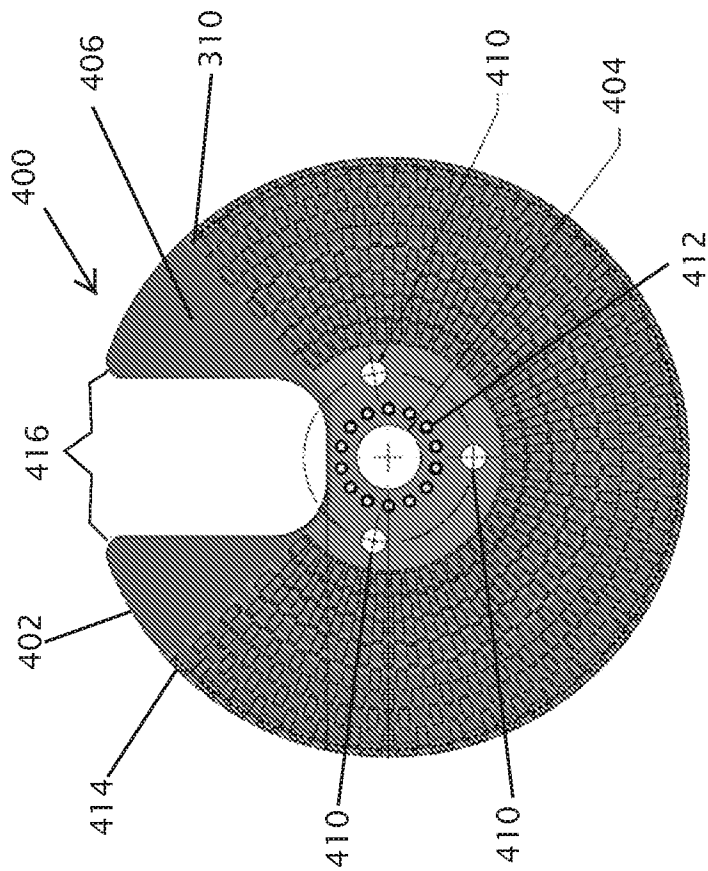


FIG. 4A

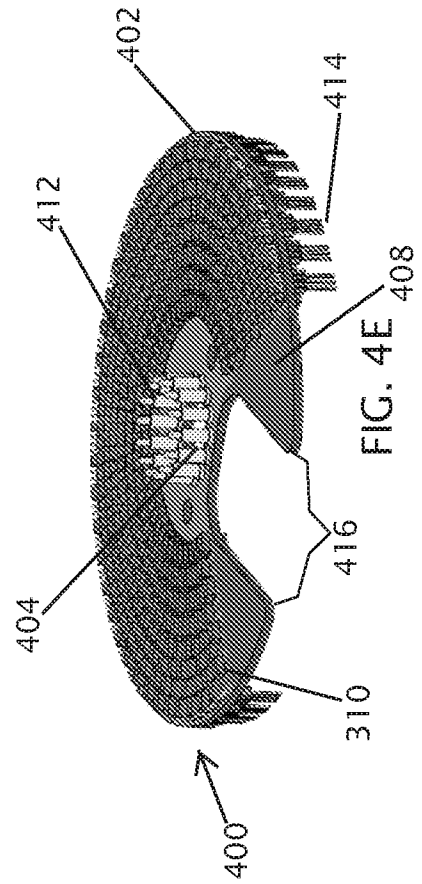


FIG. 4E

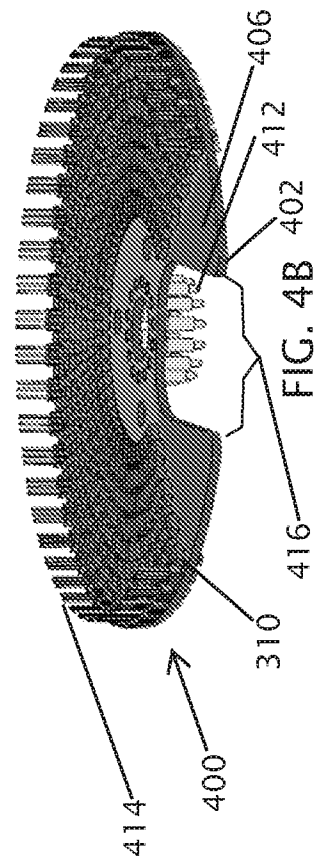


FIG. 4B

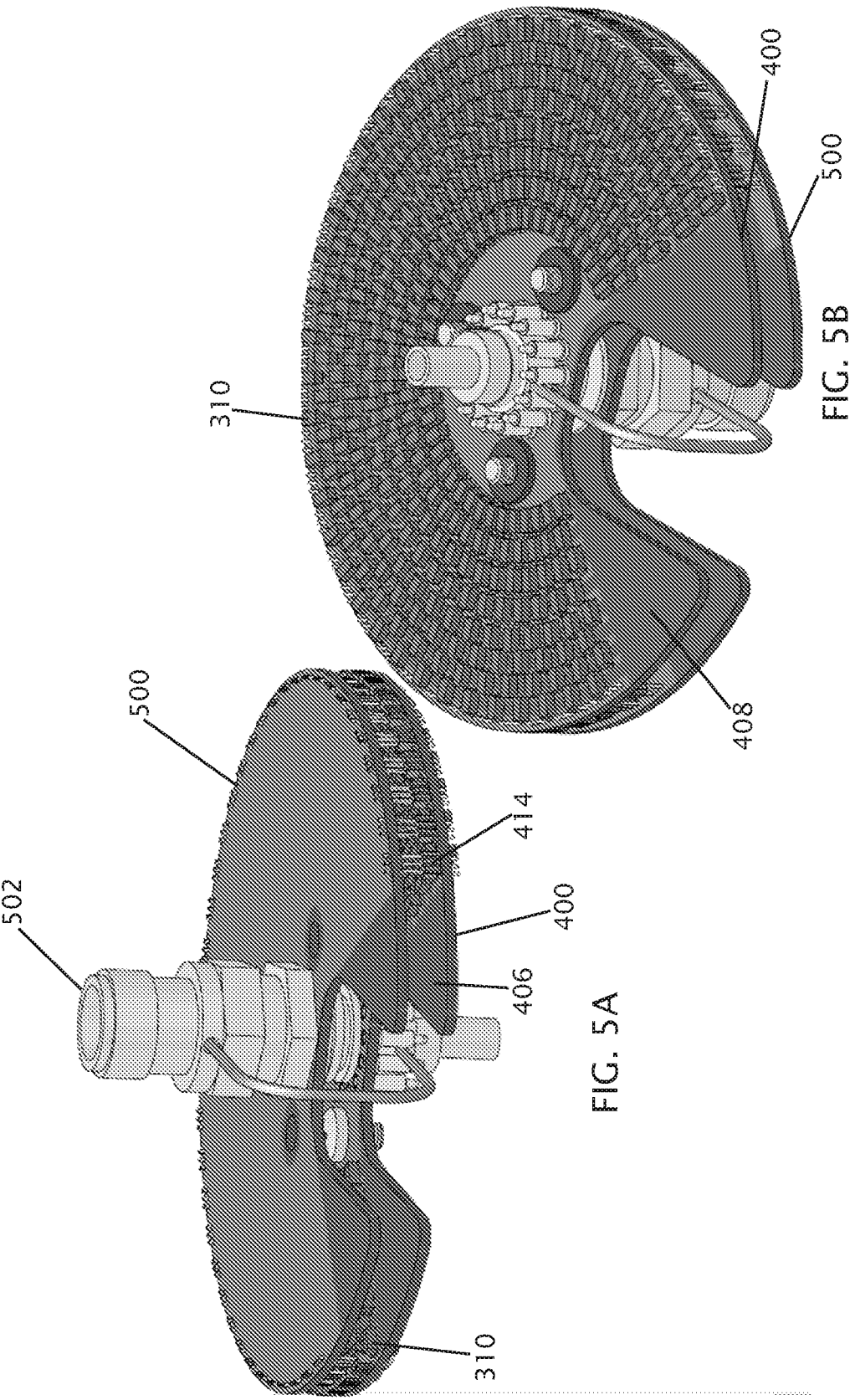


FIG. 5A

FIG. 5B

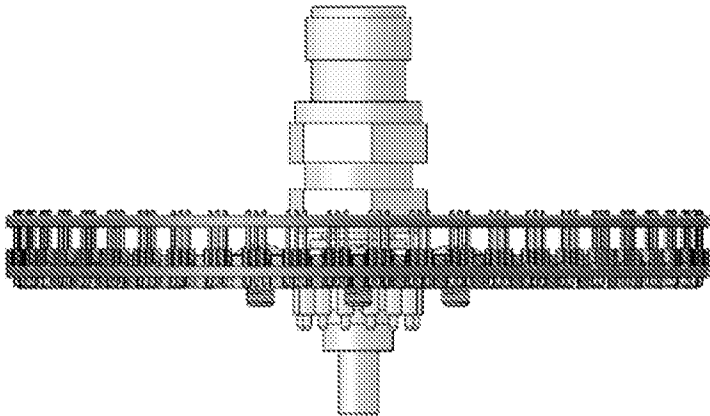


FIG. 5E

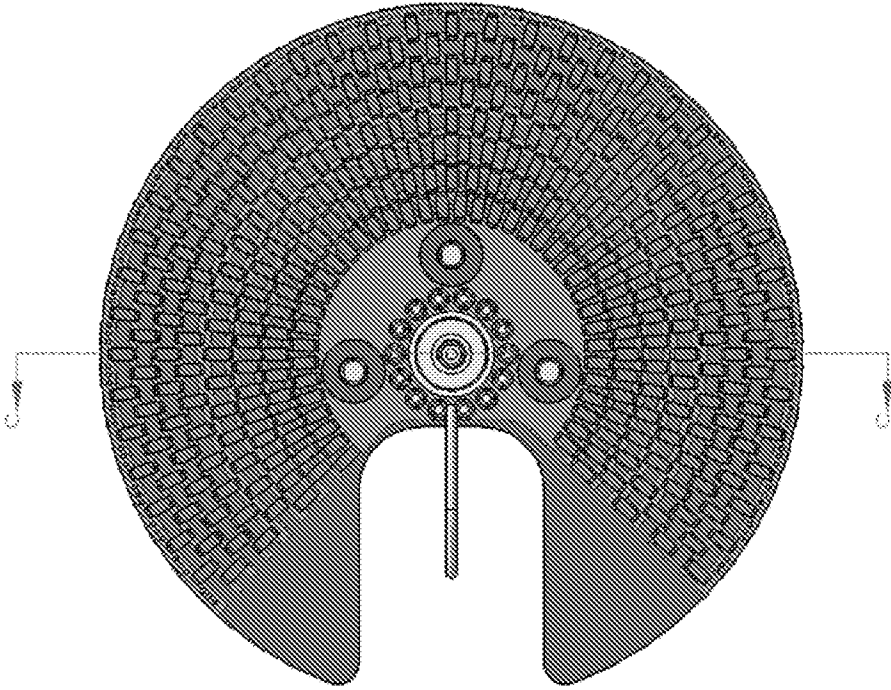


FIG. 5D

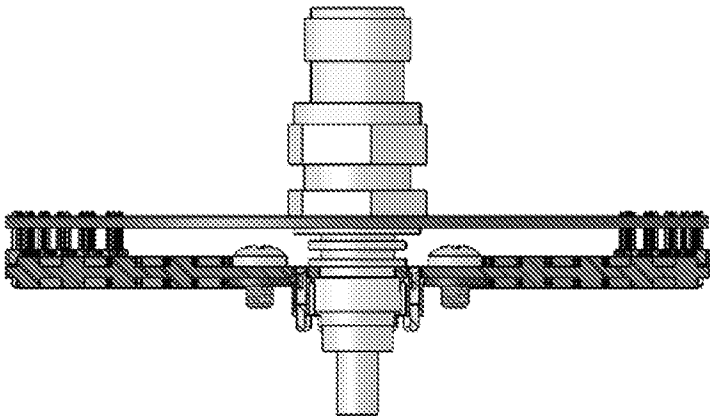
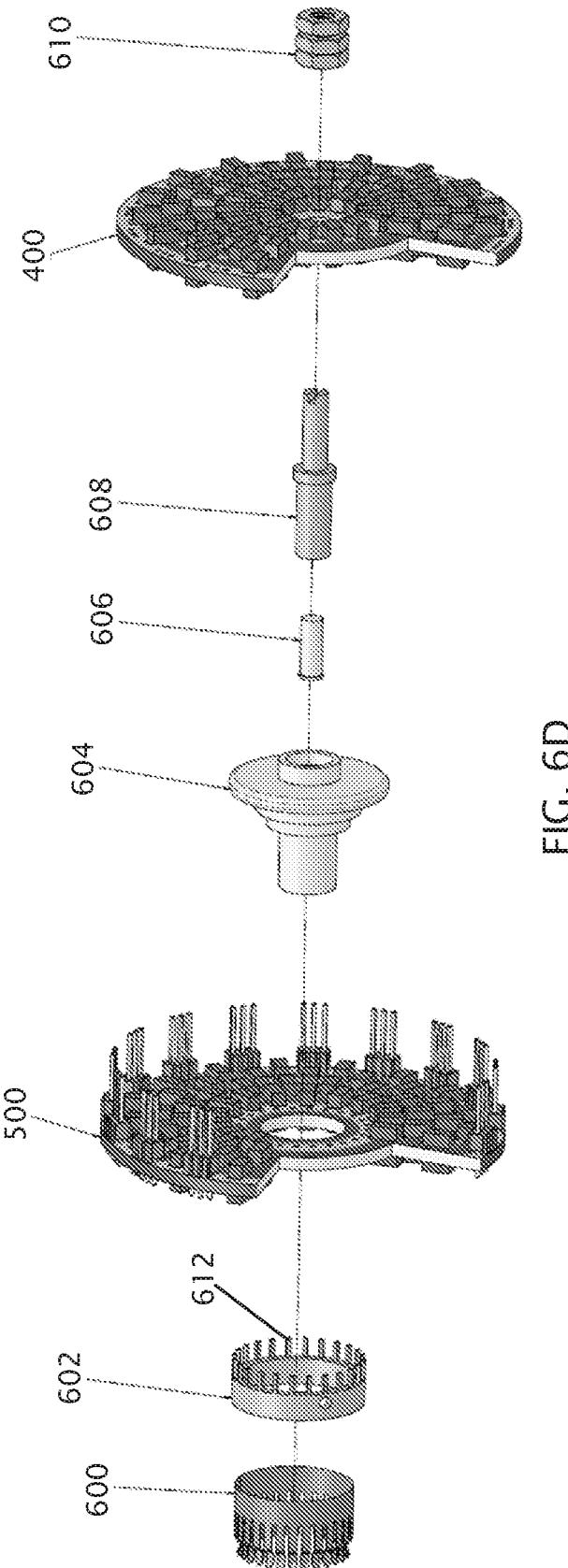
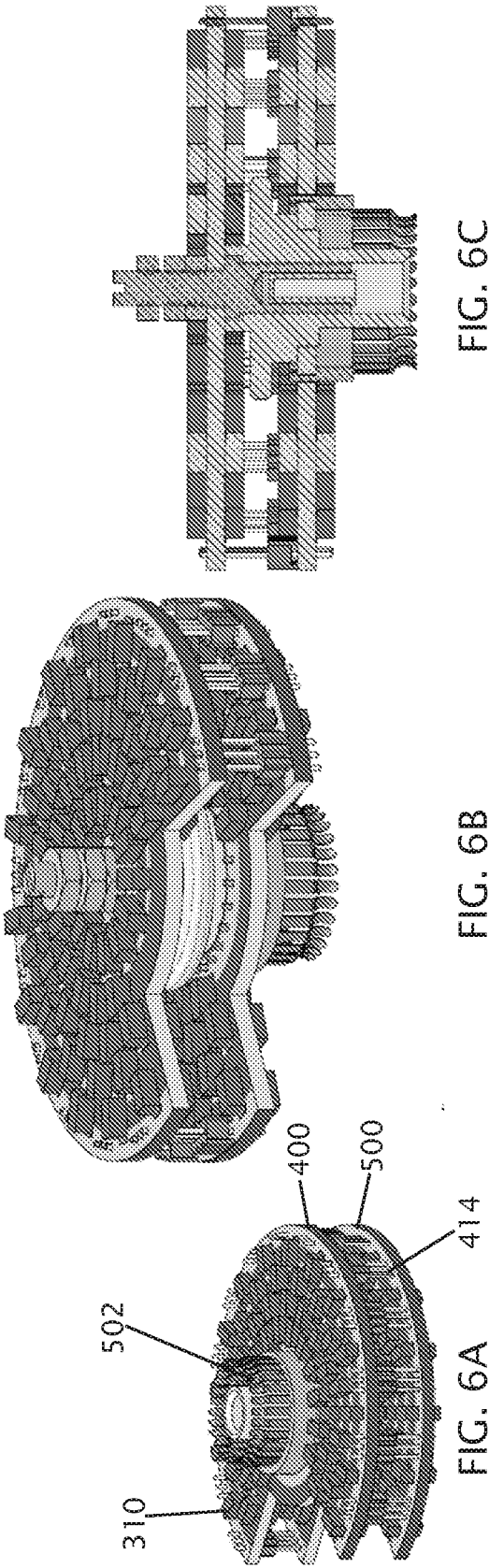
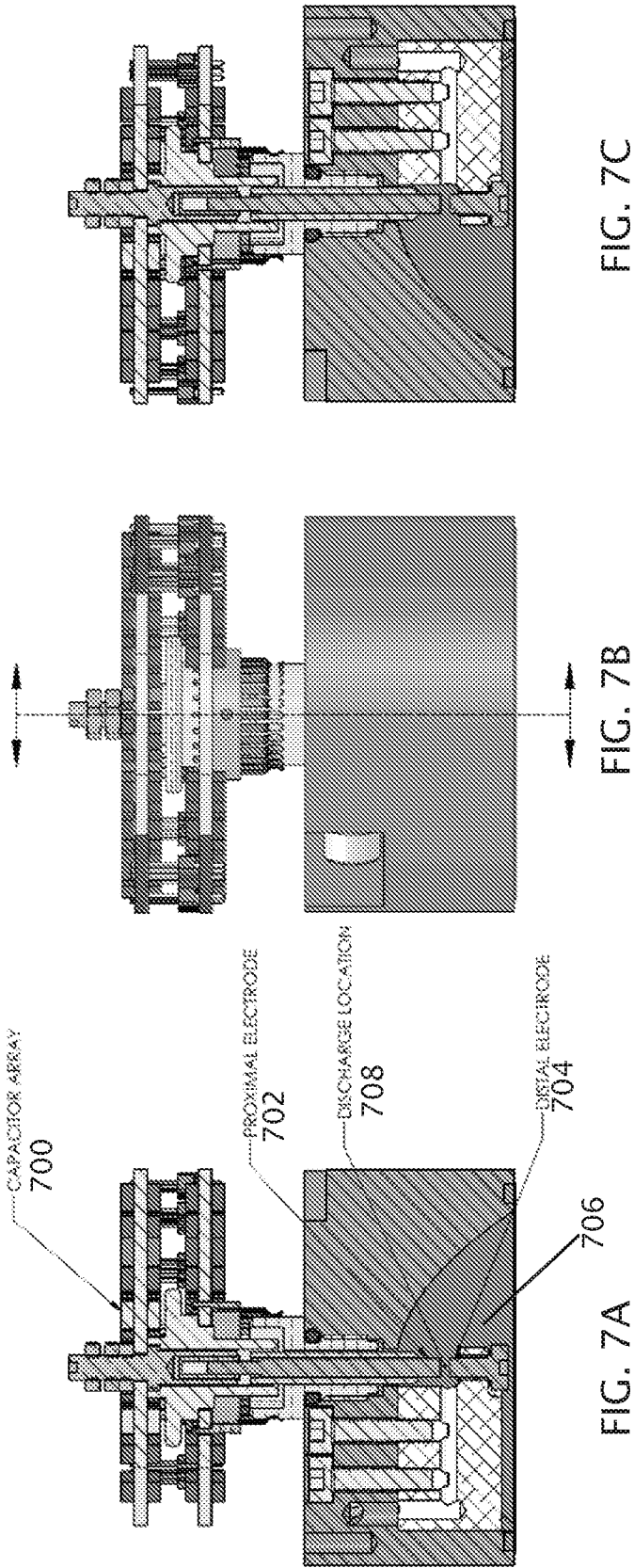


FIG. 5C





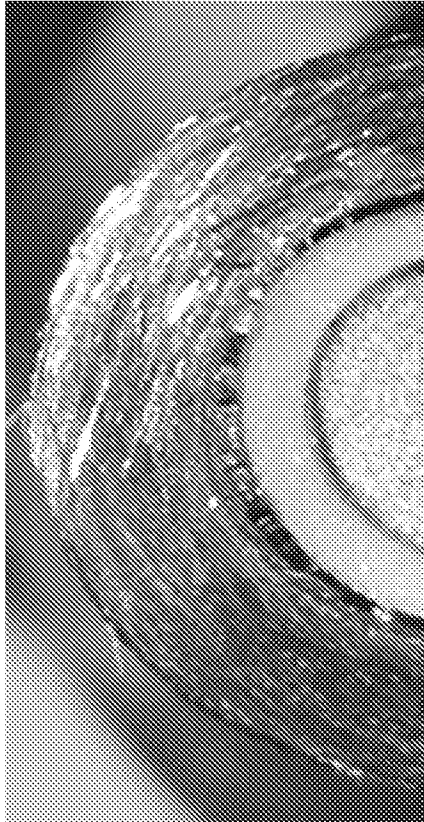


FIG. 8B

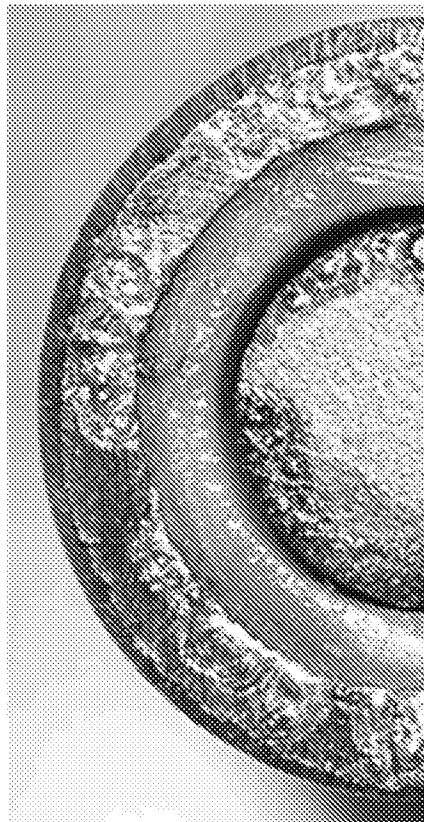


FIG. 8A

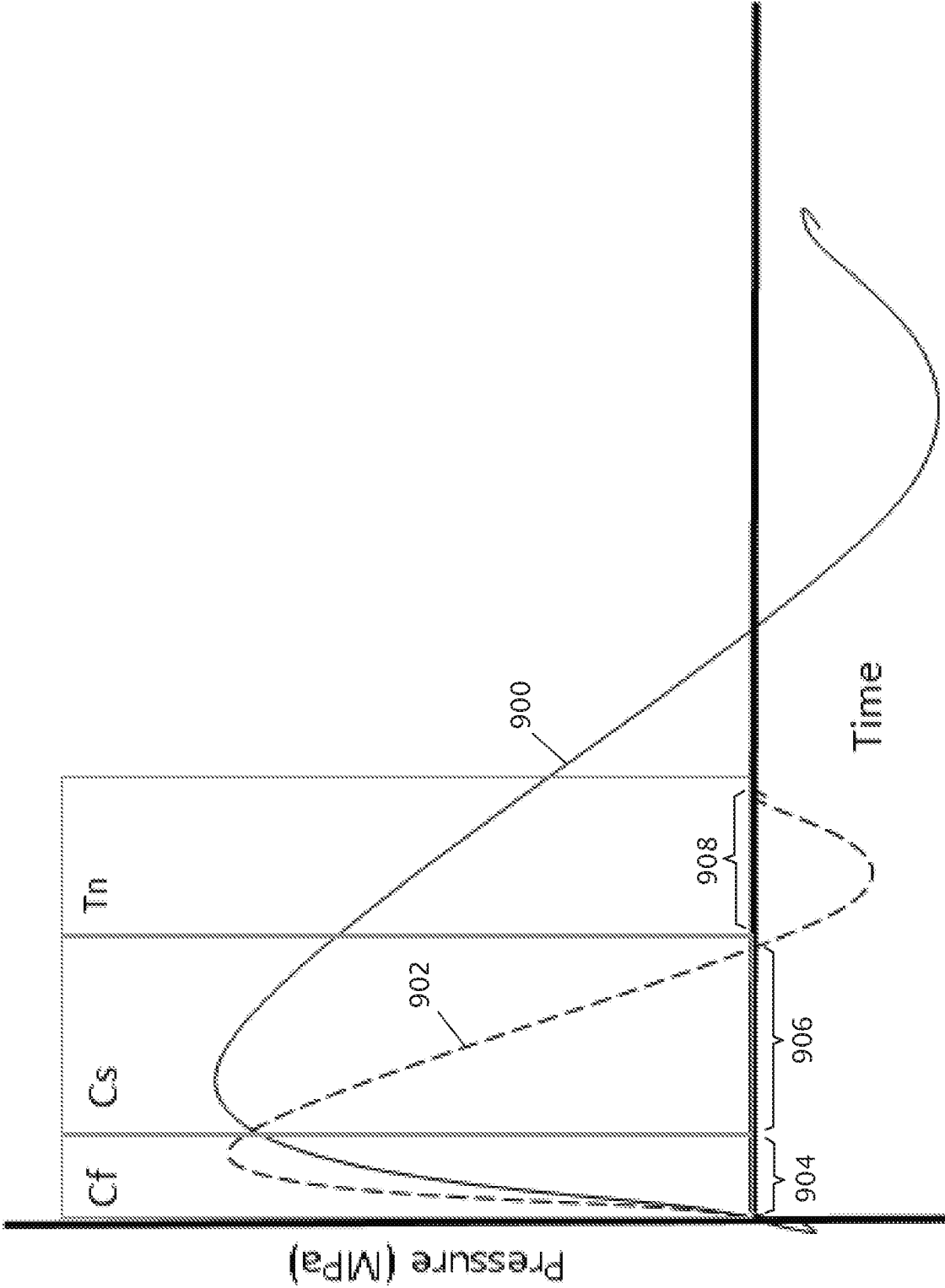


FIG. 9

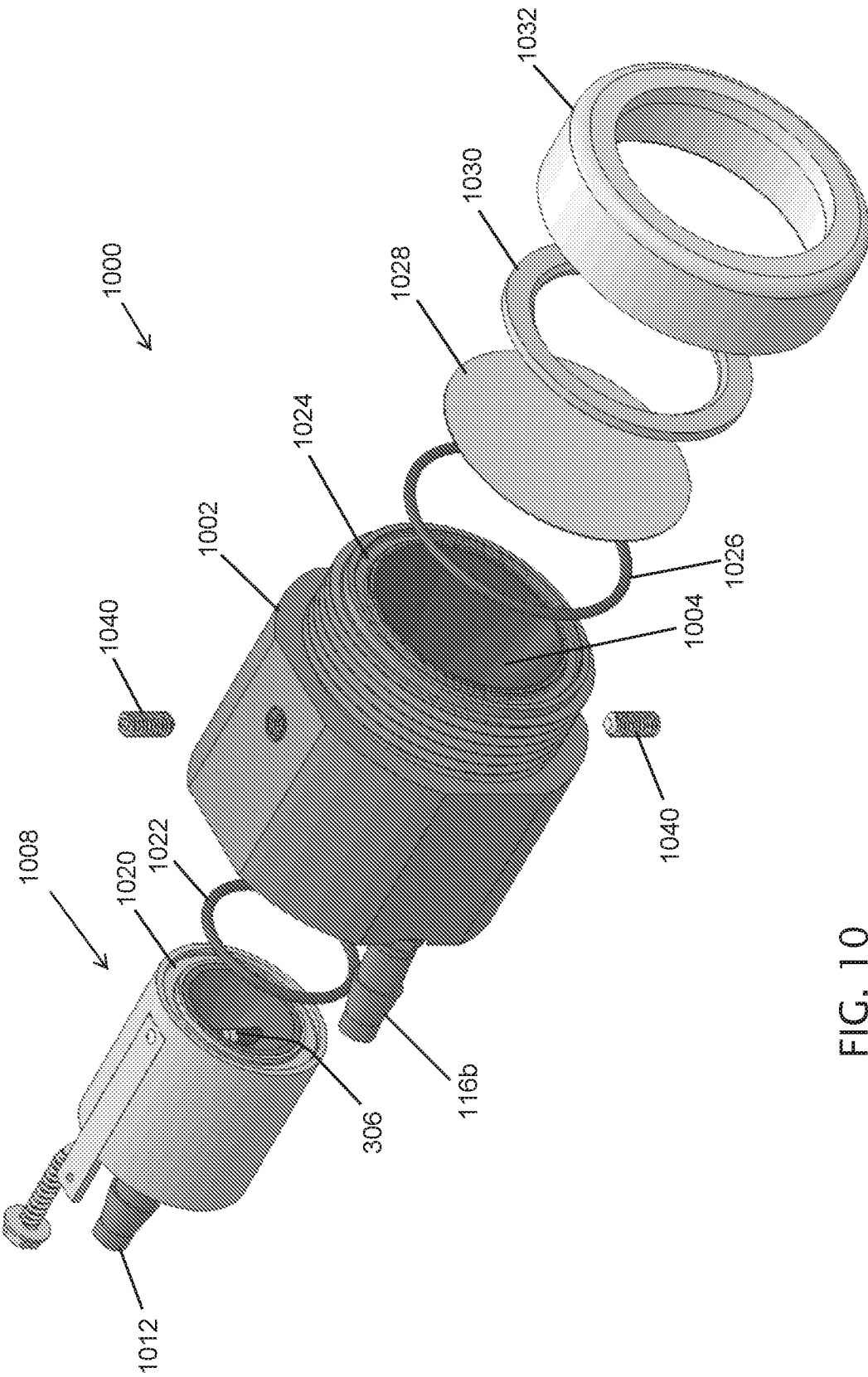


FIG. 10

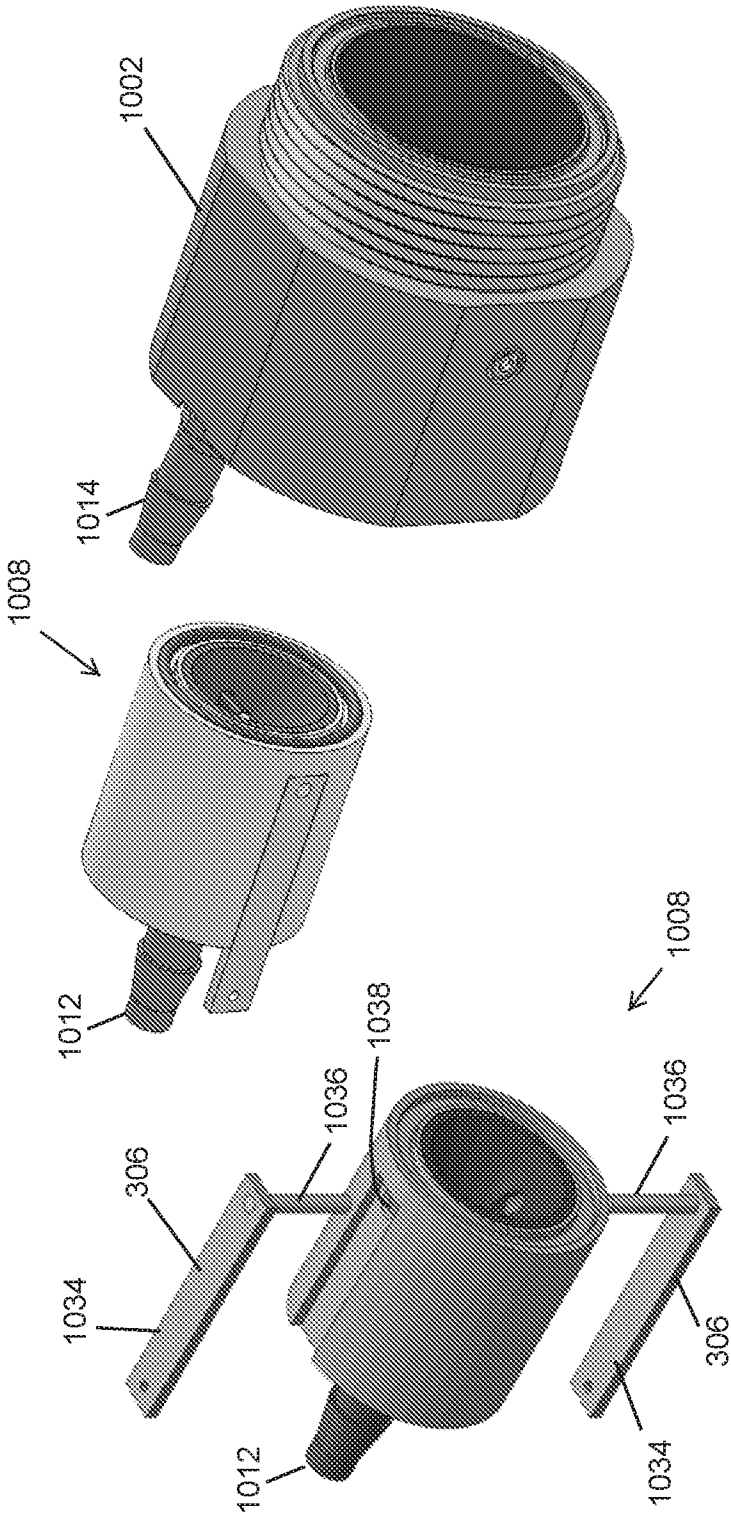


FIG. 11B

FIG. 11A

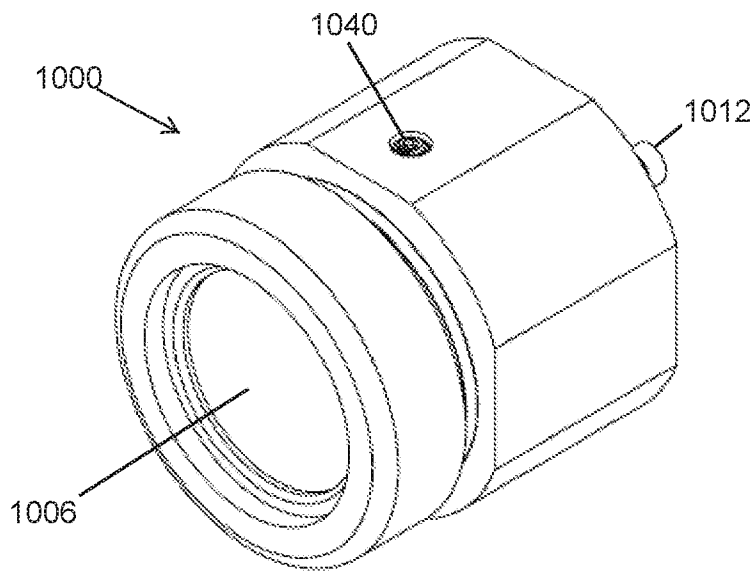


FIG. 12A

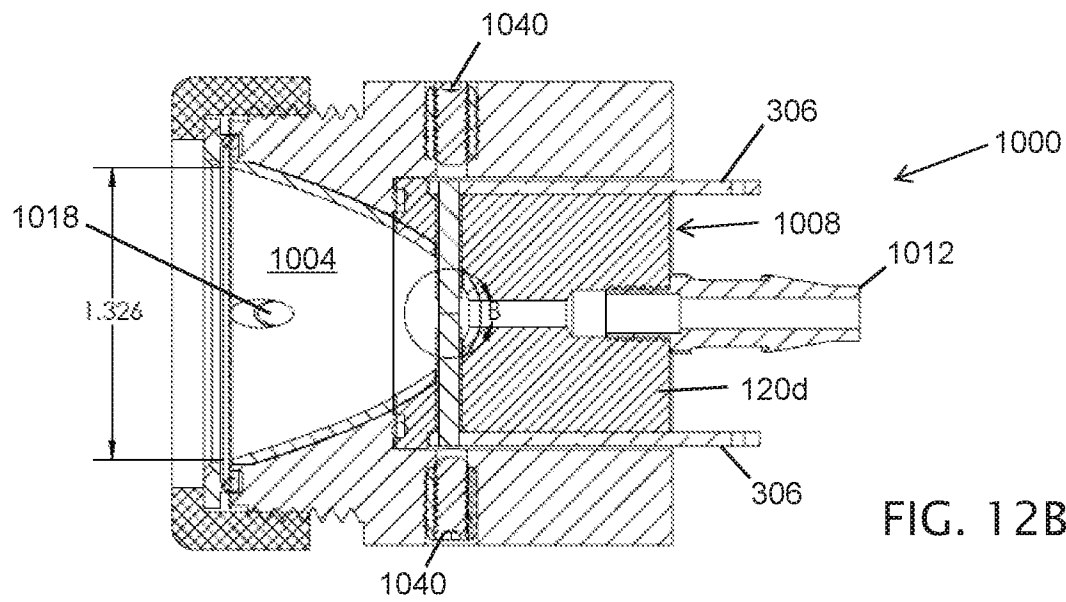


FIG. 12B

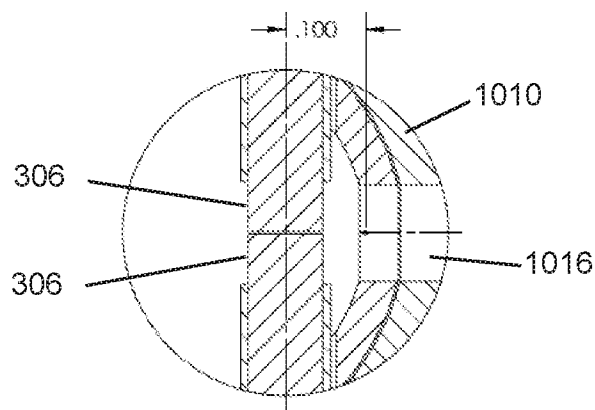


FIG. 12C