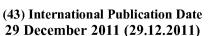
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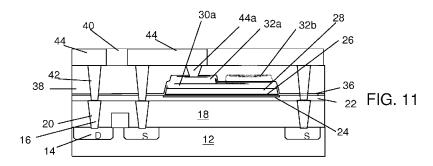
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(57) Abstract: Ferro-electric capacitor modules, methods of manufacture and design structures. The method of manufacturing the ferro-electric capacitor includes forming a barrier layer on an insulator (18) layer of a CMOS structure (10). The method further includes forming a top plate (32) and a bottom plate (28) over the barrier layer. The method further includes forming a ferro-electric material (30) between the top plate (32) and the bottom plate (28). The method further includes encapsulating the barrier layer, top plate (32), bottom plate (28) and ferro-electric material (30) with an encapsulating material (36). The method further includes forming contacts (20,44a) to the top plate (32) and bottom plate (28), through the encapsulating material (36). At least the contact (44a) to the top plate (32) and a contact (20) to a diffusion of the CMOS structure are in electrical connection through a common wire.





INTERNATIONAL SEARCH REPORT

International application No PCT/US2011/041546

A. CLASSIFICATION OF SUBJECT MATTER
INV. H01L21/02 H01L27/115 H01L21/8247 ADD. According to International Patent Classification (IPC) or to both national classification and IPC Minimum documentation searched (classification system followed by classification symbols) H01L Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal C. DOCUMENTS CONSIDERED TO BE RELEVANT Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. EP 1 887 624 A1 (FUJITSU LTD [JP] FUJITSU SEMICONDUCTOR LTD [JP]) Х 1,2, 8-10,13, 13 February 2008 (2008-02-13) paragraphs [0022] - [0047], [0106] -14,16-25 Α 3-7,11, 12,15 [0117]; figures 2,11 Χ US 2004/084701 A1 (KANAYA HIROYUKI [JP] ET 1-25 AL) 6 May 2004 (2004-05-06) paragraphs [0020], [0103] - [0112]; figures 1-5 paragraphs [0141] - [0143]; figure 20B paragraphs [0148] - [0149]; figures 23B,23C US 2009/020797 A1 (WANG WENSHENG [JP]) Χ 1-14. 22 January 2009 (2009-01-22) 16-25 paragraphs [0082] - [0144]; figures 1-5 15 Х Further documents are listed in the continuation of Box C. See patent family annex. Special categories of cited documents : "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the "A" document defining the general state of the art which is not considered to be of particular relevance invention earlier document but published on or after the international "X" document of particular relevance; the claimed invention filing date cannot be considered novel or cannot be considered to "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention citation or other special reason (as specified) cannot be considered to involve an inventive step when the document is combined with one or more other such docu-"O" document referring to an oral disclosure, use, exhibition or ments, such combination being obvious to a person skilled in the art. other means "P" document published prior to the international filing date but later than the priority date claimed "&" document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 1 December 2011 23/12/2011 Authorized officer Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016 Neumann, Andreas

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