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(54) Title: SYSTEM AND METHOD FOR CONTROLLING THE OPERATION PARAMETERS OF A DISPLAY IN RESPONSE TO CURRENT DRAW

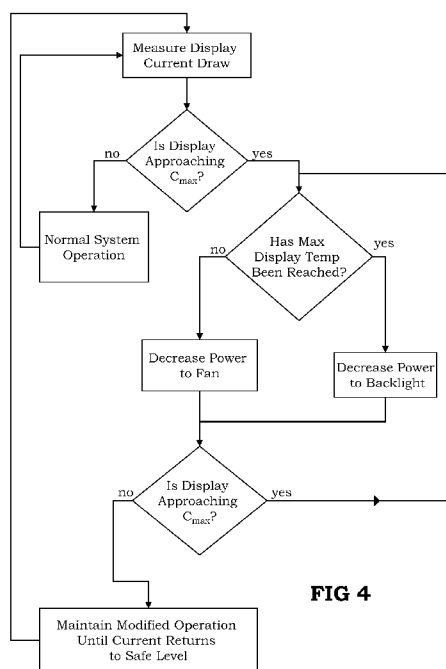


FIG 4

(57) Abstract: A system and method for controlling the power consumption of an electronic display. A maximum current value may be selected where above this value the risk to damage to the display or local circuitry may be jeopardized. Ramp-wise and/or gradual controls of the display parameters such as fan speed and backlight levels may reduce the current draw during extreme situations and line voltage fluctuations. Embodiments allow the display to continue operation without risking an overload of the local circuit or damage to the display. Further embodiments may be used to limit the power consumption of a display in order to minimize energy usage. Several parameters can be measured and controlled simultaneously to provide a minimal amount of energy usage while minimizing any noticeable difference in images.

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A. CLASSIFICATION OF SUBJECT MATTER**G09G 3/36(2006.01)i, G09G 3/32(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G09G 3/36; G03B 21/16; H01J 7/24; G03B 21/26; G03B 21/18

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: backlight, lamp, power, current, sensor, fan, temperature and similar terms

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y A	US 2006-0125773 A1 (HIROAKI ICHIKAWA et al.) 15 June 2006 See paragraphs [0034]-[0069] and figures 4, 5, 7.	1-2 3-17
Y A	JP 2009-031622 A (PANASONIC CORP.) 12 February 2009 See paragraphs [0002]-[0018] and figure 1.	1-2 3-17
A	US 2003-0020884 A1 (TAKAYUKI OKADA et al.) 30 January 2003 See paragraphs [0026]-[0049] and figures 1, 3.	1-17
A	US 2005-0242741 A1 (TETSURO SHIOTA et al.) 03 November 2005 See paragraphs [0074]-[0125] and figures 3-7.	1-17

 Further documents are listed in the continuation of Box C. See patent family annex.

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"&" document member of the same patent family

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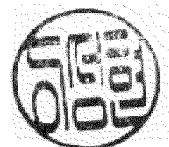
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