



- (51) International Patent Classification:  
*H01L 21/66* (2006.01) *H01L 21/67* (2006.01)  
*H01L 21/02* (2006.01)
- (21) International Application Number:  
PCT/US2016/036751
- (22) International Filing Date:  
9 June 2016 (09.06.2016)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:  
62/180,060 16 June 2015 (16.06.2015) US  
15/048,676 19 February 2016 (19.02.2016) US
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- (81) Designated States (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
- (84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).
- Published:  
— with international search report (Art. 21(3))

(54) Title: SYSTEM AND METHOD FOR MONITORING PARAMETERS OF A SEMICONDUCTOR FACTORY AUTOMATION SYSTEM

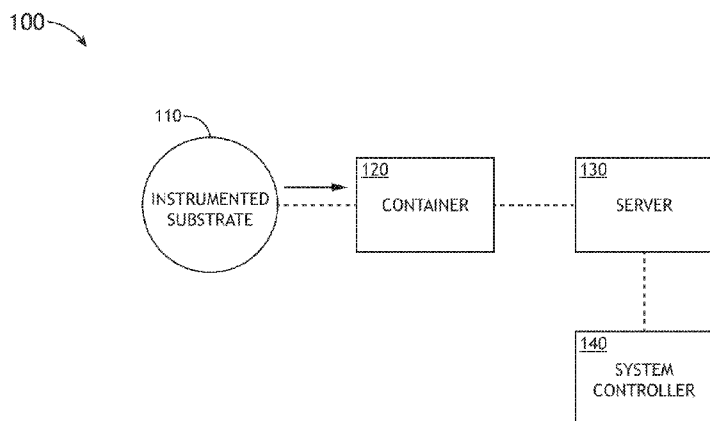


FIG.1A

(57) Abstract: A system for monitoring one or more conditions of an automation system of a semiconductor factory includes one or more instrumented substrates, one or more sealable containers and one or more system servers. The one or more instrumented substrates include one or more sensors. The one or more sensors measure one or more conditions of the one or more instrumented substrates as the one or more sealable containers transport the one or more instrumented substrates through the semiconductor factory. The one or more sealable containers also receive sensor data from the one or more sensors included on the one or more instrumented substrates. The one or more system servers are configured to receive the sensor data from the one or more sealable containers. The one or more servers are configured to identify one or more deviations in the measured one or more conditions.

**SYSTEM AND METHOD FOR MONITORING PARAMETERS OF A  
SEMICONDUCTOR FACTORY AUTOMATION SYSTEM**

**CROSS-REFERENCE TO RELATED APPLICATION**

**[0001]** The present application claims the benefit under 35 U.S.C. § 119(e) of U.S. Provisional Application Serial Number 62/180,060, filed June 16, 2015, entitled METHOD TO AUTOMATICALLY MEASURE AND MONITOR PARAMETERS SUCH AS THE LEVELING, TEACHING, ALIGNMENT, CLEANLINESS, AND HEALTH OF SEMICONDUCTOR FACTORY AUTOMATION SYSTEMS AND WAFER CHARGER, naming Mor Azarya, Michael D. Brain, Ami Appelbaum, Shai Mark and Arie Hoffman as inventors, which is incorporated herein by reference in the entirety.

**TECHNICAL FIELD**

**[0002]** The present disclosure relates generally to monitoring conditions of a semiconductor factory automation system, and, more particularly, to an instrumented substrate for monitoring parameters of a semiconductor factory automation system.

**BACKGROUND**

**[0003]** Process flows in modern semiconductor device fabrication lines utilize a multitude of fabrication steps performed by a series of semiconductor fabrication tools. Process flows may contain more than 800 process steps in a re-entrant flow among a set of semiconductor device fabrication line tools including over one thousand equipment units of over one hundred different types. Wafers in a process flow are typically moved through process steps in sealable containers, or front opening unified pods (FOUPs), by automation systems. Automation systems may include FOUP handling systems, such as automated material handling systems (e.g. overhead

transport systems, stockers and the like), and FOUN/wafer handling systems of process and metrology equipment.

**[0004]** Accelerometers are used to measure vibration and manually interpret the data to determine automation system health. Accelerometers are typically used as part of a troubleshooting exercise or an acceptance test for newly installed equipment. Some accelerometer systems are mounted to wafers or other substrates of similar dimensions. Manual identification of errors and deviations in a semiconductor fabrication line automation system is cumbersome and slow, making the identification of the root causes of the errors and deviations in the automation system difficult to identify. Therefore, it would be desirable to provide a system and method for curing defects such as those identified above.

## SUMMARY

**[0005]** A system for monitoring one or more conditions of an automation system of a semiconductor factory is disclosed, in accordance with one or more illustrative embodiments of the present disclosure. In one embodiment, the system includes one or more instrumented substrates. In another embodiment, the one or more instrumented substrates include one or more sensors. In another embodiment, the one or more sensors are configured to measure one or more conditions of the one or more instrumented substrates as the one or more instrumented substrates traverses one or more portions of the semiconductor factory. In another embodiment, the one or more instrumented substrates include substrate communication circuitry and one or more processors communicatively coupled to the one or more sensors and the substrate communication circuitry.

**[0006]** In another embodiment, the system includes one or more sealable containers configured to transport the one or more instrumented substrates through the semiconductor factory. In another embodiment, the one or more sealable containers include container communication circuitry. In another embodiment, the container communication circuitry is communicatively couplable to the substrate communication circuitry. In another embodiment, the container communication circuitry is configured to

receive sensor data from the one or more sensors of the one or more instrumented substrates via the substrate communication circuitry.

**[0007]** In another embodiment, the system includes one or more system servers. In another embodiment, the one or more system servers include server communication circuitry. In another embodiment, the server communication circuitry is communicatively couplable to the one or more sealable containers. In another embodiment, the server communication circuitry is configured to receive the sensor data from the container communication circuitry. In another embodiment, the one or more system servers are configured to identify one or more deviations in the measured one or more conditions of the one or more instrumented substrates.

**[0008]** A sealable container for transporting one or more substrates through a semiconductor factory is disclosed, in accordance with one or more illustrative embodiments of the present disclosure. In one embodiment, the sealable container includes a housing configured to contain an instrumented substrate. In another embodiment, the instrumented substrate includes one or more sensors. In another embodiment, the one or more sensors are configured to measure one or more conditions of the instrumented substrate as the sealable container transports the instrumented substrate through one or more portions of the semiconductor factory. In another embodiment, the instrumented substrate includes substrate communication circuitry and one or more processors communicatively coupled to the one or more sensors and the substrate communication circuitry. In another embodiment, the sealable container includes container communication circuitry. In another embodiment, the substrate communication circuitry is configured to transmit sensor data from the one or more sensors to the container communication circuitry. In another embodiment, the container communication circuitry is configured to transmit the sensor data to server communication circuitry of a server.

**[0009]** A method for monitoring one or more conditions of an automation system of a semiconductor factory is disclosed, in accordance with one or more illustrative embodiments of the present disclosure. In one embodiment, the method includes transporting an instrumented substrate through one or more portions of a

semiconductor fabrication facility in a sealable container. In another embodiment, the method includes measuring one or more conditions of the instrumented substrate at one or more locations within the semiconductor fabrication facility. In another embodiment, the method includes transmitting the measured one or more conditions from the instrumented substrate to the sealable container. In another embodiment, the method includes transmitting the measured one or more conditions from the sealable container to a system server. In another embodiment, the method includes identifying one or more deviations in the measured one or more conditions with the system server.

**[0010]** It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory only and are not necessarily restrictive of the invention as claimed. The accompanying drawings, which are incorporated in and constitute a part of the specification, illustrate embodiments of the invention and together with the general description, serve to explain the principles of the invention.

### **BRIEF DESCRIPTION OF DRAWINGS**

**[0011]** The numerous advantages of the disclosure may be better understood by those skilled in the art by reference to the accompanying figures in which:

FIG. 1A is a conceptual view of the system for measuring one or more conditions of a semiconductor automation system, in accordance with one or more embodiments of the present disclosure.

FIG. 1B is a block diagram view of the system for measuring one or more conditions of a semiconductor automation system, in accordance with one or more embodiments of the present disclosure.

FIG. 1C is a schematic view of an instrumented substrate, in accordance with one or more embodiments of the present disclosure.

FIG. 1D is a schematic view of an instrumented substrate inserted into a sealable container, in accordance with one or more embodiments of the present disclosure.

- FIG. 1E is a schematic view of a sealable container charged by a charging station, in accordance with one or more embodiments of the present disclosure.
- FIG. 1F is a schematic view of an instrumented substrate equipped with a radio frequency receiving coil for charging a rechargeable battery of the instrumented substrate, in accordance with one or more embodiments of the present disclosure.
- FIG. 1G is a schematic view of an external power transfer device for remotely transferring power or data to the instrumented substrate, in accordance with one or more embodiments of the present disclosure.
- FIG. 1H is a schematic view of the external power transfer device and the instrumented substrate contained within the sealable container, in accordance with one or more embodiments of the present disclosure.
- FIG. 1I is a block diagram view of an external power transfer device inductively transferring power to the instrumented substrate, in accordance with one or more embodiments of the present disclosure.
- FIG. 2 is data graph depicting acceleration data measured by one or more sensors of the instrumented substrate as the instrumented substrate traversed one or more portions of a semiconductor fabrication facility, in accordance with one or more embodiments of the present disclosure.
- FIG. 3 is a block diagram view of the implementation of the system for measuring one or more conditions of the instrumented substrate within a semiconductor fabrication facility, in accordance with one or more embodiments of the present disclosure.
- FIG. 4A is process flow diagram depicting a method of measuring one or more conditions of an instrumented substrate as the instrumented substrate traverses one or more portions of a semiconductor fabrication facility, in accordance with one or more embodiments of the present disclosure.
- FIG. 4B is a graphical view of an example report transmitted to a user indicative of the various steps and targeted tools of a measurement mission, in accordance with one or more embodiments of the present disclosure.

## DETAILED DESCRIPTION

**[0012]** Reference will now be made in detail to the subject matter disclosed, which is illustrated in the accompanying drawings.

**[0013]** Referring generally to FIGS. 1A through 4B, a system and method for monitoring one or more conditions of a semiconductor automation system is described, in accordance with one or more embodiments of the present disclosure. Embodiments of the present disclosure are directed to the automatic measurement and/or monitoring of one or more parameters and/or conditions of automation systems of a semiconductor factory, or fabrication line. For instance, parameters and/or conditions measured and/or monitored by the embodiments of the present disclosure may include, but are not limited to, leveling, teaching, alignment, cleanliness, pressure and/or the health of the automation system(s). Such automation systems may include, but are not limited to, automated material handling systems (e.g., overhead transport system, stocker and the like) and FOUP/wafer handling systems of process and metrology equipment. The automated measurement and/or monitoring approach provided by the various embodiments of the present disclosure provide actionable results and trends that aid in identifying the root cause(s) of an error or deviation in the operation of one or more portions of a semiconductor factory automation system.

**[0014]** A device for measuring process conditions in a semiconductor manufacturing facility is discussed in U.S. Patent No. 8,104,342, issued on January 31, 2012, which is incorporated herein by reference in the entirety. An interface system and a sensor wafer are discussed in U.S. Patent Pub. No. 2011/0074341, published on March 31, 2011, which is incorporated herein by reference in the entirety. An interface system for use in a semiconductor handling system is discussed in U.S. Patent Pub. No. 2014/0122654, published on May 1, 2015, which is incorporated herein by reference in the entirety.

**[0015]** FIG. 1A illustrates a conceptual block diagram view of system 100 for measuring one or more conditions of a semiconductor automation system, in accordance with one or more embodiments of the present disclosure.

**[0016]** In one embodiment, the system 100 includes one or more instrumented substrates 110. For example, the one or more instrumented substrates 110 may include, but are not limited to, one or more instrumented semiconductor wafers 110. In another embodiment, the system 100 includes one or more sealable containers 120. In one embodiment, the one or more sealable containers 120 are configured to receive and secure the one or more instrumented substrates 110. The instrumented substrate 110 is configured to measure one or more conditions of the instrumented substrate 110 as the sealable container 120 containing the instrumented substrate 110 travels through the semiconductor automation system. The one or more instrumented wafers 110 may be communicatively coupled (e.g., wireless or wireline coupling) with the one or more sealable containers 120. In one embodiment, the conditions measured by the one or more instrumented wafers 110 may be stored on the one or more instrumented substrates 110 and then transferred to the one or more sealable containers 120. For example, the instrumented wafer 110 may be placed into a sealable container 120 to observe the vibrations and/or other conditions that are experienced by a product wafer as it moves through the sealable container transport and storage system, and then as it moves through the wafer handling systems associated with operations such as, but not limited to, process, metrology, sorting, transportation, storage, and shipping.

**[0017]** In another embodiment, the system 100 includes a server 130. In one embodiment, the server 130 is communicatively coupled (e.g., wireless or wireline coupling) to the one or more sealable containers 120. In this regard, the conditions measured by the one or more instrumented wafers 110 may be stored on the one or more instrumented substrates 110 and then transferred to the one or more sealable containers 120. In turn, the one or more sealable containers 120 may transfer the measured conditions to server 130. In this sense, the one or more sealable containers 120 may act to extend the capabilities of the one or more instrumented wafers 110. For instance, the one or more sealable containers 120 may include memory to buffer sensor data from the instrumented substrate 110 and a power source to recharge the instrumented substrate 110. In addition, the one or more sealable containers 120 may include additional sensors for providing additional container sensor data concerning location, activity and status. The one or more sealable containers 120 may also include

one or more processors that may provide additional processing power to analyze the acquired sensor data. For example, the one or more sealable containers 120 may manage data acquisition or serve as a data buffer, whereby the one or more measured conditions from the one or more instrumented substrates 110 are stored by the one or more sealable containers 120 until the data can be transferred to the server 130. By way of another example, the one or more sealable containers 120 may serve as a transmission repeater, whereby the one or more measured conditions from the one or more instrumented substrates 110 immediately (or nearly immediately) transfers the received one or more measured conditions from the one or more instrumented substrates 110 to the server 130. In another embodiment, the conditions measured by the one or more instrumented wafers 110 may be stored on the one or more instrumented substrates 110 and then transferred directly to the server 130 at a later time. In this regard, the measured conditions may be buffered on the one or more instrumented substrates 110 and transferred to the server 130 at a later time.

**[0018]** In another embodiment, the server 130 may receive and analyze the one or more measured conditions. For example, the server 130 may analyze the one or more conditions measured by the instrumented substrate 110 in order to identify one or more deviations in the one or more conditions measured by the one or more instrumented substrates 110.

**[0019]** In another embodiment, the system 100 includes a system controller 140. The system controller 140 may be communicatively coupled to the server 130. In one embodiment, upon completion of the analysis of the measurement one or more conditions from the one or more instrumented substrates 110, the server 130 transmits the analyzed data to a system controller 140. The system controller 140 may include, but is not limited to, a manufacturing execution system (MES).

**[0020]** FIG. 1B illustrates a block diagram view of system 100 for measuring one or more conditions of a semiconductor automation system, in accordance with one or more embodiments of the present disclosure.

**[0021]** In one embodiment, the instrumented substrate 110 includes one or more sensors 111 and support circuitry. The support circuitry may include, but is not limited to, one or more processors (e.g., microprocessor), memory 114, communication circuitry 116 and/or a power supply circuitry 118. FIG. 1C illustrates a simplified schematic view of the instrumented substrate 110, in accordance with one embodiment of the present disclosure. In one embodiment, as shown in FIG. 1C, the one or more sensors 111 and support circuitry of the instrumented substrate 110 are disposed on the surface of the substrate 110. In another embodiment, the one or more sensors 111 and the support circuitry of the instrumented substrate 110 are disposed within the substrate 110 (e.g., disposed within a cavity of the substrate 110).

**[0022]** The one or more sensors 111 of the instrument substrate 110 may include any sensor known in the art suitable for measuring one or more conditions of the instrumented substrate 110 as it passes through a semiconductor automation system. In one embodiment, the one or more sensors 111 include one or more motion sensors. For example, the one or more sensors 111 may include, but are not limited to, a triaxial accelerometer, magnetometer, and/or gyroscope. In another embodiment, the one or more sensors 111 include one or more additional sensors. For example, the one or more sensors 111 include, but are not limited to, a sensor for measuring contamination levels, a humidity sensor for measuring humidity, a gas sensor for measuring the presence of selected gases, a chemical sensor for measuring the presence of selected compounds/chemicals, a temperature sensor for measuring temperature, a pressure sensor for measuring pressure and/or an audio sensor for measuring sound levels. It is noted that the list of sensors provided above is not limiting and is provided merely for illustrative purposes.

**[0023]** Referring again to FIG. 1B, in one embodiment, the one or more sealable containers 120 include one or more processors 122, memory 124, communication circuitry 126, one or more sensors 121 and/or power supply circuitry 128. As noted previously herein, the one or more processors 122, memory 124, communication circuitry 126, one or more sensors 121 and/or power supply circuitry 128 of the one or more sealable containers 102 may be used to expand the capabilities of the one or

more instrumented substrates 110. In another embodiment, the server 130 includes one or more processors 132, memory 134 and communication circuitry 136.

**[0024]** The one or more sensors 121 of the sealable container 120 may serve to measure and monitor various conditions of the sealable container 121 and/or the instrumented wafer 110 contained within the sealable container 120. For example, the one or more sensors 121 of the sealable container 121 measure motion (e.g., acceleration, rotational velocity, and the like) associated with the transport of the sealable container 121, which allows the system 100 to locate the sealable container 120 and the instrumented substrate 110 in a semiconductor fabrication automation system.

**[0025]** The one or more sensors 121 of the sealable container 120 may include any sensor known in the art suitable for measuring one or more conditions within the sealable container 120 as it passes through a semiconductor automation system. In one embodiment, the one or more sensors 121 include one or more motion sensors. For example, the one or more sensors 121 may include, but are not limited to, a triaxial accelerometer, magnetometer, and/or gyroscope. In another embodiment, the one or more sensors 121 include one or more additional sensors. For example, the one or more sensors 121 include, but are not limited to, a sensor for measuring contamination levels, a humidity sensor for measuring humidity, a gas sensor for measuring the presence of selected gases, a chemical sensor for measuring the presence of selected compounds/chemicals, a temperature sensor for measuring temperature, a pressure sensor for measuring pressure and/or an audio sensor for measuring sound levels. It is noted that the list of sensors provided above is not limiting and is provided merely for illustrative purposes.

**[0026]** The one or more processors 112, 122 and 132 of the instrumented substrate 110, the sealable container 120 and/or the server 130 may include any one or more processing elements known in the art. In this sense, the one or more processors may include any microprocessor-type device configured to execute software algorithms and/or program instructions. It should be recognized that the data processing steps described throughout the present disclosure may be carried out by anyone of the

processors 111, 122 and 132. In general, the term “processor” may be broadly defined to encompass any device having one or more processing or logic elements, which execute program instructions from a non-transitory memory medium (e.g., memory 114, 124 and/or 134). Moreover, different subsystems of the system 100 may include processor or logic elements suitable for carrying out at least a portion of the steps described throughout the present disclosure. Therefore, the above description should not be interpreted as a limitation on the present invention but merely an illustration.

**[0027]** The memory 114, 124 and 134 of the instrumented substrate 110, the sealable container 120 and the server 130 may include any data storage medium known in the art suitable for storing program instructions executable by the associated one or more processors. For example, the memory 114, 124 and/or 134 may include a non-transitory memory medium. For instance, the memory may include, but is not limited to, a read-only memory, a random access memory, a magnetic or optical memory device (e.g., disk), a magnetic tape, a solid state drive and the like. In another embodiment, it is noted herein that the memory is configured to store one or more results from the system 100 and/or the output of the various steps described herein. In another embodiment, the memory 114, 124 and/or 134 store the program instructions for causing the one or more associated processors to carry out various data processing steps described through the present disclosure.

**[0028]** In one embodiment, a communication link is established between the communication circuitry 116 of the instrumented substrate 110 and the communication circuitry 126 of sealable container 120. For example, one or more data signals 117 indicative of the one or more conditions measured by the one or more sensors 111 of the instrumented substrate 110 are transmitted via substrate communication circuitry 116 to the container communication circuitry 126. The sealable container 120 may then transfer one or more additional data signals 127 indicative of the sensor data having been stored in the container 120 to the server 130 via the communication link between container communication circuitry 126 and server communication circuitry 136.

**[0029]** In one embodiment, the communication link between the instrumented wafer 110 and the sealable container 120 is established via a wireless connection. Any

wireless communication protocol known in the art may be used to establish the communication link between the instrumented wafer 110 and the sealable container 120. For example, the communication circuitry 116 (e.g., transmitter and receiver) of the substrate 110 and the communication circuitry 126 (e.g., transmitter and receiver) of the container 120 may be configured to establish a radio frequency (RF) data link. For instance, the RF data link between the instrumented substrate 110 and the container 120 may include a short range RF communication protocol, such as, but not limited to, BLUETOOTH (e.g., LOW ENERGY BLUETOOTH), WIFI, or a customized inductively coupled communication link. By way of another example, the communication circuitry 116 of the substrate 110 and the communication circuitry 126 of the container 120 may be configured to establish an infrared data link.

**[0030]** In another embodiment, the communication link between the instrumented substrate 110 and the sealable container 120 is established via a transmission line connection. Any transmission line technology known in the art may be used to establish the communication link 117 between the instrumented wafer 110 and the sealable container 120. For instance, the transmission line may include, but is not limited to, a wireline connection (e.g., TCP/IP) or a fiberline connection (e.g., fiber bundle). In addition, in the case of a transmission line connection, the instrumented substrate 110 and the container 120 may each be equipped with corresponding input-output ports to allow for the direct connection between communication circuitry 116 and 126.

**[0031]** In another embodiment, the communication link between the sealable container 120 and the communication circuitry 136 of the server 130 is established via a wireless connection. Any wireless communication protocol known in the art may be used to establish the communication link between the sealable container 120 and the server 130. For example, the communication circuitry 126 (e.g., transmitter and receiver) of the sealable container 120 and the communication circuitry 136 (e.g., transmitter and receiver) of the server 130 may be configured to establish a radio frequency (RF) data link. For instance, the RF data link between the instrumented substrate 110 and the container 120 may include a RF communication protocol, such as, but not limited to, WIFI, BLUETOOTH, cellular data link or a customized inductively coupled

communication link. By way of another example, the communication circuitry 126 of the sealable container 120 and the communication circuitry 136 of the server 130 may be configured to establish an infrared data link.

**[0032]** In another embodiment, the communication link between the sealable container 120 and the server 130 is established via a transmission line connection. Any transmission line technology known in the art may be used to establish the communication link between the sealable container 120 and the server 130. For instance, the transmission line may include, but is not limited to, a wireline connection (e.g., TCP/IP) or a fiberline connection (e.g., fiber bundle). In addition, in the case of a transmission line connection, the sealable container 120 and the server 130 may each be equipped with corresponding input-output ports to allow for the direct connection between communication circuitry 126 and 136.

**[0033]** In another embodiment, the system 100 includes a user interface 135 communicatively coupled to server 130 via datalink 139. In one embodiment, the user interface 135 is communicatively coupled to the one or more processors 132 of server 130. In another embodiment, the user interface device 135 may be utilized by the server 130 to accept selections and/or instructions from a user, discussed further herein. In some embodiments, described further herein, a display may be used to display data to a user (not shown). In turn, a user may input, via user input device (not shown), a selection and/or instructions responsive to data displayed to the user via the display device.

**[0034]** The user interface device 135 may include any user interface known in the art. For example, the user input device of the user interface 135 may include, but is not limited to, a keyboard, a keypad, a touchscreen, a lever, a knob, a scroll wheel, a track ball, a switch, a dial, a sliding bar, a scroll bar, a slide, a handle, a touch pad, a paddle, a steering wheel, a joystick, a bezel input device or the like. In the case of a touchscreen interface device, those skilled in the art should recognize that a large number of touchscreen interface devices may be suitable for implementation in the present invention. For instance, the display device may be integrated with a touchscreen interface, such as, but not limited to, a capacitive touchscreen, a resistive

touchscreen, a surface acoustic based touchscreen, an infrared based touchscreen, or the like. In a general sense, any touchscreen interface capable of integration with the display portion of a display device is suitable for implementation in the present invention. In another embodiment, the user input device may include, but is not limited to, a bezel mounted interface.

**[0035]** The display device may include any display device known in the art. In one embodiment, the display device may include, but is not limited to, a liquid crystal display (LCD). In another embodiment, the display device may include, but is not limited to, an organic light-emitting diode (OLED) based display. In another embodiment, the display device may include, but is not limited to a CRT display. Those skilled in the art should recognize that a variety of display devices may be suitable for implementation in the present disclosure and the particular choice of display device may depend on a variety of factors, including, but not limited to, form factor, cost, and the like. In a general sense, any display device capable of integration with a user input device (e.g., touchscreen of tablet, bezel mounted interface, keyboard, mouse, trackpad, and the like) is suitable for implementation in the present disclosure.

**[0036]** FIG. 1D illustrates a simplified schematic view 150 depicting the insertion of the instrumented substrate 110 into the sealable container 120, in accordance with one embodiment of the present disclosure. In one embodiment, the sealable container 120 includes a front opening unified pod (FOUP). The contents of the sealable container 120 are accessible to the fabrication line tools (not shown in FIGS. 1A-1B) of the semiconductor automation system for processing. As shown in FIG. 1D, the sealable container 120 may receive and secure the instrumented substrate 110 within the housing of the sealable container 120. As noted previously herein, one or more data signals indicative of the one or more conditions measured by the one or more sensors 111 of the instrumented substrate 110 are transmitted via substrate communication circuitry 116 to the container communication circuitry 126 along datalink 117. The sealable container 120 may then transfer one or more additional data signals indicative of the sensor data having been stored in the container 120 to the server 130 via container communication circuitry 126. In another embodiment, upon measuring one

or more conditions of the sealable container 120 with the container sensors 121, the sealable container 120 may then transfer one or more additional data signals acquired from the sealable container sensors 121 to the server 130 via container communication circuitry 126.

**[0037]** Referring again to FIG. 1B, in one embodiment, the instrumented substrate 110 includes power supply circuitry 118. The power supply circuitry 118 may supply power to the one or more sensors 111 and support circuitry (e.g., one or more processors 112, memory 114 and/or communication circuitry 116) of the instrumented substrate 110. In one embodiment, the power supply circuitry 118 includes one or more rechargeable batteries. In another embodiment, the power supply circuitry 118 includes one or more replaceable batteries. In another embodiment, in the case of a rechargeable battery, the instrumented wafer 110 may include a wireless battery charger, whereby the wireless battery charger receives energy remotely from an external source and then, in turn, charges the one or more rechargeable batteries.

**[0038]** In another embodiment, the sealable container 120 includes power supply circuitry 128. The power supply circuitry 128 of the sealable container 120 may supply power to the instrumented wafer 110 in contained within the sealable container (see FIG. 1D). As described in greater detail further herein, electrical energy may be transferred from a power source of the sealable container 120 via a direct connection (e.g., copper wire) or by inductive coupling. In one embodiment, the power supply circuitry 128 of the sealable container 120 includes one or more replaceable batteries. In one embodiment, the power supply circuitry 128 of the sealable container 120 includes one or more rechargeable batteries.

**[0039]** FIG. 1E illustrates a schematic view 150 of a recharging station 152 for recharging one or more rechargeable batteries of the sealable container 120. In one embodiment, the recharging station 152 wirelessly supplies electrical power to the sealable container 120. In another embodiment, in the case where the sealable container 120 includes one or more rechargeable batteries, the sealable container 120 includes a wireless battery charger, whereby the wireless battery charger receives energy remotely from the charging station 152 and then, in turn, charges the one or

more rechargeable batteries of the sealable container 120. For example, the sealable container 120 may include one or more RF receiving coils (not shown), whereby the energy is inductively transferred from one or more RF transmission coils of the charging station 152 and then, in turn, energy collected by the one or RF receiving coils charges the one or more rechargeable batteries of the sealable container 120.

**[0040]** The system 100 may include any number of charging stations 152 and may be located at any position throughout the semiconductor automation system. For example, one or more charging stations 152 may be located at or near any OHT accessible location (e.g., stockers, buffers, under-track storage, load-ports and etc.). In another embodiment, the charging station 152 is equipped with communication circuitry (not shown in FIG. 1E). For example, the communication circuitry of the charging station may transfer sensor data from the memory 124 of the sealable container 120 or the memory of the instrumented substrate 110 via communication circuitry 126 or 116 respectively. Further, the datalink established between the communication circuitry 126 of the sealable container 120 and/or the communication circuitry 116 of the instrumented substrate 110 may be established via a wireless (e.g., WIFI, BLUETOOTH and the like) or transmission line (e.g., TCP/IP or fiberline) connection.

**[0041]** FIG. 1F illustrates a simplified schematic view 160 of the instrumented wafer 110 equipped with a wireless charger 162 and one or more rechargeable batteries 163 electrically coupled to the wireless charger 162. For example, the wireless battery charger 162 may include one or more RF coils 167, which may inductively receive energy from an external power transfer device or devices. In turn, electrical energy may be transferred to one or more rechargeable batteries 163, which then power the other various electronic components of the instrumented substrate 110.

**[0042]** FIG. 1G illustrates a simplified schematic view 161 of an external power transfer device 165 suitable for transferring power to one or more RF receiving coils 167 of the instrumented substrate 110. In one embodiment, the external power transfer device 165 includes a set of RF transmission coils 164, which serve to inductively transfer power from the external power transfer device 165 to the RF receiving coil 167 of the wireless battery charger 162 of the instrumented wafer 110.

**[0043]** In one embodiment, the external power transfer device 165 may be located at a position below the instrumented wafer 110, allowing the external power transfer device 165 to inductively transfer power to the wireless battery charger 162 disposed on or within the instrumented substrate 110. In another embodiment, the external power transfer device 165 may be located at a position above the instrumented wafer 110. The closer the transmission coil 164 is to the RF receiving coil 167 of the wireless battery charger 162 the better the inductive coupling. For example, the distance between an RF transmission coil 164 and the RF receiving coil 167 of the wireless battery charger 162 may be less than 10 mm.

**[0044]** In another embodiment, the set of RF transmission coils 164 are arranged in a circular array about the perimeter of the external power transfer device 165. It is noted that the RF receiving coil 167 of the wireless battery charger 162 should be aligned with one or more of the RF transmission coils 164 so to direct the magnetic field to establish sufficient coupling and efficient power transfer. Such an aligned arrangement is depicted in FIG. 1G. For example, a single transmission coil 164 may be used to transfer power from the external power transfer device 165 to the RF receiving coil 167 of the wireless battery charger 162 of the instrumented wafer 110. For instance, the RF transmission coil used to transfer power may be the RF transmission coil 168 that is best aligned with the RF receiving coil 167 of the wireless battery charger 162.

**[0045]** FIG. 1H illustrates a simplified schematic view 170 of the external power transfer device 165 disposed within the sealable container 120, in accordance with one embodiment of the present disclose. In one embodiment, the external power transfer device 165 is disposed within the sealable container 120 and below the instrumented wafer 110. For purposes of simplicity, only the wireless battery charger 162 and the RF receiving coil 167 are depicted on the instrumented wafer 110 in FIG. 1H.

**[0046]** In another embodiment, although not shown, the external power transfer device 165 may be coupled to power supply circuitry 128 (shown in FIG. 1B) in the sealable container 120. In this regard, the power supply circuitry 128 of the sealable container 120 may supply power to the external transfer device 165, which, in turn, transmits power to the RF receiving coil 167 of the instrumented wafer 110.

**[0047]** FIG. 1I illustrates a block diagram view 180 of the power transfer from a transmission coil of the external power transfer device 165 to the RF receiving coil 167, in accordance with one embodiment of the present disclosure. In one embodiment, each transmission coil 164a-164d (or group of three transmission coils) is controlled by one of the coil controllers 182a-182d (e.g., integrated circuit power controller). In another embodiment, each controller 182a-182d periodically “pings” its corresponding transmission coil 164a-164d and looks for an inductive load on the corresponding transmission coil 164a-164d. The presence of such an inductive load indicates magnetic coupling between one of the transmission coils 164a-164d and the RF receiving coil 167 of the instrumented wafer 110. In the case where such an inductive load is present on one of the transmission coils 164a-164d, the controller begins power delivery to the RF receiving coil 167 via the transmission coil (e.g., 164b) indicated as experiencing the inductive load.

**[0048]** In another embodiment, control circuitry 184 is communicatively coupled to the coil controllers 182a-182d and is configured to control each of the controllers 182a-182d. In one embodiment, the control circuitry 184 activates the controllers 182a-182d one at a time such that the best aligned transmission coil is selected for the power delivery. For example, the control circuitry 184 may include, but is not limited to, a field programmable gate array (FPGA). In another embodiment, the control circuitry 184 is communicatively coupled to one or more portions of the instrumented wafer 110 (e.g., one or more sensors 111) or one or more portions of the sealable container 120 (e.g., USB channel).

**[0049]** In another embodiment, an active coil controller (e.g., 182b in FIG. I) may monitor input power, losses, and/or the value of power reported by the RF receiving coil 167. Based on this information, the active controller may then estimate how much power is unaccounted for and the presumed lost due to metal objects placed in the wireless power transfer path. In the event that this unexpected loss exceeds a selected threshold, a fault is reported and power transfer is halted.

**[0050]** In another embodiment, the external power transfer device 165 and/or the instrumented substrate 110 includes shielding portions. For example, magnetic

shielding material/elements may be added as a backing to the one or more transmission coils 164 of the external power transfer device 165 and/or the RF receiving coil 167 of the instrumented substrate 110. The shielding serves to direct the magnetic field to the coupled zone. As such, any magnetic field outside the coupled zone does not transfer power. Thus, shielding also serves to contain the fields to avoid coupling to other adjacent system components.

**[0051]** It is noted that the configuration of the external power transfer device 165 is not limited to the arrangement described above and depicted in FIG. 1G, which are provided merely for illustrative purposes. It is recognized herein that any number of transmission coils and any arrangement of transmission coils may be used in the external power transfer device 165.

**[0052]** For example, the arrangement of transmission coils 164 may take on any geometrical arrangement known in the art. For instance, the arrangement may include a circular array, an ellipsoidal array, a polygonal array, a rectangular array, one or more linear arrays and the like. Further, it is noted that the configuration of the receiving coil 167 is not limited to the arrangement described above and depicted in FIG. 1F, which are provided merely for illustrative purposes. It is recognized herein that any number of receiving coils and any arrangement of the receiving coil or coils may be used on the instrumented substrate 110. For instance, the instrumented substrate 110 may include two or more receiving coils 167.

**[0053]** In one embodiment, the system 100 performs a teach sequence to establish a baseline data set. In one embodiment, a baseline data set may be acquired for each semiconductor automation system when the system is installed or otherwise known to be in proper operating condition. In another embodiment, multiple baseline datasets may be acquired to look for normal variations in magnitude of conditions or timing. In one embodiment, the acquired sensor data may be examined manually via user interface to assure that the system is in the correct state. In another embodiment, the acquired sensor data may be examined by one or more algorithms executed by a computational system to assure that the system is in the correct state.

**[0054]** FIG. 2 illustrates graph 200 depicting sensor data measured with the one or more sensors 111 of the instrumented substrate 100 used to perform a teach sequence, in accordance with one embodiment of the present disclosure. Graph 200 illustrates acceleration (measured with one or more motion sensors of the one or more sensors 111) as a function of time for tracking vibration experienced by the instrumented substrate 110 and, thus, product substrates.

**[0055]** In one embodiment, after the sensor data is acquired from the one or more sensors 111, the system 100 can display, via user interface 135, the sensor data along with other system information to enable a user to identify physical locations and critical activities. For example, one or more critical activities may include, but are not limited to, picking up the substrate 110 from the sealable container 120 by a robotic end effector. By way of another example, one or more critical activities may include, but are not limited to, placing the substrate 110 on a measurement stage. This and like information may be captured during the teach cycle and stored as teach data.

**[0056]** It is noted that the substrate 110 takes a physical path as it is moved through the equipment of semiconductor automation system and gather condition/parameter data associated with the movement (and other physical conditions) of the substrate via the one or more sensors 111. In one embodiment, the collected motion data (e.g., acceleration, time and/or gyroscope data) may be used to mathematically reconstruct the motion of the instrumented substrate 110. In another embodiment, the collected motion data (e.g., acceleration, time and/or gyroscope data) may be used to mathematically reconstruct the physical path of the instrumented substrate 110 through the semiconductor automation system. In turn, the constructed path may be displayed on the display of the user interface 135. In another embodiment, the areas along the constructed path may be labeled by the user, thereby providing geographical context for the location. In another embodiment, the motion of the instrumented substrate 110 may be labeled by the user, thereby providing mechanical context for the motion. It is noted that the understanding of the mechanical context of the motion of the instrumented substrate 110 aids in identifying various mechanism failures along the semiconductor fabrication automation system.

**[0057]** For example, as shown in FIG. 2, in the case of placement of a substrate onto an aligner, the user may label or tag this location as "ALIGNER." It is noted that a substrate aligner (e.g., semiconductor wafer aligner) acts to spin the substrate at high speed about an axis. The labeling of this area as ALIGNER allows for this area to be meaningfully noted in future events occurring at this location, compared with other similar aligners on other equipment, or compared to a theoretical model of how such an aligner should perform. It is noted that any number of areas along the constructed path may be labeled by the user. Additional areas include, but are not limited to, one or more sealable one or more substrate stages, which may be labeled as FOUP or STAGE respectively, as shown in FIG. 2.

**[0058]** In another embodiment, program instructions executed by the one or more processors 132 of server 130 may analyze the sensor data to observe changes in the state of the instrumented substrate 110, such as, but not limited to, directional changes registered by the one or more sensors 111 (e.g., accelerometer and/or gyroscope). In another embodiment, these changes in state can be automatically selected and highlighted by the server 130. In turn, the server 132 may transmit a request to label these changes of state to a user via the user interface 135. A series of potential changes of state are depicted in FIG. 2 and are labeled as 202, 204, 206, 208, 210, 212, 214, 216 and 218. These points correspond to different changes of state as the substrate 110 is removed from the sealable container, aligned, processed by the given process tool and then placed back into the sealable container 120. As shown in FIG. 2, point 202 corresponds to the substrate 110 being picked from the sealable container 120 (FOUP). Point 204 corresponds to the placement of the substrate 110 on the aligner. Point 206 corresponds to the start of the alignment of the substrate 110. Point 208 corresponds to the substrate 110 being picked from the aligner. Point 210 corresponds to the placement of the substrate 110 onto a stage. Point 212 corresponds to the execution of a process recipe. Point 214 corresponds to the substrate 110 being picked from the stage. Point 216 corresponds to the placement of the substrate 110 back into the sealable container 110.

**[0059]** It is noted herein that regular monitoring generates data streams which can be compared to the stored baseline either in real-time or offline. Additional data such as the physical location of the substrate 110 may be determined by sensors 111 and/or 121, such as gyroscope and accelerometer data, and may be used to segment the new data and compare each segment with the corresponding segment from the known good baseline data. In another embodiment, the sensor data may be compared to one or more waveforms to identify the behavior of the given segment. For example, the measured sensor data may be compared to one or more simulated waveforms to identify the behavior of the given segment. By way of another example, the measured sensor data may be compared to a waveform from the sensors 111 and/or 121 when the instrument substrate 110 and/or sealable container 120 are in a proper or accepted operating state. By way of another example, the measured sensor data may be compared to a waveform obtained with sensors 111 and/or 121 when the instrument substrate 110 and/or sealable container 120 in an unacceptable operating state. In this regard, a waveform library may be generated, whereby the system 100 may collect waveforms from sensors 111 and/or 121 for various expected problems. In another embodiment, the segment may be compared to a set of segments of similar type across a number of pieces of equipment. For example, the waveform associated with an aligner may be compared to all (or a portion of) aligners in the semiconductor factory. In another embodiment, the data may be processed and the results compared to pre-selected thresholds (e.g., maximum deviations) or other markers derived from the sensor data.

**[0060]** Based on the information provided by the teach data, the implications of any deviation between the baseline and a later measurement can be interpreted to determine if there is a problem with one or more portions of the semiconductor automation system. It is further noted that the one or processors 132 may identify possible errors in the handling of the substrate 110 as it moves from location to location. For example, point 218 in FIG. 2 may correspond to an error or problem when the substrate 110 was transferred to the stage. Once a deviation or possible problem has been identified the context data enables analysis to determine a likely root cause or causes of the given deviation. As the system is utilized new root causes may be

uncovered by the user. An expert system approach may be utilized to capture these new root causes from the users and pattern recognition techniques may be used to recognize future occurrences of these root causes.

**[0061]** FIG. 3 illustrates system 100 implemented within a semiconductor factory 300, in accordance with one embodiment of the present disclosure. In this example, the instrumented substrate 110 is an instrumented wafer, the sealable container 120 is a FOUP and the system controller 140 is a MES. In one embodiment, the server 130 is integrated with station 302 of the semiconductor factory 300. In this regard, the server 130 of station 302 may analyze one or more portions of the semiconductor factory 300 by receiving one or more conditions of the instrumented wafer 110 (from the sensors 111) or the FOUP 120 (from sensors 121), which may be transmitted to the server 130 via datalink 127. In another embodiment, the server 130 may analyze the operation of one or more automation systems of the semiconductor factory 300 by analyzing one or more measured conditions of the instrumented wafer 110 or the FOUP 120 as the automation system(s) move the FOUP 120 along a physical path of the semiconductor factory. As noted, the automation system(s) may include, but are not limited to, automated material handling systems (e.g., OHT, stocker), FOUP/wafer handling systems of process and metrology equipment. For instance, as shown in FIG. 3, the OHT 306 may pick the FOUP 120 from station 302 and transport the FOUP 120 to the target tool 304. As the FOUP 120 is transported through the semiconductor factory 300, the instrumented wafer 110 and/or the FOUP 120 may measure and track one or more conditions of the instrumented wafer 110 and/or the FOUP 120, such as, but not limited to, the leveling, teaching, alignment, cleanliness, pressure, and health of one or more semiconductor factory automation systems. It is noted that in order to accomplish the most relevant results the test instrument substrate 110 is moved through the factory 300 in the same way that product wafers move through the factory. In another embodiment, one or more container chargers 152 may be distributed throughout the factory 300 to charge the FOUP 120 at these various locations.

**[0062]** To integrate system 100 into the semiconductor factory system 300 a sequence of operations may be carried out. In one embodiment, a scheme may be utilized where

the instrumented substrates 110 appear to be standard metrology equipment in the factory system. It is noted that standard equipment may communicate with the factory system controller 140 (e.g., one or more MES servers 140) according to a selected communications protocol. For example, the selected communications protocol may include an industry standard communications protocol consistent with standards defined by the Semiconductor Equipment and Materials Institute (SEMI). These standards are referred to as SEMI Equipment Communications Standards (SECS) and Generic Equipment Model (GEM).

**[0063]** In one embodiment, the one or more servers 130 of system 100 may connect to the system controller (e.g., MES) and interact with the system controller 140 as if it were measurement equipment. The server 130, which is integrated with the automated metrology station 302, may interface with the instrumented wafer 110 and/or the FOUP 120 to establish the parameters of a measurement mission and gather the results. Once the results are gathered by the server, the FOUP 120 or the instrumented wafer 110 may determine that an exception case has occurred. This exception may be reported to the system controller 140 (e.g., MES) as a warning or as an alarm. In another embodiment, the system server 130 may directly communicate with other factory systems and users via methods such as, but not limited to, web-based graphical user interfaces, mobile devices, text messages, or emails.

**[0064]** FIG. 4 illustrates a process flow diagram 400 depicting a set of steps 400 carried out by the system 100 when monitoring a target tool 304 of a semiconductor factory, in accordance with one embodiment of the present disclosure. In step 402, the instrumented wafer 110 and the FOUP 120 are prepared for a mission by the server 130 of station 302. For example, a measurement recipe may be loaded onto the instrumented wafer 110. It is noted that the server 130 may act as a proxy measurement instrument in the semiconductor factory 300 for all the metrology instruments gathering data.

**[0065]** In step 404, the FOUP 120 is transferred from the station 302 to the target tool 306. For example, the OHT 306 may pick the FOUP 120 from the station 302 and transport the FOUP 120 to the target tool 304 (e.g., etcher). In step 406, the product

wafers contained in the FOUP 120 are processed by the target tool 304. For example, in the case of an etcher, the etcher may apply a selected etcher recipe (e.g., Etcher PM recipe). In step 408, while the target tool 304 processes the product wafers, the instrumented wafer 110 measures one or more conditions of the instrumented wafer 110. For example, the instrumented wafer 110 may measure one or more conditions of the instrumented wafer 110 via sensors 111 (shown in FIG. 1B). Further, the FOUP 120 may measure one or more conditions of the FOUP 120 or the wafer 111 via sensors 121 (shown in FIG. 1B). In step 410, the measured sensor data of step 408 is transmitted to the server 130. For example, the measured sensor data may be transferred from the instrumented wafer 110 and to the FOUP 120. In turn, the FOUP 120 may transfer the measured sensor data to the server 130. By way of another example, the measured sensor data may be transferred directly from the instrumented wafer 110 to the server 130. In addition, the measurement context may be provided with the sensor data. For example, in the case of an etcher, the sensor data may be correlated on the server 130 with Etcher 1 using Etcher PM recipe. The server can use these tags and the parameters of a stored recipe to compare the resulting data to baseline or other data.

**[0066]** In step 412, it is determined if additional tools are to be measured in the mission. In the case where additional tools should be measured, the process 400 moves back to step 404 and steps 404-412 are repeated. For example, following measurement of the first target tool 304 (e.g., first etcher), the OHT 306 may transport the FOUP 120 to a second target tool (e.g., second etcher). It is further noted that a batch of one or more instrumented wafers 110 may be issued as a "lot" to follow this test process, thereby triggering measurement of the two target tools (e.g., etchers) and interpretation of the data.

**[0067]** In step 414, the FOUP 120 is transferred back to the station 302. In step 416, the FOUP 120 may transfer sensor data from the FOUP 120 to the station 302. For example, the FOUP 120 may transfer sensor data from the FOUP 120 to the server 103 of station 302. In step 418, the station 302 analyzes the sensor data received from the FOUP 418. For example, the server 130 of station 302 analyzes the sensor data

received from the FOUP 418. For instance, any of the analysis techniques described previously herein may be used to analyze the sensor data. In step 420, the station 302 transfers the analyzed data to the system controller 140. For example, the server 130 of station 302 may transfer the analyzed data to the system controller 140. In the event the analysis of the sensor data identifies a problem, then an alarm or warning message may be sent to the host, independent of the mission and job. Further, the text of the alarm may identify the location of the problem, indicator of detected symptoms, and an estimate of the root cause. For example, a textual report, such as that depicted in FIG. 4B may be provided to a user via user interface 135.

**[0068]** It is noted herein that some components of the semiconductor factory 300 may be better analyzed by a segment-type analysis. For example, the transport components (e.g., OHT) that transport material around the factory may be measured and the vibration data and/or other data may be analyzed with the computed physical location data based on motion sensor data (e.g., accelerometer data, magnetometer data and gyroscope data). Deviations that can be identified as systemic track problems may be physically located using the sensor data. In addition, deviations that follow the transport component may also be identified. It is noted herein that basic moves, such as left, right straight, hoist up and hoist down, may be categorized accordingly and analyzed by comparing the measured sensor data to a baseline or set point for that type of segment.

**[0069]** All of the methods described herein may include storing results of one or more steps of the method embodiments in the memory. The results may include any of the results described herein and may be stored in any manner known in the art. The storage medium may include any storage medium described herein or any other suitable storage medium known in the art. After the results have been stored, the results can be accessed in the storage medium and used by any of the method or system embodiments described herein, formatted for display to a user, used by another software module, method, or system, etc. Furthermore, the results may be stored "permanently," "semi-permanently," temporarily, or for some period of time. For example, the storage medium may be random access memory (RAM), and the results may not necessarily persist indefinitely in the storage medium.

**[0070]** The herein described subject matter sometimes illustrates different components contained within, or connected with, other components. It is to be understood that such depicted architectures are merely exemplary, and that in fact many other architectures can be implemented which achieve the same functionality. In a conceptual sense, any arrangement of components to achieve the same functionality is effectively "associated" such that the desired functionality is achieved. Hence, any two components herein combined to achieve a particular functionality can be seen as "associated with" each other such that the desired functionality is achieved, irrespective of architectures or intermedial components. Likewise, any two components so associated can also be viewed as being "connected", or "coupled", to each other to achieve the desired functionality, and any two components capable of being so associated can also be viewed as being "couplable", to each other to achieve the desired functionality. Specific examples of couplable include but are not limited to physically mateable and/or physically interacting components and/or wirelessly interactable and/or wirelessly interacting components and/or logically interacting and/or logically interactable components.

**[0071]** Furthermore, it is to be understood that the invention is defined by the appended claims. It will be understood by those within the art that, in general, terms used herein, and especially in the appended claims (e.g., bodies of the appended claims) are generally intended as "open" terms (e.g., the term "including" should be interpreted as "including but not limited to," the term "having" should be interpreted as "having at least," the term "includes" should be interpreted as "includes but is not limited to," etc.). It will be further understood by those within the art that if a specific number of an introduced claim recitation is intended, such an intent will be explicitly recited in the claim, and in the absence of such recitation no such intent is present. For example, as an aid to understanding, the following appended claims may contain usage of the introductory phrases "at least one" and "one or more" to introduce claim recitations. However, the use of such phrases should not be construed to imply that the introduction of a claim recitation by the indefinite articles "a" or "an" limits any particular claim containing such introduced claim recitation to inventions containing only one such recitation, even when the same claim includes the introductory phrases "one or more" or

"at least one" and indefinite articles such as "a" or "an" (e.g., "a" and/or "an" should typically be interpreted to mean "at least one" or "one or more"); the same holds true for the use of definite articles used to introduce claim recitations. In addition, even if a specific number of an introduced claim recitation is explicitly recited, those skilled in the art will recognize that such recitation should typically be interpreted to mean at least the recited number (e.g., the bare recitation of "two recitations," without other modifiers, typically means at least two recitations, or two or more recitations). Furthermore, in those instances where a convention analogous to "at least one of A, B, and C, etc." is used, in general such a construction is intended in the sense one having skill in the art would understand the convention (e.g., "a system having at least one of A, B, and C" would include but not be limited to systems that have A alone, B alone, C alone, A and B together, A and C together, B and C together, and/or A, B, and C together, etc.). In those instances where a convention analogous to "at least one of A, B, or C, etc." is used, in general such a construction is intended in the sense one having skill in the art would understand the convention (e.g., "a system having at least one of A, B, or C" would include but not be limited to systems that have A alone, B alone, C alone, A and B together, A and C together, B and C together, and/or A, B, and C together, etc.). It will be further understood by those within the art that virtually any disjunctive word and/or phrase presenting two or more alternative terms, whether in the description, claims, or drawings, should be understood to contemplate the possibilities of including one of the terms, either of the terms, or both terms. For example, the phrase "A or B" will be understood to include the possibilities of "A" or "B" or "A and B."

**[0072]** It is believed that the present disclosure and many of its attendant advantages will be understood by the foregoing description, and it will be apparent that various changes may be made in the form, construction and arrangement of the components without departing from the disclosed subject matter or without sacrificing all of its material advantages. The form described is merely explanatory, and it is the intention of the following claims to encompass and include such changes. Furthermore, it is to be understood that the disclosure is defined by the appended claims.

## CLAIMS

What is claimed is:

1. A system for monitoring one or more conditions of an automation system of a semiconductor factory comprising:

one or more instrumented substrates, wherein the one or more instrumented substrates include one or more sensors, wherein the one or more sensors are configured to measure one or more conditions of the one or more instrumented substrates as the one or more instrumented substrates traverse one or more portions of the semiconductor factory, wherein the one or more instrumented substrates include substrate communication circuitry and one or more processors communicatively coupled to the one or more sensors and the substrate communication circuitry;

one or more sealable containers configured to transport the one or more instrumented substrates through the semiconductor factory, wherein the one or more sealable containers include container communication circuitry, wherein the container communication circuitry is communicatively couplable to the substrate communication circuitry, wherein the container communication circuitry is configured to receive sensor data from the one or more sensors of the one or more instrumented substrates via the substrate communication circuitry; and

one or more system servers, wherein the one or more system servers include server communication circuitry, wherein the server communication circuitry is communicatively couplable to the one or more sealable containers, wherein the server communication circuitry is configured to receive the sensor data from the container communication circuitry, wherein the one or more system servers are configured to identify one or more deviations in the measured one or more conditions of the one or more instrumented substrates.

2. The system of Claim 1, wherein the one or more portions of the semiconductor fabrication facility traversed by the one or more instrumented substrates comprise:

at least one of one or more process tools or one or more metrology tools.

3. The system of Claim 1, further comprising:

a system controller communicatively coupled to the one or more system servers, wherein the system controller includes one or more processors, wherein the one or more processors are configured to execute program instructions configured to cause the one or more processors to load a measurement recipe onto the one or more instrument wafers.

4. The system of Claim 1, wherein the system controller comprises:

a manufacturing execution system (MES).

5. The system of Claim 1, wherein the one or more instrumented substrates comprise:

one or more instrumented semiconductor wafers.

6. The system of Claim 1, wherein at least one of the one or more sensors, the substrate communication circuitry or the one or more processors are disposed on a surface of the one or more instrumented substrates.

7. The system of Claim 1, wherein at least one of the one or more sensors, the substrate communication circuitry or the one or more processors are disposed within the one or more instrumented substrates.

8. The system of Claim 1, wherein the one or more sensors comprise:

at least one of one or more motion sensors, one or more gas sensors, one or more humidity sensors, one or more contamination sensors, one or more chemical sensors, one or more pressure sensors, one or more temperature sensors.

9. The system of Claim 1, wherein the measured one or more conditions are buffered in memory of the one or more instrumented substrates.

10. The system of Claim 1, wherein the substrate communication circuitry and the container communication circuitry are communicatively couplable via a wireless datalink.
11. The system of Claim 1, wherein the substrate communication circuitry and the container communication circuitry are communicatively couplable via a transmission line.
12. The system of Claim 1, wherein the one or more instrumented substrates include power supply circuitry.
13. The system of Claim 12, wherein the power supply circuitry includes one or more wireless chargers.
14. The system of Claim 13, wherein the one or more wireless chargers include one or more radio frequency receiving coils inductively couplable to at least one of a set of radio frequency transmission coils of an external power transfer device.
15. The system of Claim 14, wherein the at least one of a set of radio frequency transmission coils comprises:
  - an array of transmission coils.
16. The system of Claim 15, wherein the array of transmission coils comprises:
  - a circular array of transmission coils.
17. The system of Claim 12, wherein the power supply circuitry includes at least one of one or more rechargeable batteries or one or more replaceable batteries.
18. The system of Claim 1, wherein the container communication circuitry and the server communication circuitry are communicatively couplable via a wireless datalink.

19. The system of Claim 1, wherein the container communication circuitry and the server communication circuitry are communicatively couplable via a transmission line.
20. The system of Claim 1, wherein the sealable container includes power supply circuitry.
21. The system of Claim 20, wherein the power supply circuitry includes one or more wireless chargers.
22. The system of Claim 20, wherein the one or more wireless chargers include one or more radio frequency receiving coils inductively couplable to at least one of a set of radio frequency coils of a charging station.
23. The system of Claim 20, wherein the power supply circuitry includes at least one of one or more rechargeable batteries or one or more replaceable batteries.
24. The system of Claim 1, wherein the sealable container comprises:  
a front opening unified pod (FOUP).
25. The system of Claim 1, wherein the system server is configured to perform a teach sequence to establish a baseline data set for the system.
26. The system of Claim 25, wherein the system server is configured to identify one or more changes in the state of the one or more instrumented substrates by comparing measured sensor data to the baseline data established in the teach sequence.
27. The system of Claim 1, wherein the one or more sealable containers include:

one or more sensors configured to measure one or more conditions of the one or more sealable containers as the sealable container traverses one or more portions of the semiconductor fabrication facility.

28. The system of Claim 1, wherein the one or more sealable containers are configured to transmit the one or more conditions measured with the one or more sensors of the one or more sealable containers to the one or more servers via the container communication circuitry.

29. A sealable container for transporting one or more substrates through a semiconductor factory comprising:

a housing configured to contain an instrumented substrate, wherein the instrumented substrate includes one or more sensors, wherein the one or more sensors are configured to measure one or more conditions of the instrumented substrate as the sealable container transports the instrumented substrate through one or more portions of the semiconductor factory, wherein the instrumented substrate includes substrate communication circuitry and one or more processors communicatively coupled to the one or more sensors and the substrate communication circuitry; and

container communication circuitry, wherein the substrate communication circuitry is configured to transmit sensor data from the one or more sensors to the container communication circuitry, wherein the container communication circuitry is configured to transmit the sensor data to server communication circuitry of a server.

30. The sealable container of Claim 29, wherein the substrate communication circuitry and the container communication circuitry are communicatively couplable via at least one of a wireless datalink or a transmission line.

31. The sealable container of Claim 29, wherein the instrumented substrate includes power supply circuitry.

32. The sealable container of Claim 31, wherein the power supply circuitry includes one or more wireless chargers.

33. The sealable container of Claim 32, wherein the one or more wireless chargers include one or more radio frequency receiving coils inductively couplable to at least one of a set of radio frequency transmission coils of an external power transfer device.

34. The sealable container of Claim 33, wherein the at least one of a set of radio frequency transmission coils comprises:  
an array of transmission coils.

35. The sealable container of Claim 34, wherein the array of transmission coils comprises:  
a circular array of transmission coils.

36. The sealable container of Claim 31, wherein the power supply circuitry includes at least one of one or more rechargeable batteries or one or more replaceable batteries.

37. The sealable container of Claim 29, wherein the container communication circuitry and the server communication circuitry are communicatively couplable via a wireless datalink.

38. The sealable container of Claim 29, wherein the container communication circuitry and the server communication circuitry are communicatively couplable via a transmission line.

39. The sealable container of Claim 29, further comprising:  
power supply circuitry.

40. The sealable container of Claim 39, wherein the power supply circuitry includes one or more wireless chargers.

41. The sealable container of Claim 40, wherein the one or more wireless chargers include one or more radio frequency receiving coils inductively couplable to one or more radio frequency coils of a charging station.

42. The sealable container of Claim 40, wherein the power supply circuitry includes at least one of one or more rechargeable batteries or one or more replaceable batteries.

43. The sealable container of Claim 29, wherein the sealable container comprises: a front opening unified pod (FOUP).

44. The sealable container of Claim 29, wherein the sealable container includes:  
one or more sensors configured to measure one or more conditions of the sealable container as the sealable container traverses one or more portions of the semiconductor factory.

45. The sealable container of Claim 29, wherein the sealable container is configured to transmit the one or more conditions measured with the one or more sensors of the sealable container to the server via the container communication circuitry.

46. A method for monitoring one or more conditions of an automation system of a semiconductor factory comprising:

transporting an instrumented substrate through one or more portions of a semiconductor fabrication facility in a sealable container;

measuring one or more conditions of the instrumented substrate at one or more locations within the semiconductor factory;

transmitting the measured one or more conditions from the instrumented substrate to the sealable container;

transmitting the measured one or more conditions from the sealable container to a system server; and

identifying one or more deviations in the measured one or more conditions with the system server.

47. The method of Claim 46, wherein the identifying one or more deviations in the measured one or more conditions with the system server comprises:

receiving sensor data from at least one of one or more sensors of the instrumented substrate or one or more sensors of the sealable container; and

identifying one or more deviations from an expected state in at least a portion of the received sensor data.

48. The method of Claim 46, further comprising:

identifying a physical location of the identified one or more deviations.

49. The method of Claim 46, further comprising:

identifying a time of the identified one or more deviations relative to one or more known recorded events.

50. The method of Claim 46, wherein the identifying one or more deviations in the measured one or more conditions with the system server comprises:

receiving sensor data from at least one of one or more sensors of the instrumented substrate or one or more sensors of the sealable container;

comparing at least a portion of the received sensor data to one or more models; and

identifying one or more deviations in the at least a portion of the received sensor data from the one or more models.

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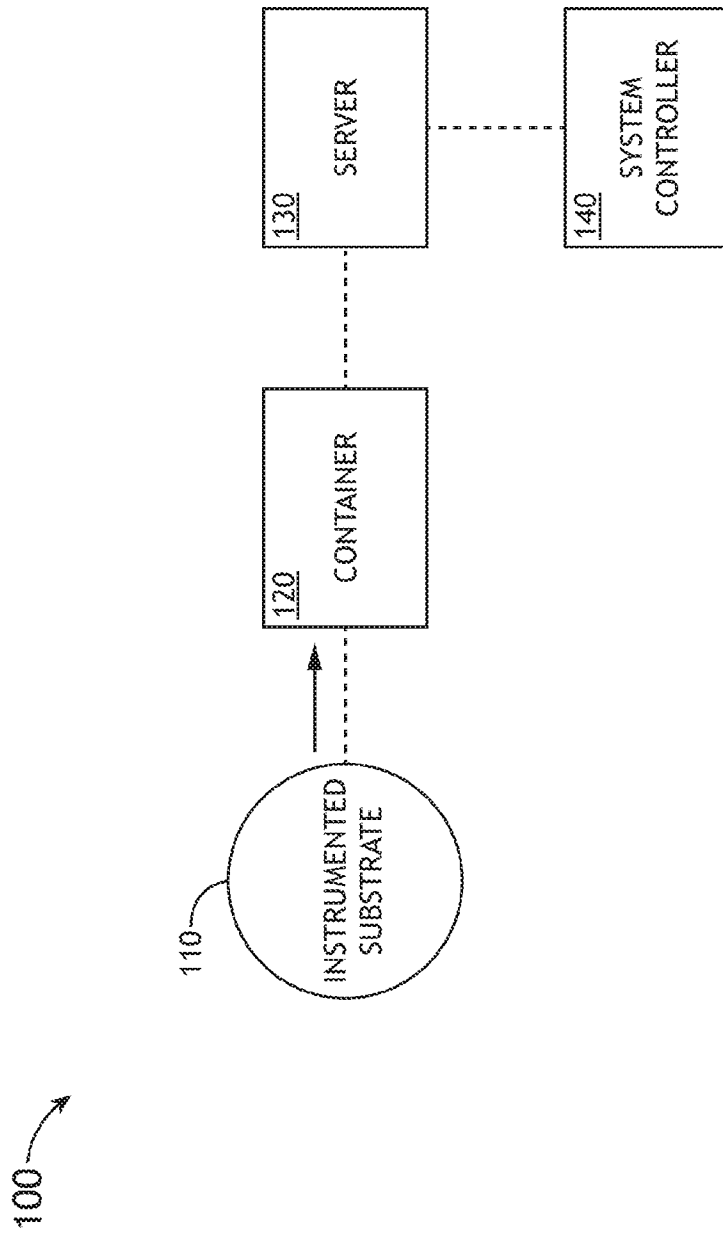
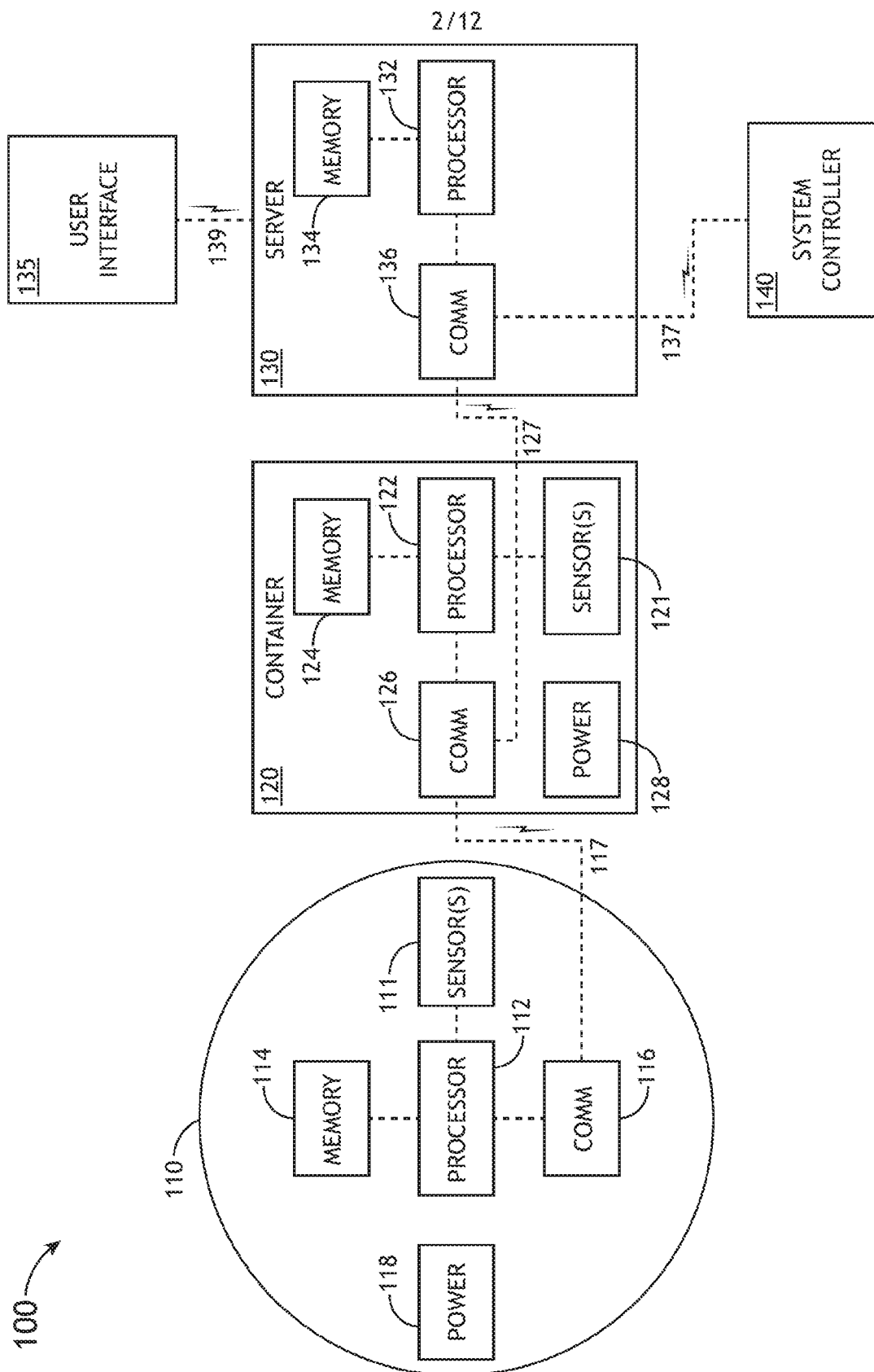


FIG.1A



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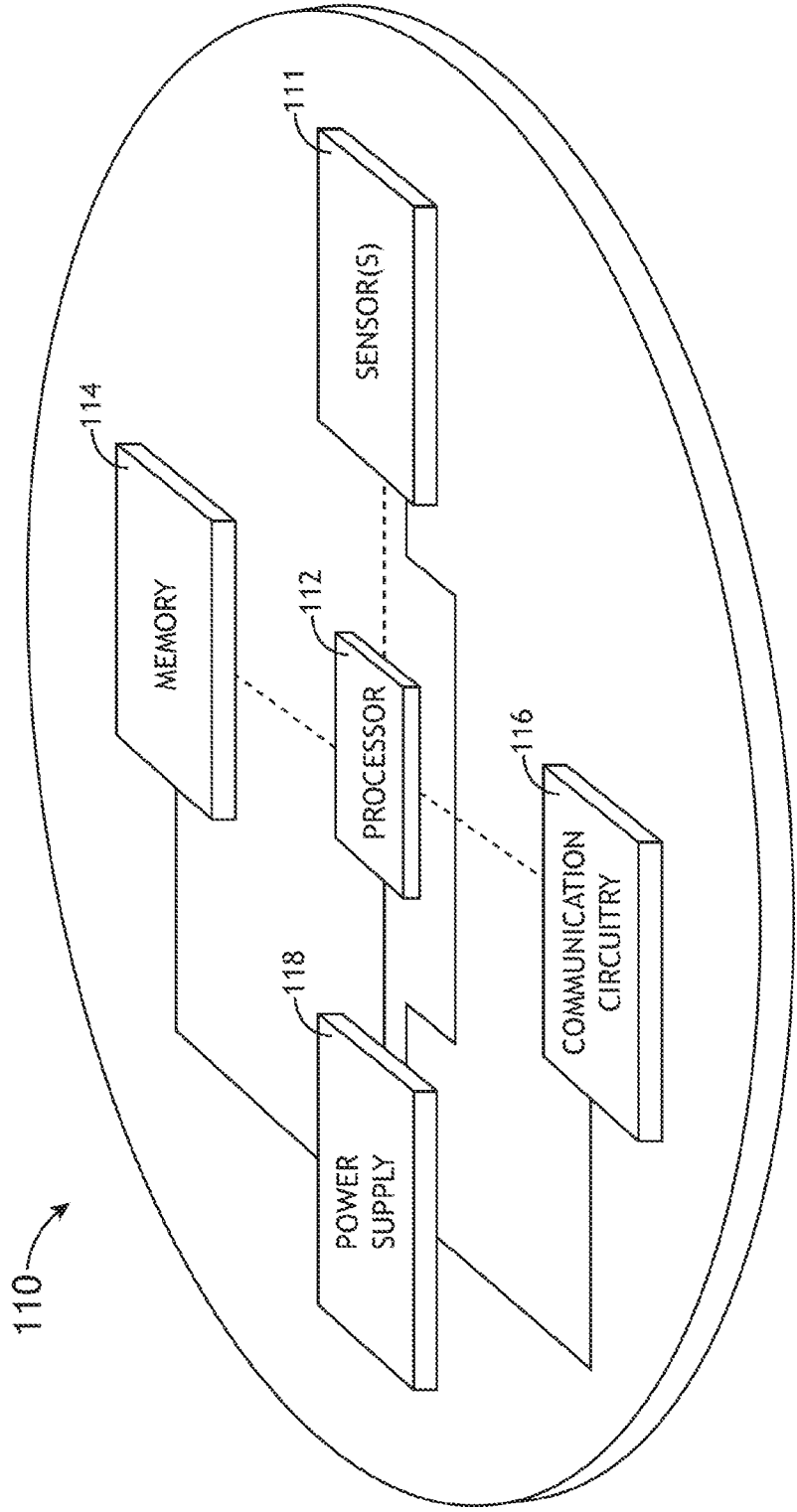


FIG.1C

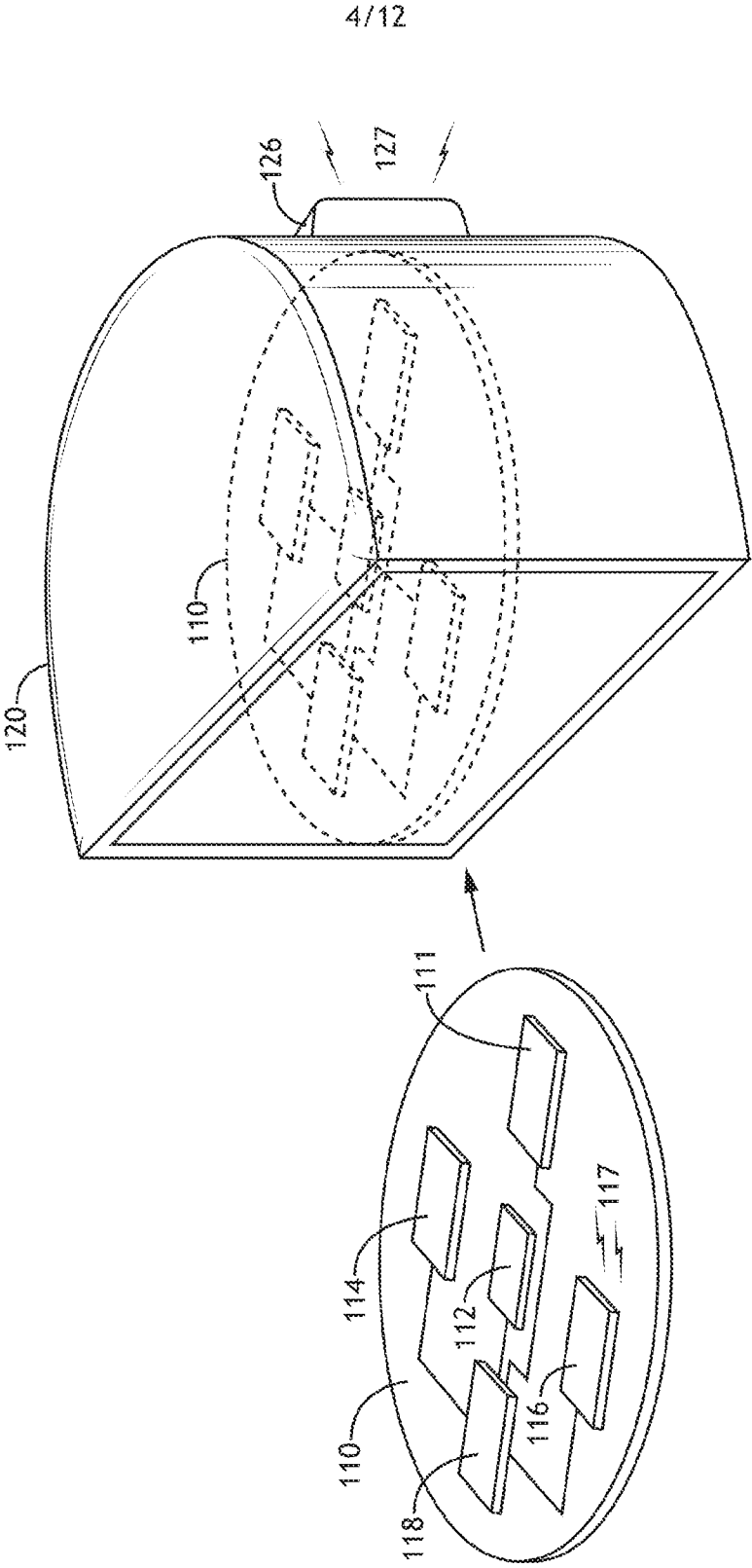


FIG. 1D

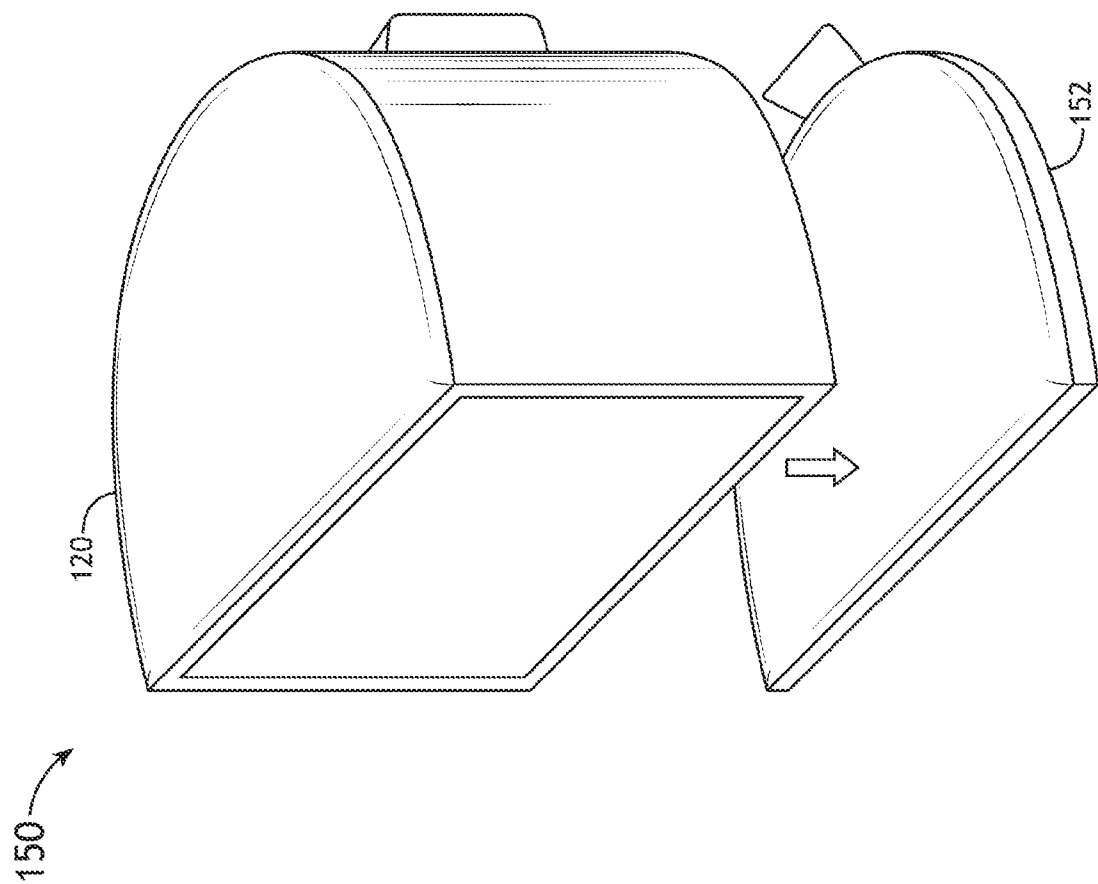


FIG. 1E

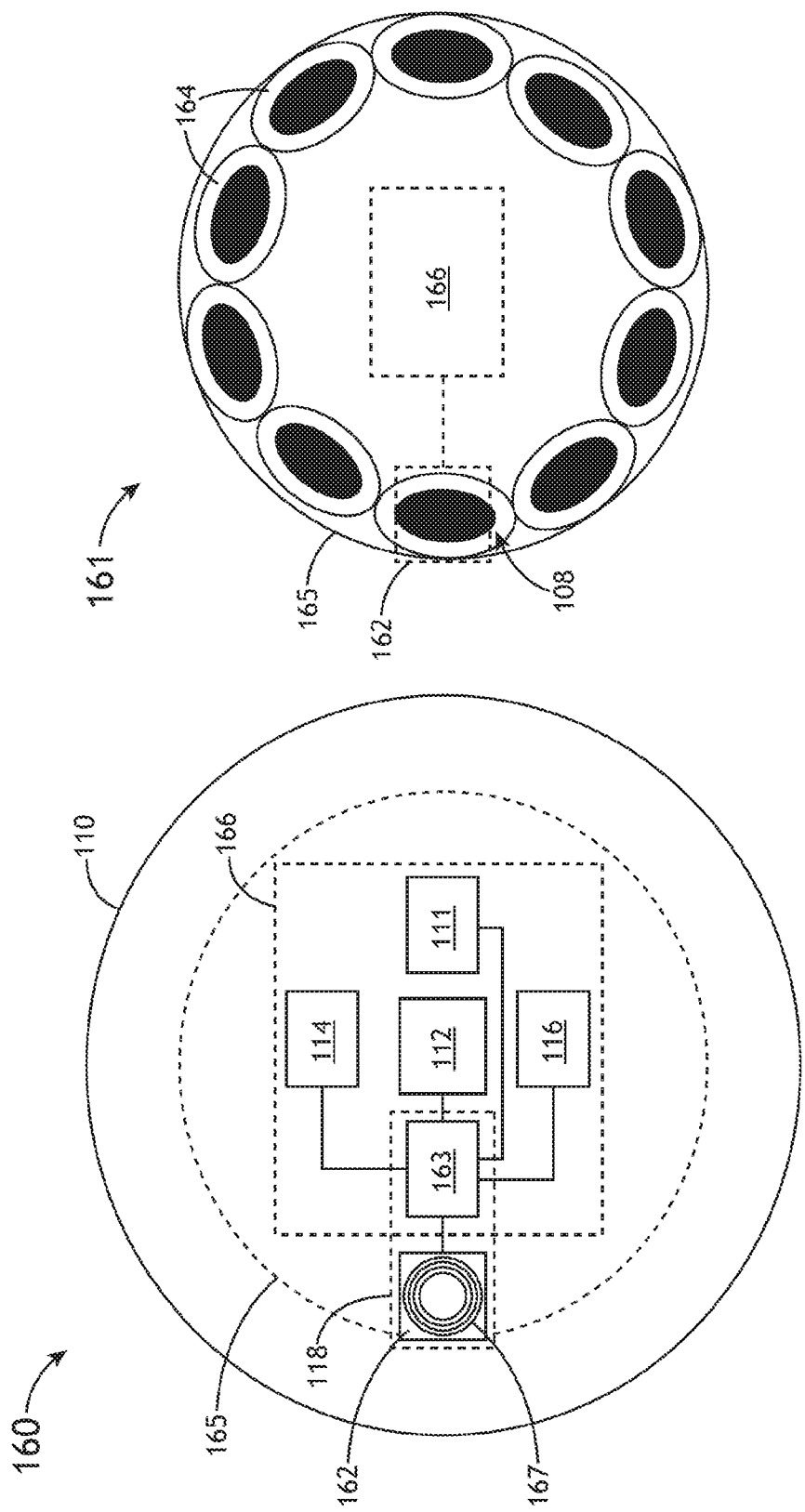


FIG.1G

FIG.1F

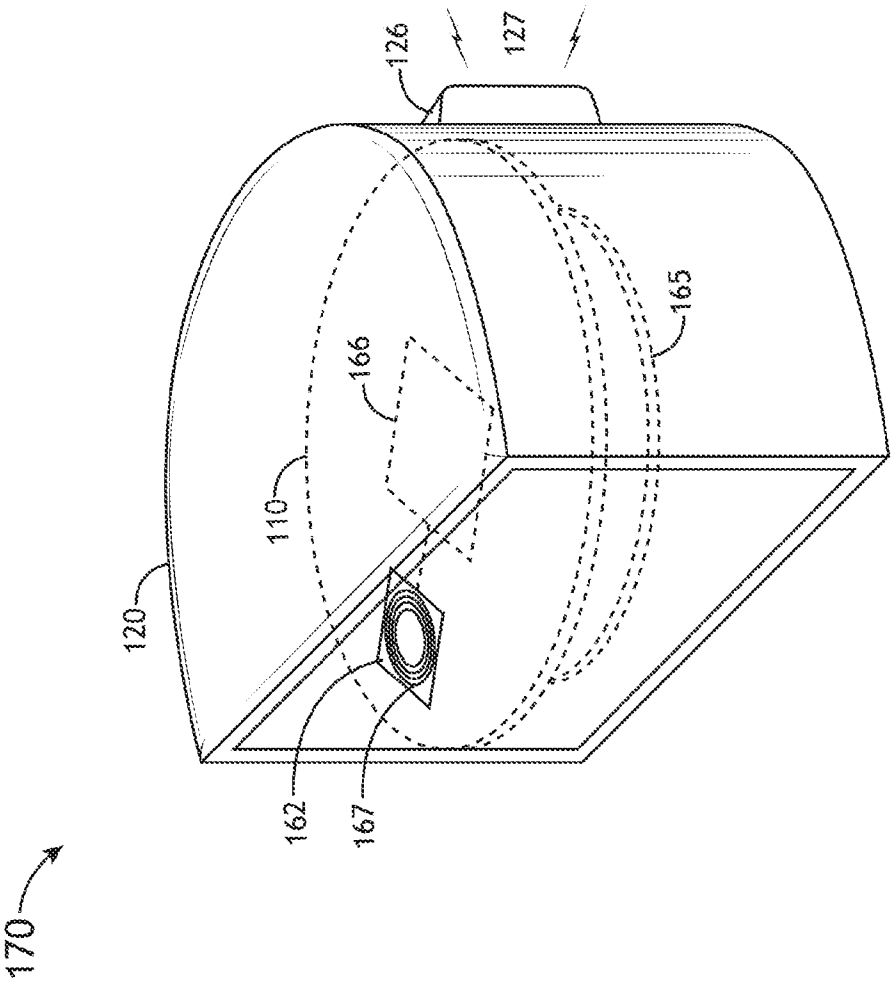


FIG. 1H

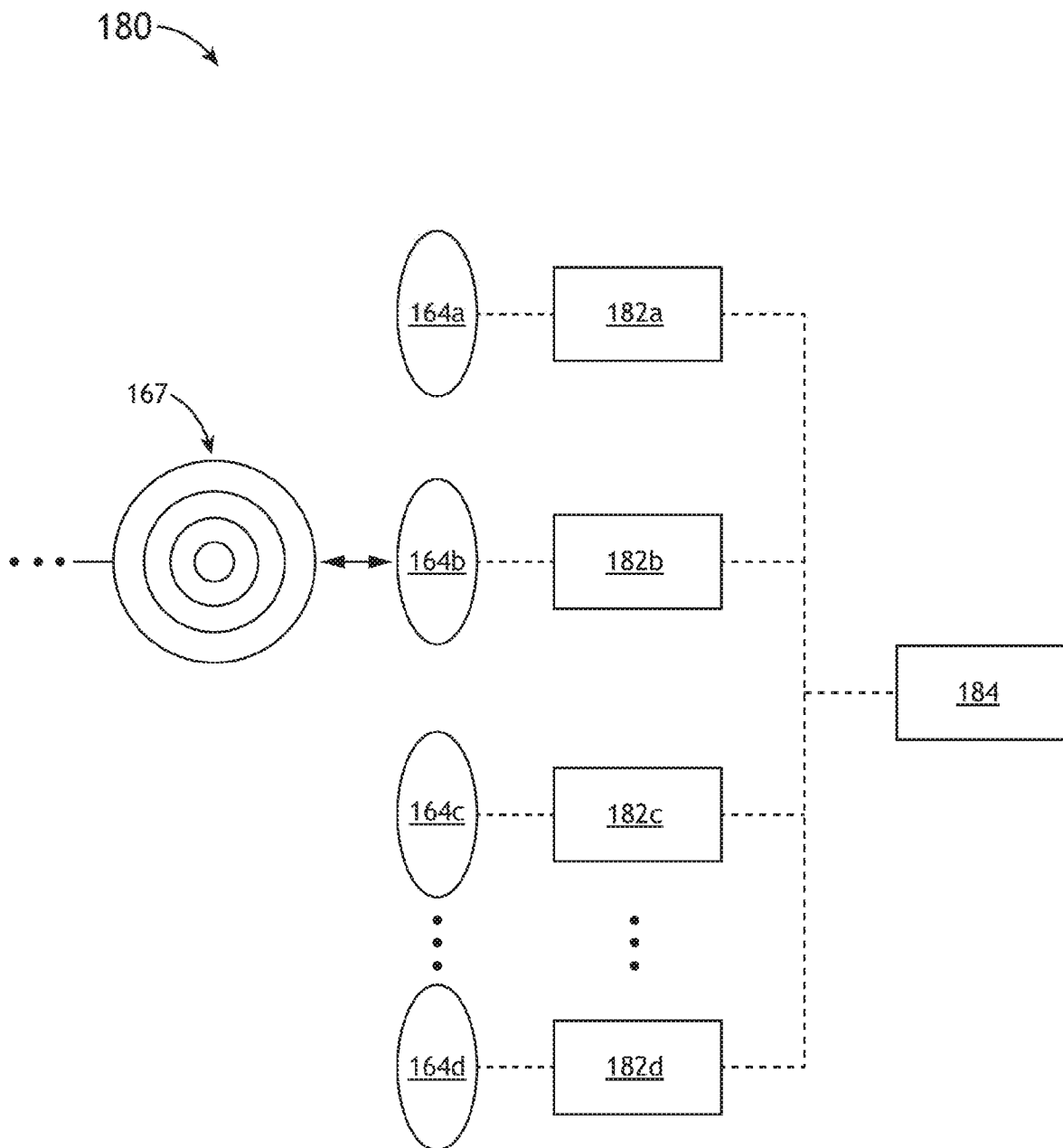


FIG. 11

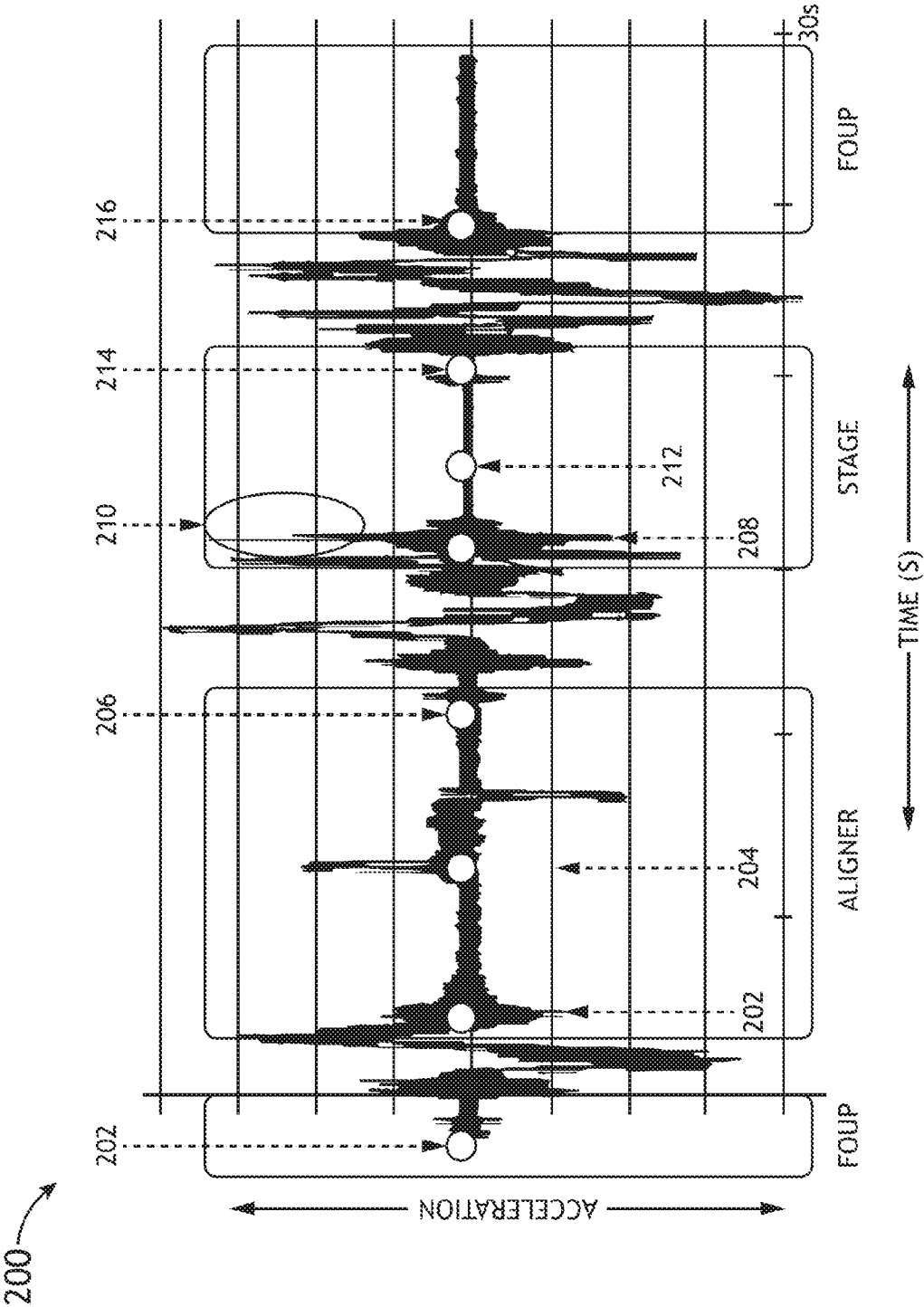
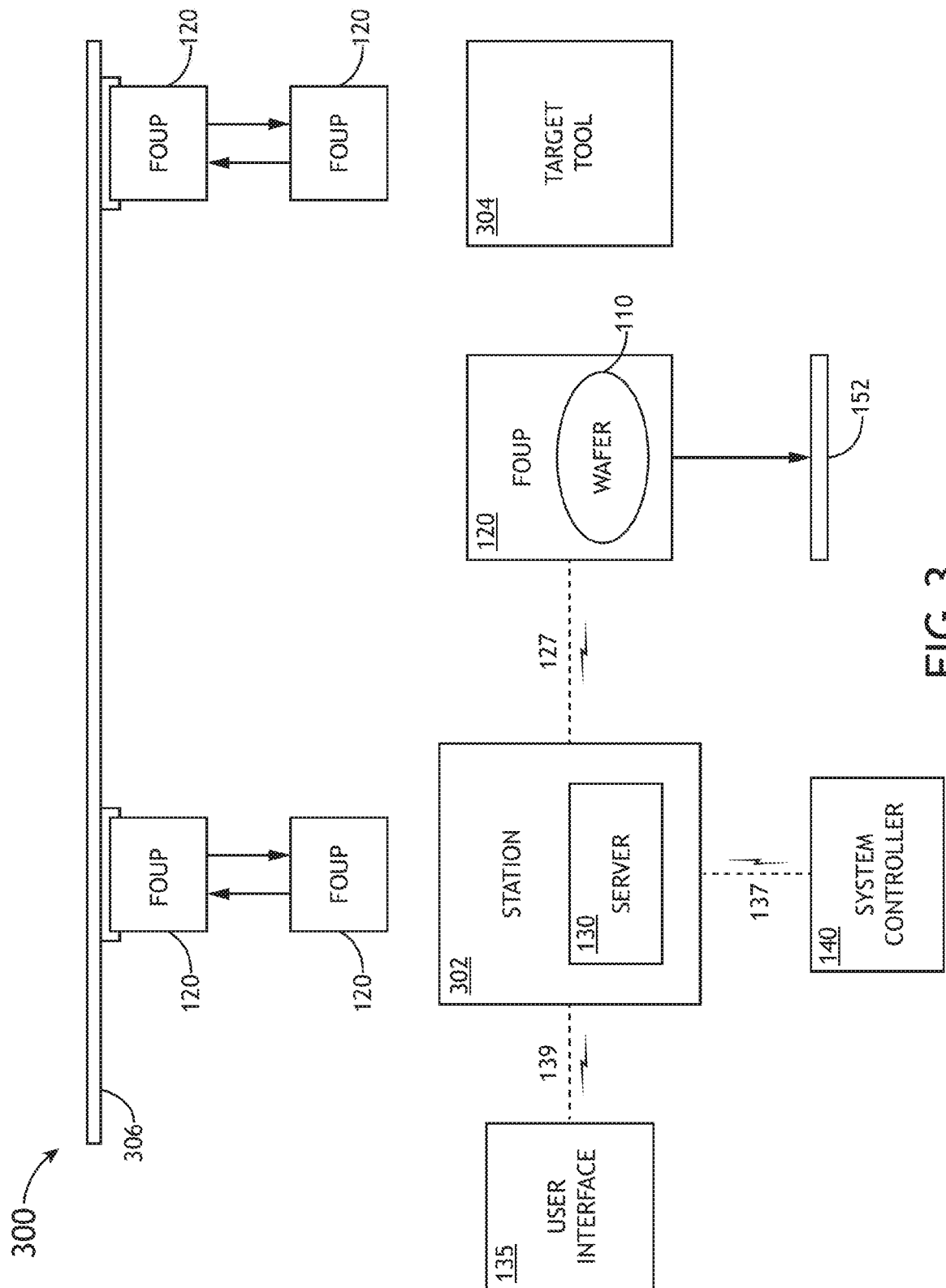


FIG.2



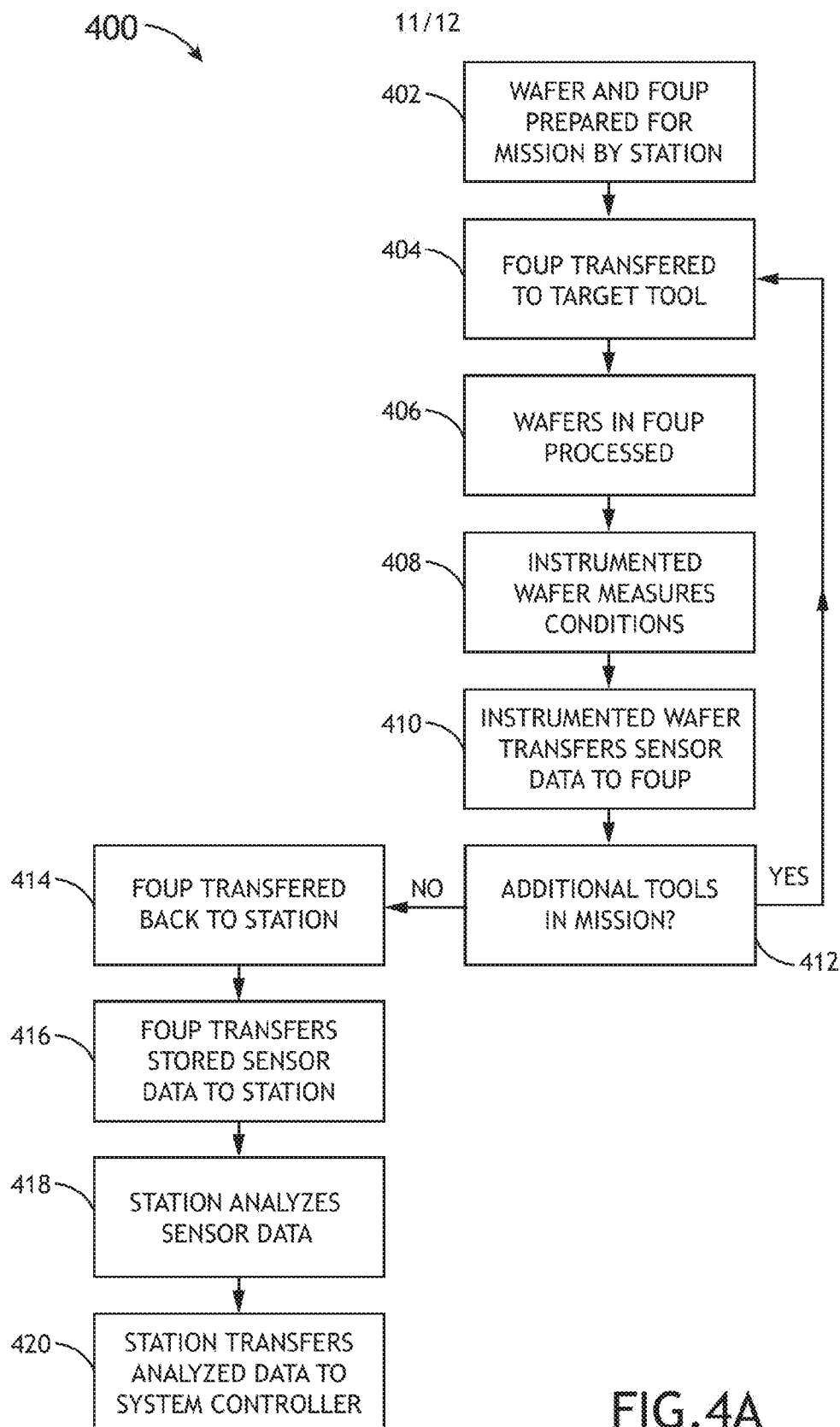


FIG. 4A

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| Step         | Recipe           | Tools Qualified | Parameter |
|--------------|------------------|-----------------|-----------|
| 1 Instrument | Recipe A         | AMSTN-001       | Etcher 1  |
| 2 Etch       | Etcher-PM Recipe | Etcher 1        |           |
| 3 Instrument | Recipe A         | AMSTN-001       | Etcher 2  |
| 4 Etch       | Etcher-PM Recipe | Etcher 2        |           |
| 5 HOLD       |                  |                 |           |

FIG.4B

## INTERNATIONAL SEARCH REPORT

International application No.  
**PCT/US2016/036751****A. CLASSIFICATION OF SUBJECT MATTER****H01L 21/66(2006.01)I, H01L 21/02(2006.01)I, H01L 21/67(2006.01)I**

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/66; G01P 13/00; G06F 15/00; B05C 11/00; G03F 9/00; G01F 1/00; G03F 7/20; H01L 21/00; G01R 27/28; H01L 21/02; H01L 21/67

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) &amp; keywords: semiconductor wafer, measuring process, condition, server

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

| Category* | Citation of document, with indication, where appropriate, of the relevant passages                                                          | Relevant to claim No. |
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| A         |                                                                                                                                             | 27-28, 44-45          |
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Further documents are listed in the continuation of Box C.



See patent family annex.

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"O" document referring to an oral disclosure, use, exhibition or other means

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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&amp;" document member of the same patent family

Date of the actual completion of the international search

12 September 2016 (12.09.2016)

Date of mailing of the international search report

**12 September 2016 (12.09.2016)**

Name and mailing address of the ISA/KR

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**INTERNATIONAL SEARCH REPORT**

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International application No.

**PCT/US2016/036751**

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International application No.

**PCT/US2016/036751**

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