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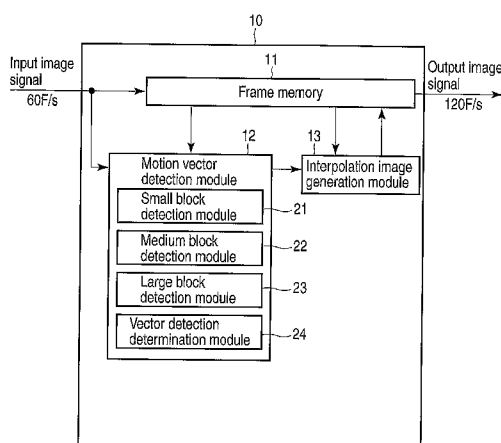


FIG. 1

(57) Abstract: An interpolation frame generation apparatus according to one embodiment, which generates an interpolation frame image to be inserted between continuous frame images, includes a motion vector detection module (12) configured to execute block matching processing in each of a plurality of blocks included in the continuous frame images and specify one motion vector on an interpolation frame, and a generation module (13) configured to, for an interpolation block in which the motion vector detection module specifies one motion vector, generate an interpolation frame image based on the motion vector, and for an interpolation block in which the motion vector detection module does not specify one motion vector, generate an interpolation frame image based on the frame image containing no motion vector component.

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D E S C R I P T I O N

5 INTERPOLATION FRAME GENERATION APPARATUS,
INTERPOLATION FRAME GENERATION METHOD,
AND BROADCAST RECEIVING APPARATUS

Technical Field

One embodiment of the invention relates to an
interpolation frame generation apparatus, interpolation
10 frame generation method, and broadcast receiving
apparatus, which detect a motion vector by block
matching processing and perform interpolation
processing using the motion vector.

Background Art

15 When a liquid crystal display device (LCD)
displays a moving image, it displays a frame image (to
be simply referred to as a frame hereinafter) at a rate
of, e.g., 60 frames/sec. The frame is a sequential
scanning signal obtained by interlaced processing of,
20 e.g., 60 fields/sec. The LCD continues to display a
frame during 1/60 sec.

In viewing such an image displayed on the LCD, the
human eye perceives an immediately preceding frame
image as an afterimage. For this reason, a moving
25 object in the image may get blurred, or its motion may
seem unnatural. To prevent such blur in a moving
image, a method is disclosed, which displays a moving
image while inserting an interpolation frame between
two continuous frames.

Jpn. Pat. Appln. KOKAI Publication No. 2007-60192
discloses an interpolation frame generation apparatus
which detects a motion vector and generates an
interpolation frame using it, thereby more clearly
5 displaying a moving image.

However, the technique disclosed in Jpn. Pat.
Appln. KOKAI Publication No. 2007-60192 always obtains
a motion vector even when it is undetectable because of
the motion of an image pattern. If the detected motion
10 vector is inaccurate, the image quality may degrade.

Disclosure of Invention

It is an object of the invention to provide an
interpolation frame generation apparatus, interpolation
frame generation method, and broadcast receiving
15 apparatus, which perform high-quality interpolation
frame processing by eliminating the influence of an
inaccurate motion vector.

According to one embodiment of the invention,
there is provided an interpolation frame generation
20 apparatus for generating an interpolation frame image
to be inserted between continuous frame images,
comprising: a motion vector detection module configured
to execute block matching processing in each of a
plurality of blocks included in the continuous frame
25 images and specify one motion vector on an
interpolation frame; and a generation module configured
to, for an interpolation block in which the motion

vector detection module specifies one motion vector,
generate an interpolation frame image based on the
motion vector, and for an interpolation block in which
the motion vector detection module does not specify one
5 motion vector, generate an interpolation frame image
based on the frame image containing no motion vector
component.

According to the embodiment, there is also
provided an interpolation frame generation method of
10 generating an interpolation frame image to be inserted
between continuous frame images, comprising: executing
block matching processing in each of a plurality of
blocks included in the continuous frame images and
specifying one motion vector on an interpolation frame;
15 for an interpolation block in which one motion vector
is specified, generating an interpolation frame image
based on the motion vector; and for an interpolation
block in which one motion vector is not specified,
generating an interpolation frame image based on the
20 frame image containing no motion vector component.

According to the embodiment, there is also
provided a broadcast receiving apparatus comprising: a
tuner module configured to select a channel of a
broadcast signal; a decoder module configured to decode
25 the broadcast signal from the tuner module and output a
video signal; a motion vector detection module
configured to execute block matching processing in each

of a plurality of blocks included in continuous frame images contained in the broadcast signal from the decoder module and specify one motion vector on an interpolation frame; a generation module configured to, for an interpolation block in which the motion vector detection module specifies one motion vector, generate an interpolation frame image based on the motion vector, and for an interpolation block in which the motion vector detection module does not specify one motion vector, generate an interpolation frame image based on the frame image containing no motion vector component and insert the interpolation frame image between the continuous frame images; and a display module configured to display, on a screen, an image based on the frame images in which the interpolation image is inserted.

Brief Description of Drawings

FIG. 1 is a block diagram showing the arrangement of an interpolation frame generation apparatus according to an embodiment of the invention;

FIG. 2 is an explanatory view showing block matching processing of the interpolation frame generation apparatus according to the embodiment;

FIG. 3 is an explanatory view showing a fixed-length block in the interpolation frame generation apparatus according to the embodiment;

FIG. 4 is an explanatory view showing matching

processing using a variable-length block in the interpolation frame generation apparatus according to the embodiment;

FIG. 5 is a graph showing a SAD characteristic obtained by block matching processing using a fixed-length block and a variable-length block in the interpolation frame generation apparatus according to the embodiment;

FIG. 6 is a view for explaining matching processing of a frame using a small block, matching processing using a medium block (variable length), and matching processing using a large block (fixed length) in the interpolation frame generation apparatus according to the embodiment, and the distribution of finally selected vectors;

FIG. 7 is a flowchart for explaining motion vector detection processing using a small block, medium block, and large block in the interpolation frame generation apparatus according to the embodiment;

FIG. 8 is a flowchart for explaining motion vector detection processing using a small block and a medium block in the interpolation frame generation apparatus according to the embodiment;

FIG. 9 is a flowchart for explaining motion vector detection processing using a small block and a large block in the interpolation frame generation apparatus according to the embodiment;

FIG. 10 is a flowchart for explaining processing of generating an interpolation frame according to a motion vector amount in the interpolation frame generation apparatus according to the embodiment; and

5 FIG. 11 is a block diagram showing the arrangement of a broadcast receiving apparatus using the interpolation frame generation apparatus according to the embodiment.

Best Mode for Carrying Out the Invention

10 Various embodiments according to the invention will be described hereinafter with reference to the accompanying drawings. In general, an interpolation frame generation apparatus according to one embodiment of the invention, which generates an interpolation
15 frame image to be inserted between continuous frame images, comprises a motion vector detection module configured to execute block matching processing in each of a plurality of blocks included in the continuous frame images and specify one motion vector on an
20 interpolation frame, and a generation module configured to, for an interpolation block in which the motion vector detection module specifies one motion vector, generate an interpolation frame image based on the motion vector, and for an interpolation block in which
25 the motion vector detection module does not specify one motion vector, generate an interpolation frame image based on the frame image containing no motion vector

component.

An embodiment of the invention will be described below with reference to the accompanying drawing.

FIG. 1 is a block diagram showing an example of
5 the arrangement of an interpolation frame generation apparatus (interpolation frame generation system) according to an embodiment of the invention.

<Interpolation Frame Generation Apparatus According to Embodiment of Invention>

10 (Arrangement)

An example of the arrangement of an interpolation frame generation apparatus according to an embodiment of the invention will be described first with reference to the accompanying drawing. As shown in FIG. 1, an
15 interpolation frame generation apparatus 10 according to an embodiment of the invention has a frame memory 11 which receives an input image signal, a motion vector detection module 12 which receives the input image signal from the input terminal and the frame memory 11,
20 and an interpolation image generation module 13 which receives a motion vector from the motion vector detection module 12 and the input image signal from the frame memory 11 and generates an interpolation image.

The motion vector detection module 12 includes,
25 e.g., a small block detection module 21 which detects a motion vector for a small block, a medium block detection module 22 which detects a motion vector for a

medium block (variable length), a large block detection module 23 which detects a motion vector for a large block (fixed length), and a vector detection determination module 24 which collectively determines whether the detection modules have detected motion vectors.

<Interpolation Frame Generation Processing of Interpolation Frame Generation Apparatus According to Embodiment of Invention>

Interpolation frame generation processing of the interpolation frame generation apparatus according to the embodiment of the invention will be described next in detail with reference to the accompanying drawing. FIG. 2 is an explanatory view showing an example of block matching processing of the interpolation frame generation apparatus according to the embodiment of the invention.

(Block Matching Processing of Fixed-Length Block and Variable-Length Block)

First, block matching processing of a fixed-length block and a variable-length block will be described with reference to the accompanying drawing.

· Block Matching Processing

As described above, the interpolation image generation module 13 generates an interpolation frame image based on an inter-frame motion vector detected by the motion vector detection module 12. A motion vector

is detected as the direction and distance of the motion of an object by matching image blocks between two preceding and succeeding frames or more input frames.

5 A new interpolation frame to be located between the two input frames is created using the motion vector of each block and inserted between the input frames, thereby increasing the number of frames and displaying a moving image.

Block matching is a method of detecting, one by one, which image block 40-1 of a predetermined size in a region 40 of a frame 31 matches which image block 42-1 of a predetermined size in a region 42 of a succeeding frame 33, as shown in FIG. 2.

For this purpose, the difference between corresponding pixels is calculated between an image block of the preceding frame and an image block of the succeeding frame. An image block of the succeeding frame, in which the sum (SAD: Sum of Absolute Difference) of differences is minimum, is detected as an image block most similar to the image block of the preceding frame. The difference of the positions of the most similar image blocks between the preceding frame and the succeeding frame is detected as a motion vector.

25 · Fixed-Length Block (Small Block) and Variable-Length Block (Large Block)

The block matching processing is not necessarily

done using the fixed-length block 40-1 having a fixed block size, as shown in FIG. 2. It is sometimes more effective to use variable-length block having variable block sizes.

5 FIG. 3 is an explanatory view showing an example of a fixed-length block in the interpolation frame generation apparatus according to the embodiment of the invention. Assume that a periodical pattern such as a vertical stripe pattern exists in each input frame when
10 estimating the motion of an object based on block matching using SAD, as shown in FIG. 3. When a fixed-length image block is used in the periodical pattern, it is impossible to estimate an accurate motion vector based only on the SAD value.

15 FIG. 5 is a graph showing a SAD characteristic obtained by block matching processing using a fixed-length block and a variable-length block in the interpolation frame generation apparatus.

 In the graph of FIG. 5, a SAD value α of the
20 fixed-length block exhibits motion vector candidates of a plurality of indistinguishable minimal values P1, P2, and P3, and block matching is difficult.

 FIG. 4 is an explanatory view showing an example of matching processing using a variable-length block in
25 the interpolation frame generation apparatus according to the embodiment of the invention. Matching processing is performed using a variable-length block

which is sufficiently large with respect to a periodical pattern such as a vertical stripe pattern shown in FIG. 4. In block matching using the variable-length block, a SAD value β of the variable-length block in the graph of FIG. 5 exhibits not a plurality
5 of minimal values but only one minimal value PL1. Hence, block matching is not difficult.

For the interpolation frame creation method which detects a motion vector by block matching, it is
10 effective to use at least two blocks of different sizes. Normally, a motion vector detected using a smaller one of the two blocks is used. If a plurality of motion vector candidates are detected upon vector detection using the small block, a vector detected
15 using the large block is referred to. This allows to select a genuine motion vector candidate from the plurality of motion vector candidates detected using the small block.

In the graph shown in FIG. 5, it is possible to
20 determine, by referring to the SAD value β of the variable-length block, that the genuine motion vector candidate is the minimal value P2 closest to the minimal value PL1. This improves the detection accuracy of a motion vector in a video image that
25 includes a periodical pattern.

· Size Determination of Variable-Length Block
(Large Block)

An example of a reference to determine an appropriate block size a variable-length block (large
5 block) for motion detection will be described.

As shown in FIG. 3, the rectangular regions of small blocks B1, B2, and B3 include identical images which cannot be distinguished. However, as shown in FIG. 4, the rectangular regions of large blocks B4, B5,
10 and B6 whose size covers the whole width of the periodical repetitive pattern include different images. It is therefore possible to uniquely detect a correct motion vector.

Hence, the appropriate size of the large block in
15 motion detection is a size that is large enough not to be affected by a periodical repetitive pattern and is also not larger than necessary to include a plurality of objects with motions.

As a detailed method of the width of a periodical
20 repetitive pattern, "a region where a plurality of motion vector candidates are generated" is detected. Then, "the size of the variable-length block (large block)" is determined based on the width of the region.

That is, a determination method based on, e.g.,
25 "a region where a plurality of motion vector candidates are generated" $\approx$ "the size of the variable-length block (large block)"

or

"a region where a plurality of motion vector candidates are generated" $\approx$ "the size of the variable-length block (large block)" + a (constant) is preferable. However, the invention is not limited to this.

More specifically, as another method of calculating the width of a periodical repetitive pattern,

· in vector detection using the small block, the number of times of continuous occurrence of a phenomenon that a plurality of reliable motion vector candidates are found (i.e., an indeterminate state in which no vector can uniquely be detected) is measured, and a width corresponding to it is determined,

or

· the number of times of repetition of a unit pattern on a line of an input image is measured, and a width corresponding to it is determined.

(Explanation of Outline)

In the embodiment of the invention, if motion vector determination has failed in spite of repetitive motion vector detection, further detection is abandoned. Instead, an interpolation frame image is generated and used without referring to a motion vector.

That is, if a motion vector is detectable, it is

used. If no motion vector is detectable, a stable interpolation frame image based on the preceding frame image or succeeding frame image is generated and used without detecting a poor-quality motion vector.

5 As a detailed example, four steps which yield the following results are applicable, as shown in FIG. 6. FIG. 6 is a view for explaining matching processing of a frame using a small block, matching processing using a medium block (variable length), and matching
10 processing using a large block (fixed length) in the interpolation frame generation apparatus according to the embodiment of the invention, and the distribution of finally selected vectors.

FIG. 6 explains the following four steps.

15 1) Motion vector detection by small blocks a:
Since the vector detection results of the small blocks themselves are reliable, a vector having a minimum SAD is selected from the vector detection results of the small blocks a (step S1).
20 2) Motion vector detection using medium blocks (variable length) b: Since the vector detection results of the medium blocks (variable length) b are reliable, a vector having a minimum SAD is selected from the vector detection results of the medium blocks (variable
25 length) b, and a vector detection minimal point in a small block a close to the value is selected (step S2).

3) Motion vector detection using large blocks

(fixed length) c: Since the vector detection results of the large blocks (fixed length) c are reliable, a vector having a minimum SAD is selected from the vector detection results of the large blocks (fixed length) c, and a vector detection minimal point in a small block a close to the value is selected (step S3).

4) An example of the distribution of finally selected vectors: The vectors in the respective blocks of one frame image are classified into four results A to D, including a case in which no motion vector is used (step S4).

Result A: Result A of motion vector detection using the small block a

Result B: Result B of motion vector detection using the medium block b

Result C: Result C of motion vector detection using the large block c

Result D: Result D of stopped motion vector detection

In the result D, vector detection is stopped upon determining that accurate vector detection is difficult because no reliable vector is detectable in steps S1, S2, and S3. An instruction to generate an interpolation frame image without using a motion vector is sent to the interpolation image generation module

(Detailed Description of Interpolation Frame Generation Processing Using Flowchart: FIG. 7)

Interpolation frame generation processing will be described below in detail with reference to the flowchart in FIG. 7. The flowchart in FIG. 7 explains an example of motion vector detection processing using a small block, medium block, and large block in the interpolation frame generation apparatus according to the embodiment of the invention.

The blocks of the flowcharts in FIGS. 7 to 10 to be described below can be replaced with circuit blocks. Hence, all the blocks of the flowcharts can be redefined as circuit blocks.

Motion vector detection to be executed in the order of "small block, medium block, and large block" will be described here.

The motion vector detection module 12 receives an image signal of 60 frames/sec as an input image signal. The small block detection module 21 in the motion vector detection module 12 executes motion vector detection for the image signal using the small block a, as shown in step S1 of FIG. 6. In this case, since the vector detection results of the small blocks themselves are reliable, a vector having a minimum SAD is selected from the vector detection results of the small blocks a (block 21). If the vector detection results of the small blocks include only one value that satisfies

$| \text{minimum value} - \text{minimal value} | < (\text{threshold value}),$

one motion vector is specified (block 22). In this case, the process advances to result A. The small block detection module 21 selects a motion vector with a minimum SAD from the detection results of the small blocks (block 23).

However, if two or more values satisfy $| \text{minimum value} - \text{minimal value} | < (\text{threshold value})$, no motion vector can be specified.

Then, the medium block detection module 22 executes vector detection using medium blocks having variable lengths, as shown in step S2 of FIG. 6 (block 24). The block length is determined by, e.g., detecting "a region where a plurality of motion vector candidates are generated", and determining "the size of the variable-length block (medium block)" based on the width of the region.

If the medium block detection module 22 can specify, among the detection results, only one value that satisfies

$| \text{minimum value} - \text{minimal value} | < (\text{threshold value})$,

one motion vector is specified (block 25). In this case, the process advances to result B. The medium block detection module 22 selects a motion vector with a minimum SAD from the detection results of the medium blocks and selects a vector detection minimal point in

a small block close to the value (block 26).

However, if two or more values satisfy

$| \text{minimum value} - \text{minimal value } i | < (\text{threshold value}),$

5 no motion vector can be specified.

Then, the large block detection module 23 executes vector detection using large blocks having a fixed length, as shown in step S3 of FIG. 6 (block 27).

If the large block detection module 23 can
10 specify, among the detection results, only one value that satisfies

$| \text{minimum value} - \text{minimal value } i | < (\text{threshold value}),$

one motion vector is specified. In this case, the
15 process advances to result C. The large block detection module 23 selects a motion vector with a minimum SAD from the detection results of the large blocks and selects a vector detection minimal point in a small block close to the value (block 29).

20 However, if two or more values satisfy

$| \text{minimum value} - \text{minimal value } i | < (\text{threshold value}),$

no motion vector can be specified (block 28).

Determining that only an inaccurate motion vector
25 will be detected by further vector detection, the vector detection determination module 24 interrupts vector detection (block 30).

The motion vector detected by the small block detection module 21, the motion vector detected by the medium block detection module 22, or the motion vector detected by the large block detection module 23 is
5 supplied to the interpolation image generation module 13. The interpolation image generation module 13 generates part of an interpolation frame image corresponding to the motion vector in cooperation with the frame memory 11 and inserts the interpolation frame
10 image between the preceding frame image and the succeeding frame image (block 31).

On the other hand, when vector detection is interrupted in block 30, the interpolation image generation module 13 generates part of an interpolation
15 frame image containing no motion vector component in cooperation with the frame memory 11 and inserts the interpolation frame image between the preceding frame image and the succeeding frame image. For example, the preceding frame image 31, the succeeding frame image
20 33, or a mixed frame of the preceding frame image 31 and succeeding frame image 33 shown in FIG. 2 is inserted between the preceding frame image and the succeeding frame image as part of the interpolation frame image (block 32).

25 This prevents the part of the interpolation frame image from being affected by a motion vector, so the problem of image quality degradation caused by use of

an inappropriate interpolation frame image corresponding to an inaccurate motion vector hard to detect is not posed.

As indicated by the selection result distribution
5 in one interpolation screen in step S4 of FIG. 6, an interpolation frame image based on a motion vector detected by the small block is applied in the result A. In the result B, an interpolation frame image based on a motion vector detected by the medium block is
10 applied. In the result C, an interpolation frame image based on a motion vector detected by the large block is applied. In the result D, an interpolation frame image containing no motion vector component is applied. It is therefore possible to provide an appropriate
15 interpolation frame image in each result as a whole.

As described above, the procedure shown in FIG. 6 enables to execute vector detection processing while flexibly switching the results A to D adaptively for each part and a motion vector detected using a block of
20 a size appropriately selected in accordance with the pattern of an input image. As a result, it is possible to detect a motion vector for generating a high-quality interpolation image.

An example of the block sizes in one interpolation
25 screen and the distribution of finally selected vectors is shown in FIG. 6 as the operation result of the invention.

· Reconfirmation processing

As another embodiment, in block 28, if two or more values satisfy

5 $|\text{minimum value} - \text{minimal value } i| < (\text{threshold value}),$

no motion vector can be specified. Instead of immediately interrupting vector detection at this point in time, an opportunity of reconfirming " $|\text{minimum value} - \text{minimal value } i|$ and (threshold value)" in each
10 of blocks S22, S25, and S28 is preferably provided.

That is, the vector detection determination module 24 preferably provides an opportunity of reconfirming " $|\text{minimum value} - \text{minimal value } i|$ and (threshold value)" in each step by calculating the sum again or
15 making comprehensive consideration. For example, the (threshold value) is preferably given some tolerance so that the motion vector detected by, e.g., the medium block detection module 22 can be restored and used.

· Processing order

20 In the above-described embodiment, small block detection processing, medium block detection processing, and large block detection processing are executed in this order. However, the order is not limited to this. A not so largely different detection
25 result may be obtained even when the processing is executed in the order of large block detection processing, medium block detection processing, and

small block detection processing in consideration of the magnitudes of the threshold values of small block detection processing, medium block detection processing, and large block detection processing.

5 More specifically, even when large block detection processing is performed first, not all blocks are detected by the large block if, e.g., the threshold value is large. Which one of small block detection processing, medium block detection processing, and
10 large block detection processing is necessary for a block to detect a motion vector is determined by the threshold value rather than by the order. For this reason, it is preferable to arbitrarily change the order of small block detection processing, medium block
15 detection processing, and large block detection processing.

(Detailed Description of Interpolation Frame Generation Processing Using Flowcharts: FIGS. 8, 9, and 10)

· "Small Block and Medium Block"

20 FIG. 8 is a flowchart for explaining an example of motion vector detection processing using a small block and a medium block in the interpolation frame generation apparatus according to the embodiment of the invention.

25 A case in which motion vectors are detected in the order of "a small block and a medium block" will be explained here. That is, large block detection

processing is not essential. Only small block detection processing and medium block detection processing suffice for equivalent detection processing.

5 All the blocks of the flowchart in FIG. 8 are common to the flowchart in FIG. 7, and a description thereof will not be repeated here.

· "Small Block and Large Block"

10 FIG. 9 is a flowchart for explaining an example of motion vector detection processing using a small block and a large block in the interpolation frame generation apparatus according to the embodiment of the invention.

A case in which motion vectors are detected in the order of "a small block and a large block" will be explained here. That is, medium block (variable
15 length) detection processing is not essential. Only small block detection processing and large block detection processing suffice for equivalent detection processing.

20 All the blocks of the flowchart in FIG. 9 are common to the flowchart in FIG. 7, and a description thereof will not be repeated here.

(Detailed Description of Interpolation Frame Generation Processing Using Flowchart: FIG. 10)

25 FIG. 10 is a flowchart for explaining an example of processing of generating an interpolation frame according to a motion vector amount in the interpolation frame generation apparatus according to

the embodiment of the invention.

Many blocks of the flowchart in FIG. 10 are common to the flowchart in FIG. 7, and a description thereof will not be repeated here.

5 Assume that the vector detection determination module 24 determines in block 30 that detection is interrupted. When the motion vector amount in a block of interest is equal to or larger than a predetermined value (block 41), the possibility of image disturbance
10 is considered to be high. Hence, the interpolation image generation module 13 generates an interpolation frame image which is the same as the preceding frame image or succeeding frame image (block 42). If a
15 composite image of the preceding frame and succeeding frame is used as the interpolation frame image when the motion vector amount is large, the image is disturbed with a high possibility.

 On the other hand, if the motion vector amount in the block is smaller than the predetermined value
20 (block 41), the possibility of image disturbance is considered to be low. Hence, a composite image of the preceding frame and succeeding frame is preferably used as the interpolation frame image (block 43).

(Other Embodiments)

25 Other preferred embodiments will be described below.

 In the above embodiment, a technique of increasing

the motion vector detection accuracy for a periodical pattern in the horizontal direction of an image has been described. As can easily be supposed, the technique is also effective for increasing the motion vector detection accuracy for a periodical pattern in the vertical direction of an image.

More specifically, the motion vector detection module 12 preferably advances the processing of specifying a motion vector sequentially in a plurality of blocks either in the horizontal direction or vertical direction of the plurality of blocks.

In the above embodiment, simple processing using one kind of large block (fixed length) size has been described. However, it is also preferable to use blocks (fixed length) having a plurality of stages of sizes until reaching a small block as the final motion vector detection target.

<Example of Broadcast Receiving Apparatus Using Interpolation Frame Generation Apparatus of Embodiment of Invention>

An example of a broadcast receiving apparatus using the interpolation frame generation apparatus according to the embodiment of the invention will be described next with reference to the accompanying drawing. FIG. 11 is a block diagram showing an example of the arrangement of a broadcast receiving apparatus using the interpolation frame generation apparatus

according to the embodiment of the invention.

In a broadcast receiving apparatus 100, the interpolation frame generation apparatus according to the above-described embodiment of the invention is preferably used as an interpolation frame generation module 10 of the succeeding stage in a video processing module 119.

(Arrangement and Operation of Broadcast Receiving Apparatus)

10 An example of the arrangement of a broadcast receiving apparatus such as a digital television apparatus, which is an embodiment of the broadcast receiving apparatus using the interpolation frame generation apparatus of the embodiment of the invention, will be described below in detail with reference to the accompanying drawing. FIG. 11 is a block diagram showing an example of the arrangement of a broadcast receiving apparatus such as a digital television apparatus, which is an embodiment of the broadcast receiving apparatus using the interpolation frame generation apparatus.

As shown in FIG. 11, the broadcast receiving apparatus 100 is, e.g., a television apparatus. A control module 130 is connected to the modules via data bus to control the overall operation. The broadcast receiving apparatus 100 includes, as main constituent elements, an MPEG decoder module 116 which constitutes

the playback side, and the control module 130 which controls the operation of the apparatus main body. The broadcast receiving apparatus 100 has an input-side selector module 114 and an output-side selector module 120. A BS/CS/terrestrial digital tuner module 112 and a BS/terrestrial analog tuner module 113 are connected to the input-side selector module 114. A LAN or a communication module 111 having a mail function is connected to the data bus.

10 The broadcast receiving apparatus 100 also includes a buffer module 115 which temporarily stores a demodulated signal from the BS/CS/terrestrial digital tuner module 112, a demultiplexer module 117 which demultiplexes a stored packet as a demodulated signal into signals of different types, the MPEG decoder module 116 which executes MPEG decoding processing for video and audio packets supplied from the demultiplexer module 117 and outputs video and audio signals, and an OSD (On Screen Display) superimposition module 134 which generates a video signal to superimpose operation information or the like and superimposes it on a video signal. The broadcast receiving apparatus 100 also has an audio processing module 118 which, e.g., amplifies the audio signal from the MPEG decoder module 116, the video processing module 119 which receives the video signal from the MPEG decoder module 116 and executes desired video processing, the interpolation frame

generation module 10 according to the above-described embodiment of the invention, the OSD superimposition module 134, the selector module 120 to select the output destinations of the audio signal and video
5 signal, a speaker module 121 which outputs audio corresponding to the audio signal from the audio processing module 118, a display module 122 which is connected to the selector module 120 and displays, on a liquid crystal display screen or the like, an image
10 corresponding to the supplied video signal, and an interface module 123 which communicates with an external device.

The broadcast receiving apparatus 100 also includes a storage module 135 which records video
15 information and the like from the BS/CS/terrestrial digital tuner module 112 and the BS/terrestrial analog tuner module 113, as needed, and an electronic program information processing module 136 which acquires electronic program information from a broadcast signal
20 and displays it on the screen. These modules are connected to the control module 130 via the data bus. The broadcast receiving apparatus 100 also has an operation module 132 which is connected to the control module 130 via the data bus and receives a user
25 operation or an operation of a remote controller R, and a display module 133 which displays an operation signal. The remote controller R enables almost the

same operation as the operation module 132 provided on the main body of the broadcast receiving apparatus 100 and can do various kinds of settings such as a tuner operation.

5 In the broadcast receiving apparatus 100 having the above-described arrangement, a broadcast signal is input from a receiving antenna to the BS/CS/terrestrial digital tuner module 112, and a channel is selected. The demultiplexer module 117 demultiplexes the
10 demodulated signal in a packet format for the selected channel into packets of different types. Audio and video packets are decoded by the MPEG decoder module 116 so that audio and video signals are supplied to the audio processing module 118 and the video processing
15 module 119, respectively.

 In the video processing module 119, for example, an IP conversion module 141 executes image processing of the received video signal by, e.g., converting the interlaced signal into a progressive signal.
20 Additionally, the interpolation frame generation module 10 can provide a stable interpolation image by eliminating the influence of a motion vector hard to detect, as described above. The processed signal is supplied to the selector module 120.

25 The selector module 120 supplies the video signal to, e.g., the display module 122 in accordance with a control signal from the control module 130 so that the

display module 122 displays an image corresponding to the video signal. In addition, the speaker module 121 outputs audio corresponding to the audio signal from the audio processing module 118.

5 Various kinds of operation information and subtitle information generated by the OSD superimposition module 134 are superimposed on the video signal corresponding to the broadcast signal. An image corresponding to the video signal is displayed on the display module 122 via the video processing module 119.

As described above, in the broadcast receiving apparatus 100, it is possible to further improve the accuracy of the interpolation frame image of the interpolation frame generation module 10 and display a more natural moving image with a smoother motion without any failure.

The various modules of the systems described herein can be implemented as software applications, hardware and/or software modules, or components on one or more computers, such as servers. While various modules are illustrated separately, they may share some or all of the same underlying logic or code.

While certain embodiments of the inventions have been described, these embodiments have been presented by way of example only, and are not intended to limit the scope of the inventions. Indeed, the novel methods

and systems described herein may be embodied in a variety of other forms; furthermore, various omissions, substitutions and changes in the form of the methods and systems described herein may be made without
5 departing from the spirit of the inventions. The accompanying claims and their equivalents are intended to cover such forms or modifications as would fall within the scope and spirit of the inventions.

C L A I M S

1. An interpolation frame generation apparatus for generating an interpolation frame image to be inserted between continuous frame images, comprising:

5 a motion vector detection module configured to execute block matching processing in each of a plurality of blocks included in the continuous frame images and specify one motion vector on an interpolation frame; and

10 a generation module configured to, for an interpolation block in which the motion vector detection module specifies one motion vector, generate an interpolation frame image based on the motion vector, and for an interpolation block in which the
15 motion vector detection module does not specify one motion vector, generate an interpolation frame image based on the frame image containing no motion vector component.

2. The apparatus of claim 1, wherein the motion
20 vector detection module comprises:

a first detection module configured to divide the frame image into a plurality of first blocks and execute block matching processing in each first block if it is impossible to specify one vector for which a
25 difference between a minimum value and a minimal value of a Sum of Absolute Difference (SAD) value is not more than a predetermined value, determine that no motion

vector is detected, and if it is possible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a motion vector is detected; and

5 a second detection module configured to, for a block in which the first detection module detects no motion vector, divide the frame image into second blocks larger than the first block and execute block matching processing in each second block if it is impossible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that no motion vector in the second block is detected, and if it is possible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a motion vector of the second block is detected and

10 select a motion vector in the first block by referring to the vector value.

3. The apparatus of claim 1, wherein the motion vector detection module comprises:

25 a first detection module configured to divide the frame image into a plurality of first blocks and execute block matching processing in each first block if it is impossible to specify one vector for which a

difference between a minimum value and a minimal value of a Sum of Absolute Difference (SAD) value is not more than a predetermined value, determine that no motion vector is detected, and if it is possible to specify
5 one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a motion vector is detected;

a second detection module configured to, for a
10 block in which the first detection module detects no motion vector, divide the frame image into second blocks larger than the first block and execute block matching processing in each second block if it is impossible to specify one vector for which the
15 difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that no motion vector in the second block is detected, and if it is possible to specify one vector for which the difference between the
20 minimum value and the minimal value of the SAD value is not more than the predetermined value, select a motion vector of the second block; and

a third detection module configured to, for a
25 block in which the second detection module detects no motion vector, divide the frame image into third blocks larger than the second block and execute block matching processing in each third block if it is impossible to

specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that no motion vector in the third block is detected, and if
5 it is possible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, select a motion vector of the third block.

10 4. The apparatus of claim 1, wherein the motion vector detection module comprises:

a first detection module configured to divide the frame image into a plurality of first blocks and execute block matching processing in each first block
15 if it is impossible to specify one vector for which a difference between a minimum value and a minimal value of a Sum of Absolute Difference (SAD) value is not more than a predetermined value, determine that no motion vector is detected, and if it is possible to specify
20 one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a motion vector is detected; and

a second detection module configured to, for a
25 block in which the first detection module detects no motion vector, divide the frame image into second blocks smaller than the first block and execute block

matching processing in each second block if it is impossible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the
5 predetermined value, determine that no motion vector in the second block is detected, and if it is possible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a
10 motion vector of the second block is detected.

5. The apparatus of claim 1, wherein the motion vector detection module comprises:

a first detection module configured to divide the frame image into a plurality of first blocks and
15 execute block matching processing in each first block if it is impossible to specify one vector for which a difference between a minimum value and a minimal value of a Sum of Absolute Difference (SAD) value is not more than a predetermined value, determine that no motion
20 vector is detected, and if it is possible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a motion vector is detected;

25 a second detection module configured to, for a block in which the first detection module detects no motion vector, divide the frame image into second

blocks larger than the first block and execute block matching processing in each second block if it is impossible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that no motion vector in the second block is detected, and if it is possible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a motion vector of the second block is detected and select a motion vector in the first block by referring to the vector value;

 a third detection module configured to, for a block in which the second detection module detects no motion vector, divide the frame image into third blocks larger than the second block and execute block matching processing in each third block if it is impossible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that no motion vector in the third block is detected, and if it is possible to specify one vector for which the difference between the minimum value and the minimal value of the SAD value is not more than the predetermined value, determine that a motion vector of the third block is detected and select a motion vector

in the first block by referring to the vector value;
and

5 a fourth detection module configured to, for a
block in which the first detection module, the second
detection module, and the third detection module detect
no motion vector, reconfirm the SAD value of each of
the first detection module, the second detection
module, and the third detection module and determine
whether it is possible to specify one vector for which
10 the difference between the minimum value and the
minimal value of the SAD value is not more than the
predetermined value.

6. The apparatus of claim 1, wherein for the
block in which the motion vector detection module does
15 not specify one motion vector, the generation module
generates, as the interpolation frame image, one of a
preceding frame image and a succeeding frame image of
the continuous frame images, and a frame image obtained
by compositing the preceding frame image and the
20 succeeding frame image.

7. The apparatus of claim 1, wherein if an amount
of a motion vector in the block in which the motion
vector detection module does not specify one motion
vector is not less than a predetermined value, the
25 generation module generates, as the interpolation frame
image, one of a preceding frame image and a succeeding
frame image of the continuous frame images, and if the

amount of the motion vector in the block in which the motion vector detection module does not specify one motion vector is less than the predetermined value, the generation module generates, as the interpolation frame
5 image, a frame image obtained by compositing the preceding frame image and the succeeding frame image of the continuous frame images.

8. The apparatus of claim 1, wherein the motion vector detection module advances processing of
10 specifying a motion vector sequentially in said plurality of blocks either in a horizontal direction or a vertical direction of said plurality of blocks.

9. An interpolation frame generation method of generating an interpolation frame image to be inserted
15 between continuous frame images, comprising:

executing block matching processing in each of a plurality of blocks included in the continuous frame images and specifying one motion vector on an interpolation frame;

20 for an interpolation block in which one motion vector is specified, generating an interpolation frame image based on the motion vector; and

for an interpolation block in which one motion vector is not specified, generating an interpolation
25 frame image based on the frame image containing no motion vector component.

10. A broadcast receiving apparatus comprising:

a tuner module configured to select a channel of a broadcast signal;

a decoder module configured to decode the broadcast signal from the tuner module and output a video signal;

a motion vector detection module configured to execute block matching processing in each of a plurality of blocks included in continuous frame images contained in the broadcast signal from the decoder module and specify one motion vector on an interpolation frame;

a generation module configured to, for an interpolation block in which the motion vector detection module specifies one motion vector, generate an interpolation frame image based on the motion vector, and for an interpolation block in which the motion vector detection module does not specify one motion vector, generate an interpolation frame image based on the frame image containing no motion vector component and insert the interpolation frame image between the continuous frame images; and

a display module configured to display, on a screen, an image based on the frame images in which the interpolation image is inserted.

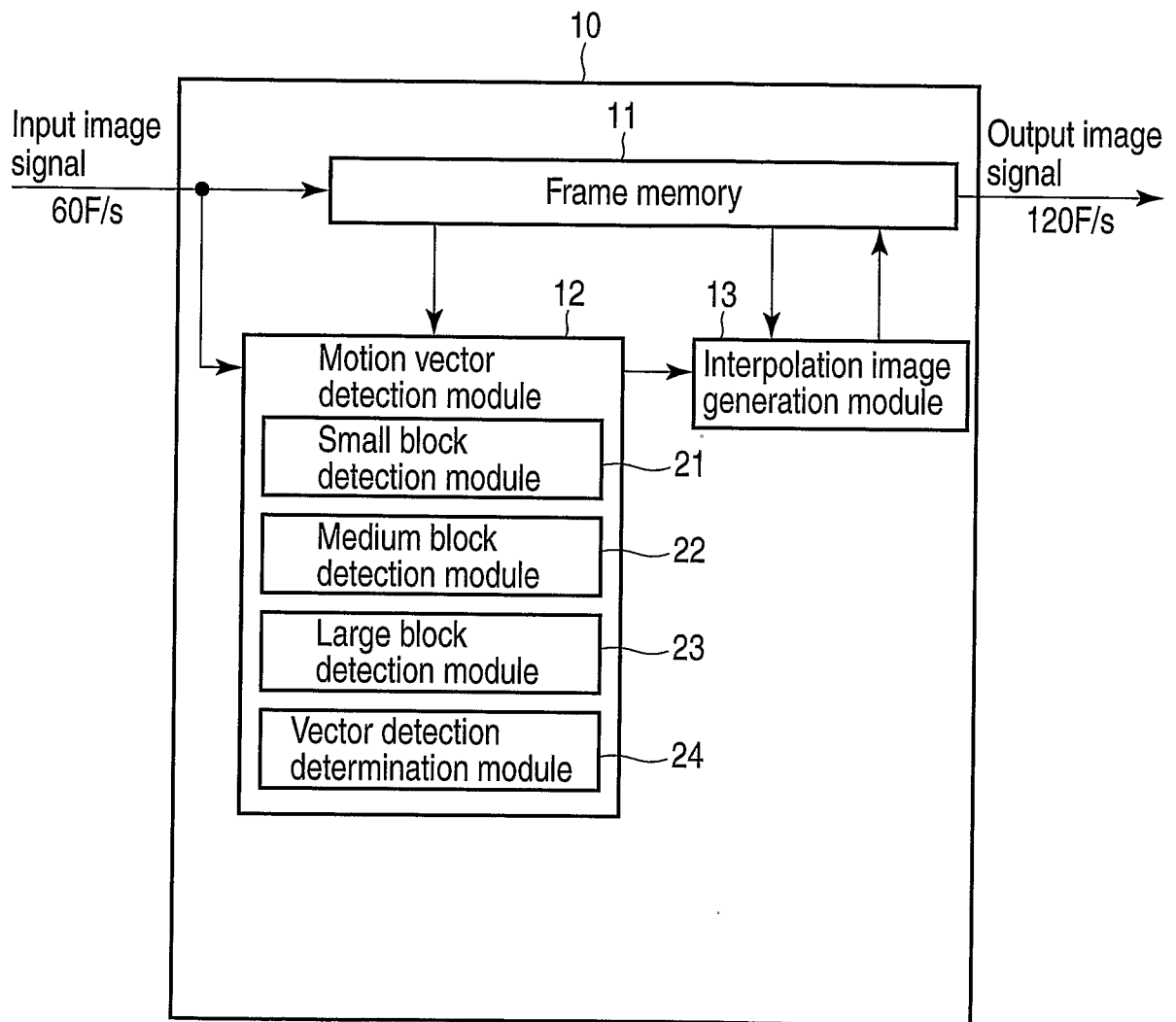


FIG. 1

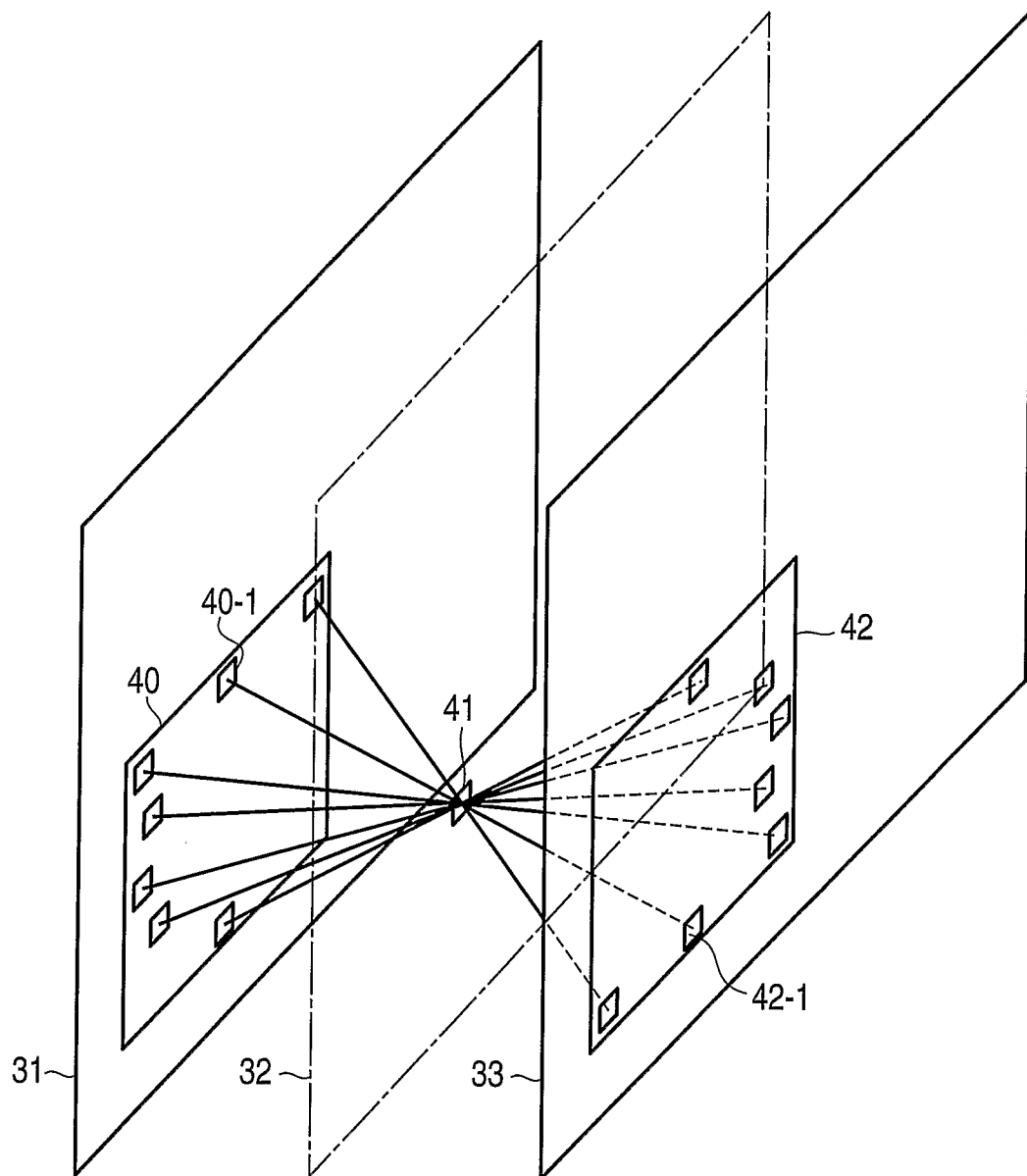


FIG. 2

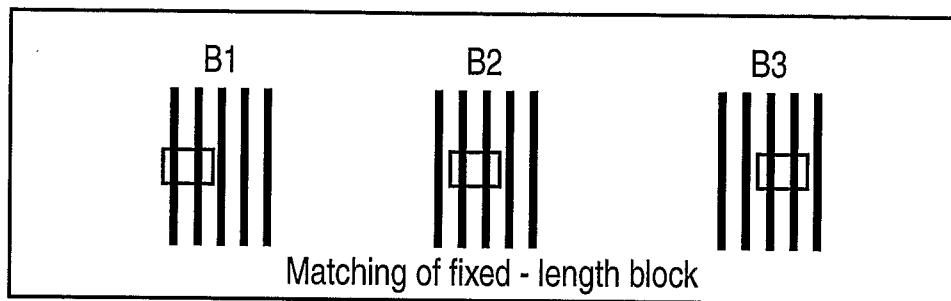


FIG. 3

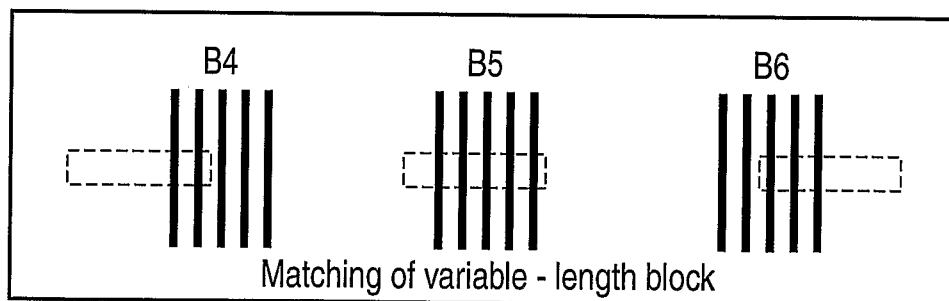


FIG. 4

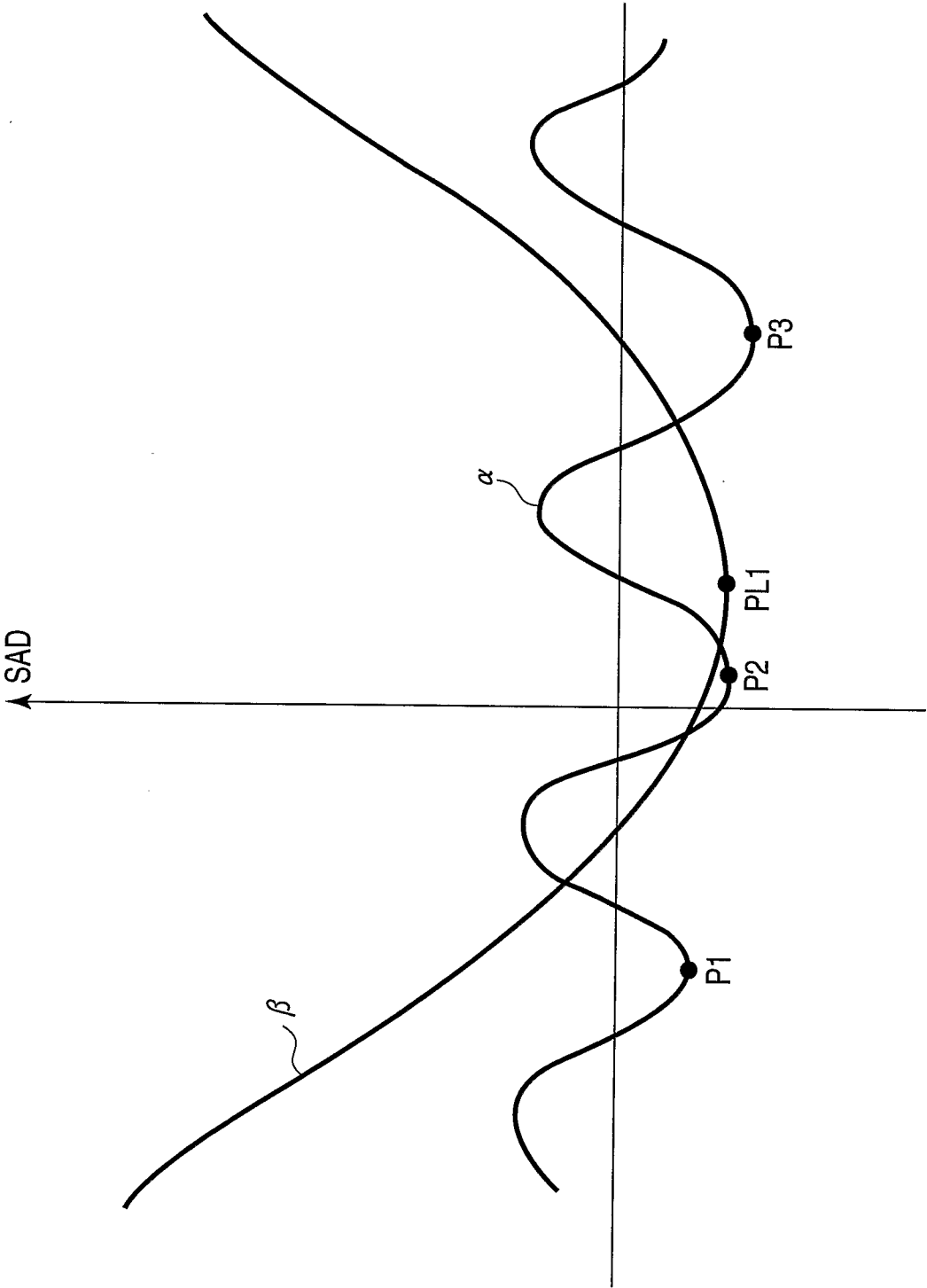


FIG.5

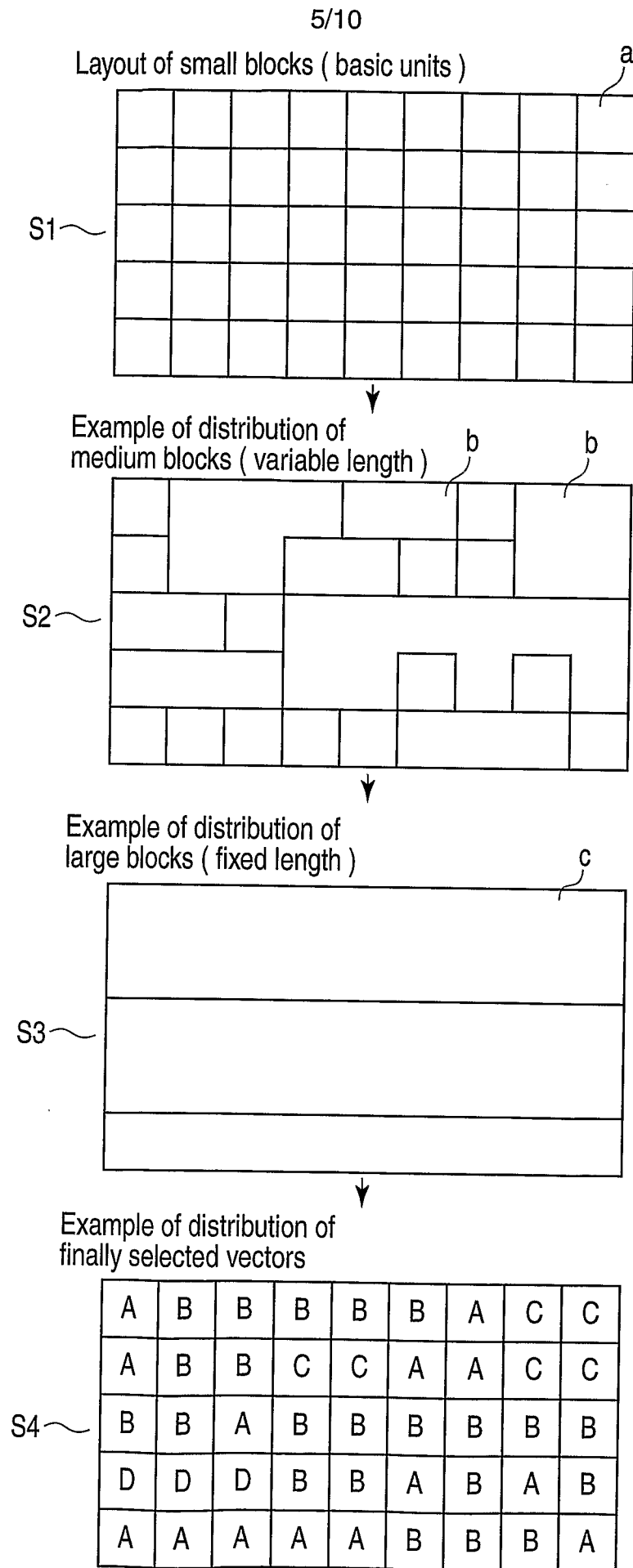
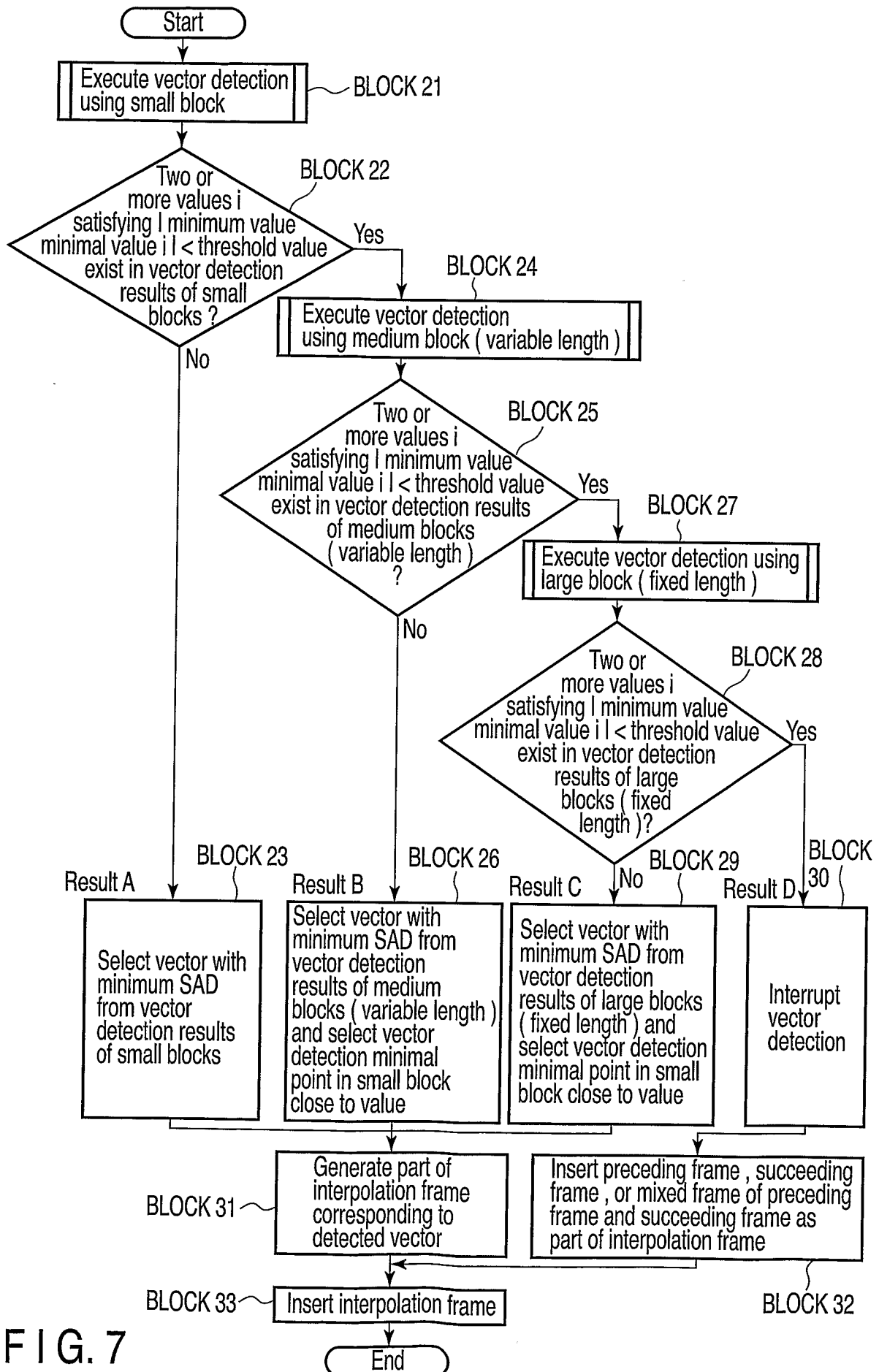


FIG. 6



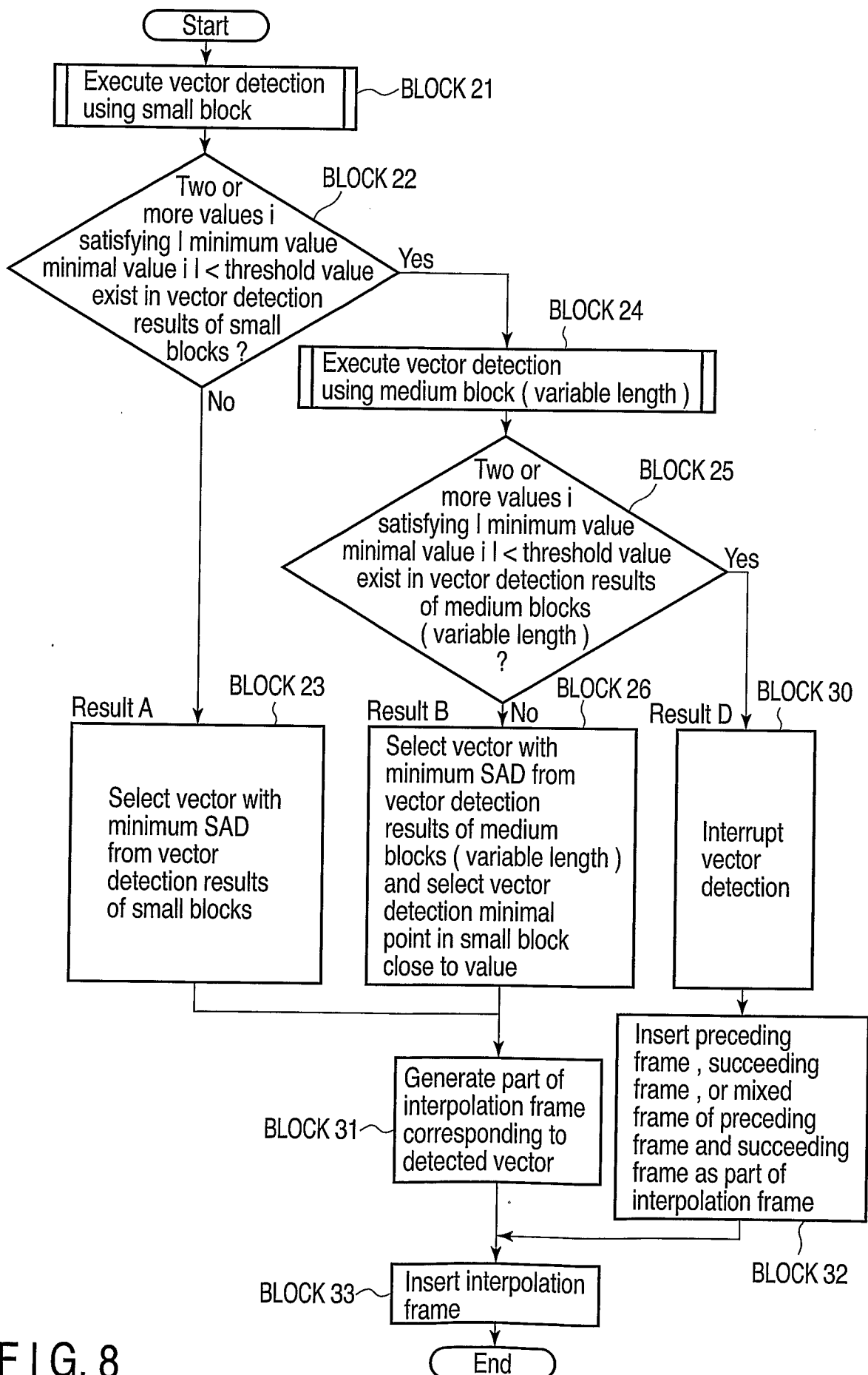


FIG. 8

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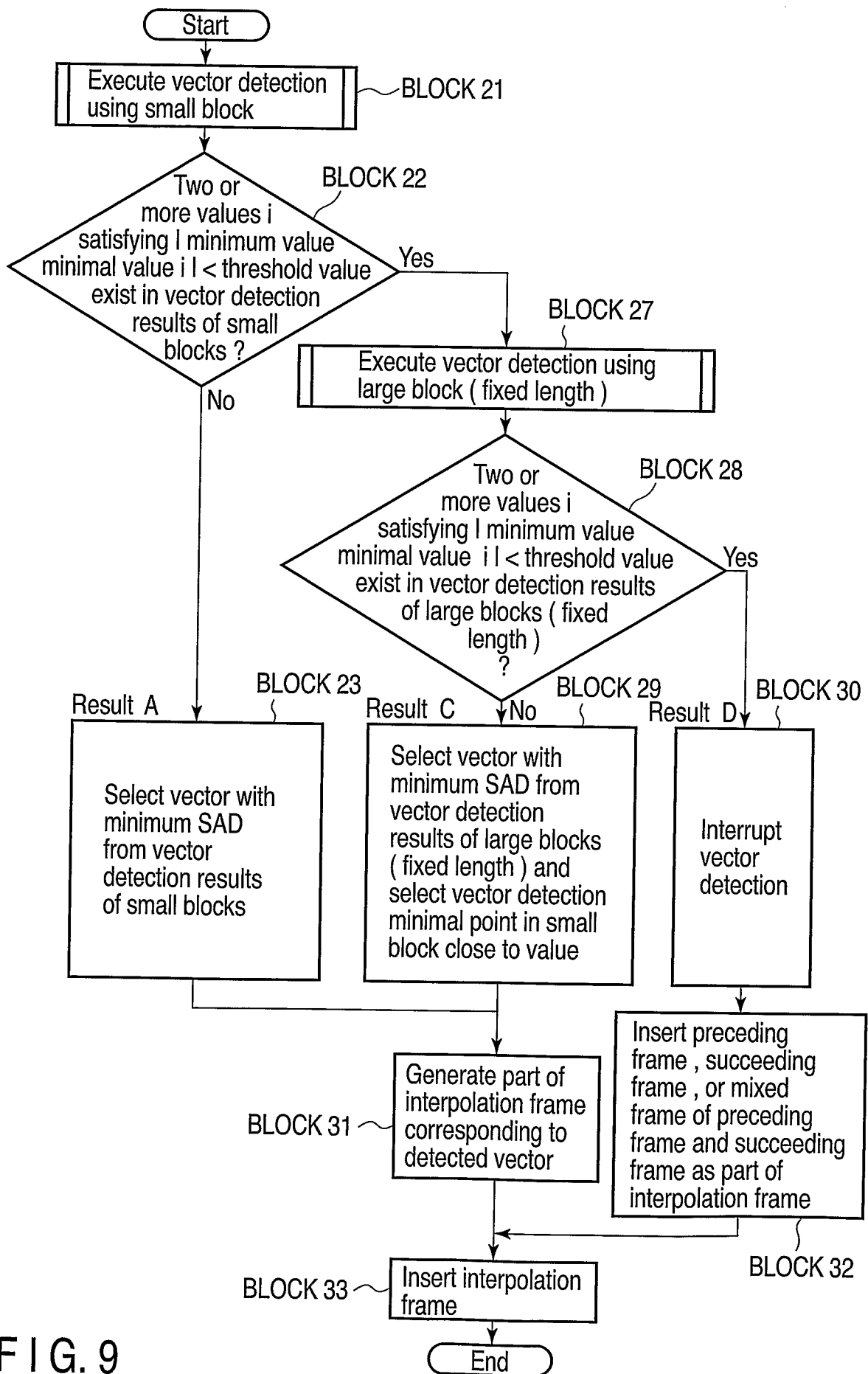


FIG. 9

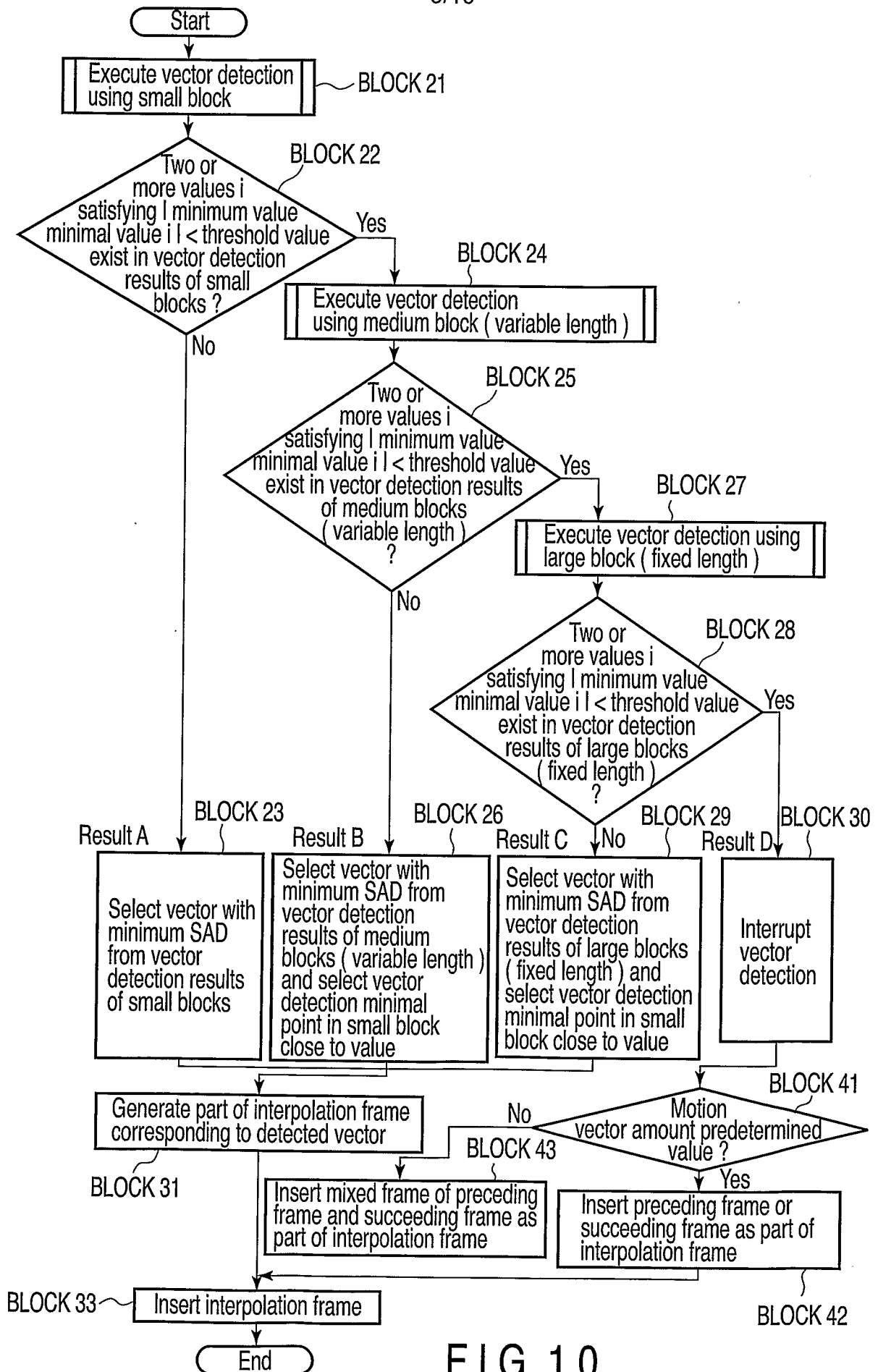


FIG. 10

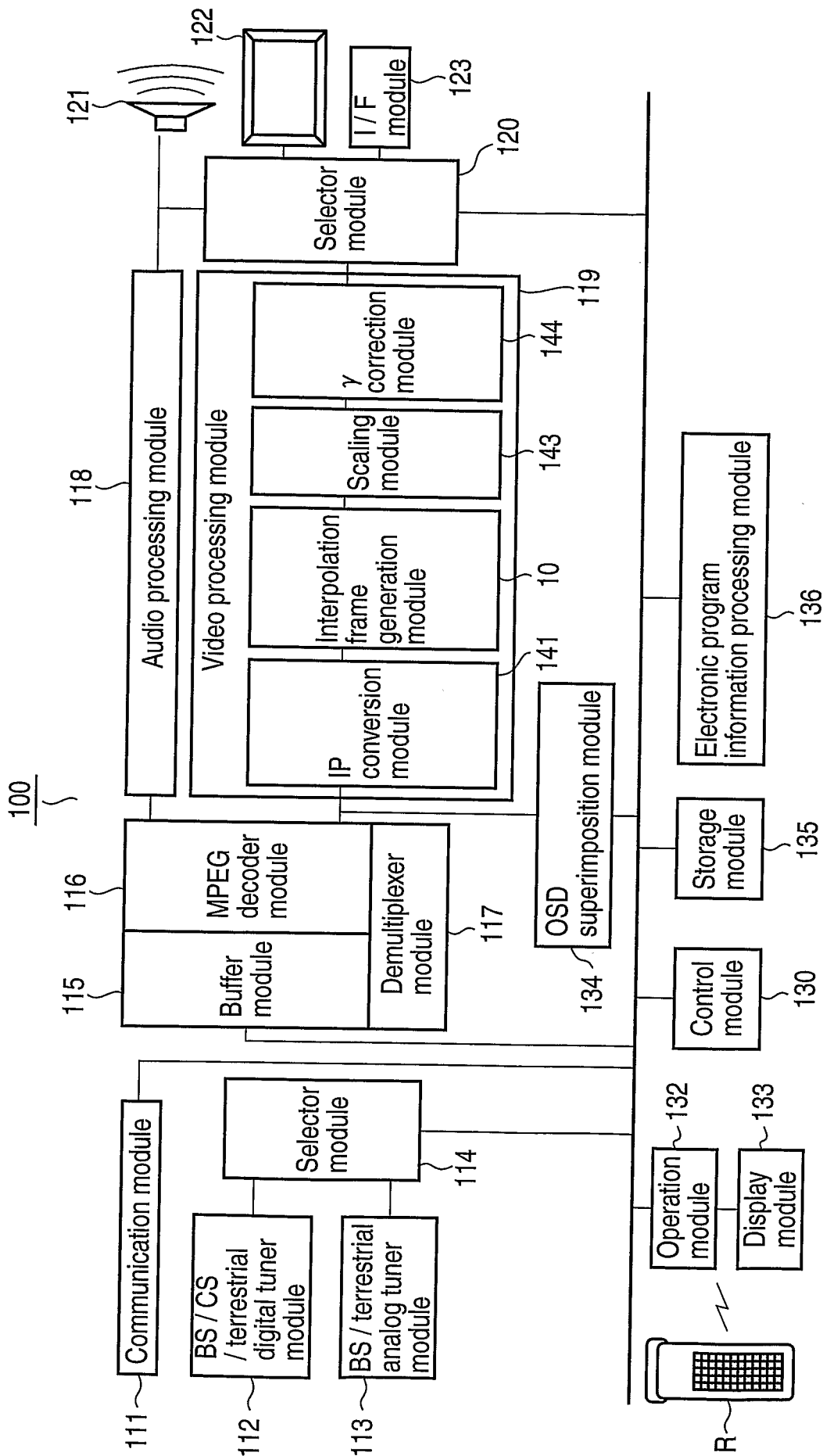


FIG. 11

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2008/070245

A. CLASSIFICATION OF SUBJECT MATTER

Int.Cl. H04N7/01 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int.Cl. H04N7/01

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Published examined utility model applications of Japan 1922-1996
 Published unexamined utility model applications of Japan 1971-2008
 Registered utility model specifications of Japan 1996-2008
 Published registered utility model applications of Japan 1994-2008

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP 2004-343715 A (SAMSUNG ELECTRONICS CO., LTD.) 2004.12.02, paragraphs [0001], [0009]-[0017], [0039]-[0045], figs 1, 3 & US 2004/0227851 A1 & KR 10-2004-0098162 A & CN 1551637 A	1-10
A	JP 7-177501 A (THOMSON CONSUMER ELECTRONICS, S.A.) 1995.07.14, paragraphs [0013], [0018]-[0021], fig 1 & US 5793430 A & EP 647919 A1 & DE 69422876 T & SG 50718 A & CN 1112338 A	1-10
A	JP 6-217263 A (OKI ELECTRIC INDUSTRY CO., LTD.) 1994.08.05, paragraphs [0047]-[0052], fig 1 (No Family)	1-10



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

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"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

22.12.2008

Date of mailing of the international search report

13.01.2009

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2008/070245

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP 2001-24988 A (HITACHI, LTD.) 2001.01.26, paragraphs [0015]-[0070], figs 1-14 (No Family)	1-10
A	JP 1-176178 A (NEC CORPORATION) 1989.07.12, line 4, upper left column, page 3 to line 4, upper left column, page 4, figs 1,4-6 (No Family)	1-10
A	JP 1-179584 A (FUJITSU LIMITED) 1989.07.17, line 19, lower left column, page 4 to line 6, lower left column, page 7, figs 2-9 (No Family)	1-10