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DE FR IT NL

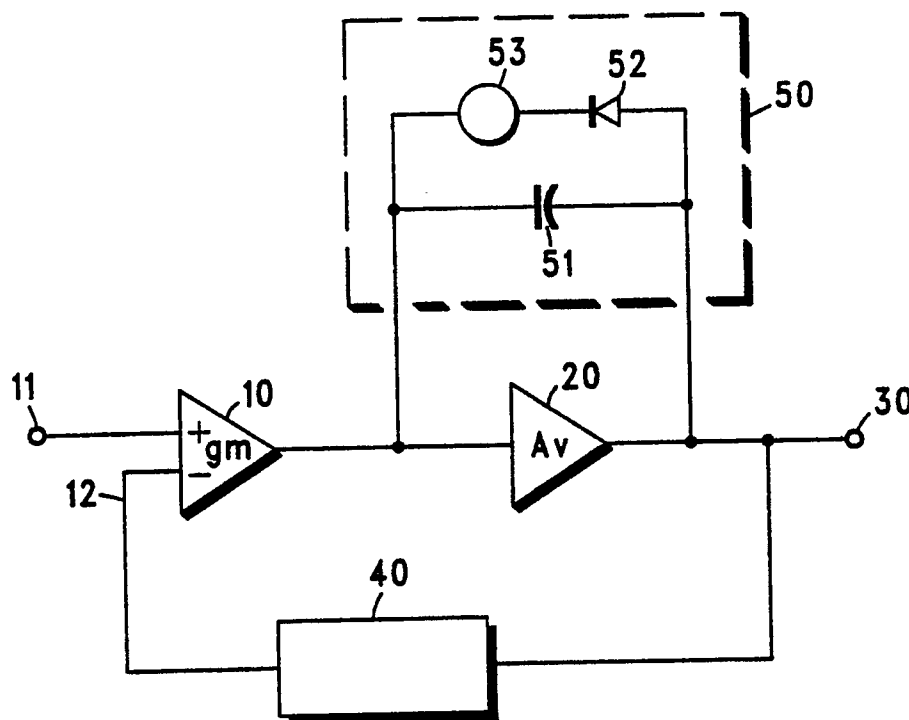
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Amplifier circuit.

An amplifier circuit is described comprising a transconductance amplifier (10) in series with a voltage gain stage (20). A compensation capacitance (51) coupled across the voltage gain stage (20)

includes a capacitance (51) whose value is dependent upon the output voltage of the voltage gain stage (20) so as to optimise amplifier stability over a wide range of output voltages.



EP 0 369 325 A3



DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
Y	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 27, no. 11, April 1985, page 6734, New York, US: "Varicap compensation of output amplifiers" * Whole document *	1	H 03 F 1/08 H 03 F 1/48
Y	US-A-4 415 868 (W.H. GROSS) * Figure 1; column 2, lines 10-27 *	1	
A	US-A-3 729 685 (D.L. LINDER) * Figure 1; column 1, line 52 - column 2, line 7 *	1	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H 03 F
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 04-10-1990	Examiner WALDORFF U.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	