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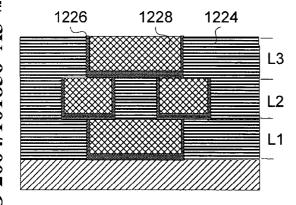
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(54) Title: METHODS FOR ELECTROCHEMICALLY FABRICATING STRUCTURES USING ADHERED MASKS, INCORPORATING DIELECTRIC SHEETS, AND/OR SEED LAYERS THAT ARE PARTIALLY REMOVED VIA PLANARIZATION



(57) Abstract: Embodiments of the present invention provide mesoscale or microscale three-dimensional structures (e.g. components, device, and the like). Embodiments relate to one or more of (1) the formation of such structures which incorporate sheets of dielectric material and/or wherein seed layer material (1226) used to allow electrodeposition (1228) over dielectric material (1224) is removed via planarization operations; (2) the formation of such structures wherein masks used for at least some selective patterning operations are obtained through transfer plating of masking material to a surface of a substrate or previously formed layer, and/or (3) the formation of such structures wherein masks (1224) used for forming at least portions of some layers are patterned on the build surface directly from data representing the mask configuration, e.g. in some embodiments mask patterning is achieved by selectively dispensing material via a computer

controlled inkjet nozzle or array or via a computer controlled extrusion device.



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Category *	Citation of document, with indication, where a		Relevant to claim No.
Y	US 5,190,637 A (GUCKEL) 02 March 1993 (02.03		1-31
Y	4, line 57-column 5, line 43; column 8, line 32-column 9, line 11. US 2002/0115292 A1 (ANDRICACOS et al) 22 August 2002 (22.08.2002), Figure 3C- Figure 3F, paragraph [0055].		
Y	US 6,027,630 A (COHEN) 22 February 2000 (22.02.2000), Figure 11a-Figure 11m, column 13, lines 23-45.		
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