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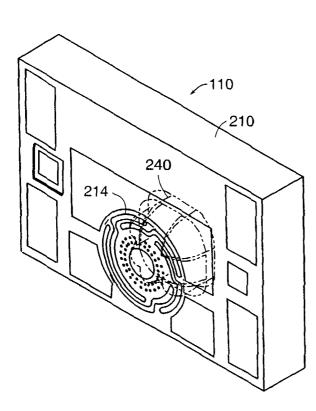
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(54) Title: SILICON ON INSULATOR OPTICAL MEMBRANE STRUCTURE FOR FABRY-PEROT MOEMS FILTER



(57) Abstract: A process for fabricating an optical membrane device comprises providing a handle wafer and then oxidizing a surface of the handle wafer to form an insulating layer. A device wafer is then bonded to the handle wafer. An optical membrane structure is formed in this device wafer. The insulating layer is selectively removed to release the membrane structure. This device wafer can be manufactured from silicon wafer material. Such material typically has a low number of dislocations yielding a stable mechanical membrane structure. The insulating layer defines the electrical cavity across which electrical fields are established that are used to electrostatically deflect the membrane structure. The insulating layer is between 3 and 6 micrometers (μm) in thickness.



WO 01/67157 A3

WO 01/67157 A3



For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

Inter onal Application No PCT/US 01/06853

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 G02B26/00 B81E B81B3/00 G02B26/02 According to International Patent Classification (IPC) or to both national classification and IPC B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC 7 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical, search terms used) WPI Data, EPO-Internal, PAJ, INSPEC C. DOCUMENTS CONSIDERED TO BE RELEVANT Category | Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. EP 0 417 523 A (TEXAS INSTRUMENTS INC) χ 1 - 3.820 March 1991 (1991-03-20) page 7, line 25 - line 38; figures 1A.1B X XIAO Z ET AL: "THE INFLUENCE OF PRESSURE 11 ON THE OPTICAL PROPERTIES OF ARTIFICIAL MICROCAVITIES IN SILICON" SEMICONDUCTOR SCIENCE AND TECHNOLOGY, INSTITUTE OF PHYSICS. LONDON, GB, vol. 12, no. 2, 1 February 1997 (1997-02-01), pages 166-172, XP000641212 ISSN: 0268-1242 page 166, right-hand column, paragraph 2 page 167, left-hand column figure 1 -/--Further documents are listed in the continuation of box C. Patent family members are listed in annex. ° Special categories of cited documents *T* later document published after the international filing date or pnority date and not in conflict with the application but cited to understand the principle or theory underlying the *A* document defining the general state of the art which is not considered to be of particular relevance invention *E* earlier document but published on or after the international *X* document of particular relevance; the claimed invention filing date cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone 'L' document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such docu-"O" document referring to an oral disclosure, use, exhibition or other means ments, such combination being obvious to a person skilled document published prior to the international filing date but later than the priority date claimed *&* document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 18 September 2001 23/10/2001 Name and mailing address of the ISA Authorized officer European Patent Office, P.B. 5818 Patentlaan 2 NL – 2280 HV Rijswijk Tel. (+31–70) 340–2040, Tx. 31 651 epo nl, Ward, S Fax: (+31-70) 340-3016

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