Title: MULTI-BIT-PER-CELL FLASH MEMORY DEVICE WITH AN EXTENDED SET OF COMMANDS

Abstract: A multi-bit-per-cell flash memory device supports a command such that each invocation of the command by the device’s host changes respective values of one or more types of reference voltage (e.g., all read reference voltages and/or all program verify reference voltages) of the device to respective new values.
INTERNATIONAL SEARCH REPORT

A. CLASSIFICATION OF SUBJECT MATTER
IPC: G11C 11/34(2006.01),16/04(2006.01)

USPC: 365/185.03,185.2,185.22
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED
Minimum documentation searched (classification system followed by classification symbols)
U.S.: 365/185.03,185.2,185.22

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
None

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
WEST 2.0, EAST 2.0

C. DOCUMENTS CONSIDERED TO BE RELEVANT

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of document, with indication, where appropriate, of the relevant passages</th>
<th>Relevant to claim No.</th>
</tr>
</thead>
</table>

Further documents are listed in the continuation of Box C. See patent family annex.

Date of the actual completion of the international search
20 May 2008 (20.05.2008)

Name and mailing address of the ISA/US
Mail Stop PCT, Attn: ISA/US Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
Facsimile No. (571) 272-3201

Date of mailing of the international search report
02 JUN 2008

Authorized officer
Viet Q. Nguyen
Telephone No. (703) 308-0956

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