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(54) **METHOD FOR DETERMINING POSITION OF PAGING FRAME, APPARATUS, AND TERMINAL DEVICE**

(57) A method and a device for determining a position of a paging frame, and a terminal device are provided. The method includes: determining a synchronization signal block in a discontinuous reception (DRX) cycle; and

determining the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

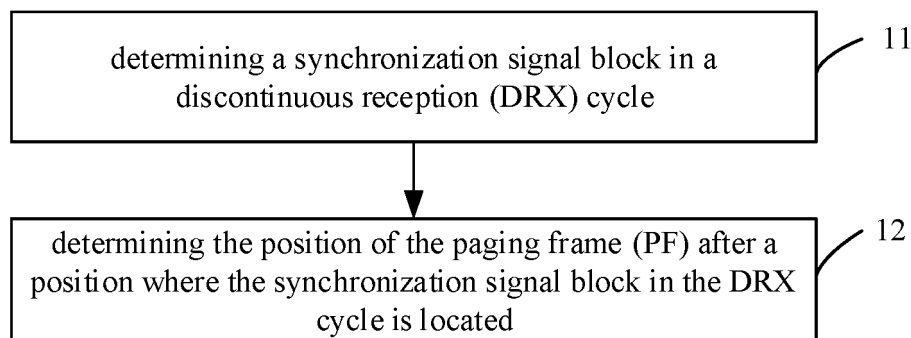


FIG. 1

EP 3 742 819 A1

Description**CROSS-REFERENCE TO RELATED APPLICATION**

- 5 **[0001]** This application claims a priority to Chinese Patent Application No. 201810150975.0 filed on February 13, 2018, the disclosure of which is incorporated in its entirety by reference herein.

TECHNICAL FIELD

- 10 **[0002]** The present disclosure relates to the technical field of communications, in particular to a method and a device for determining a position of a paging frame, and a terminal device.

BACKGROUND

- 15 **[0003]** In the future 5th Generation (5G) mobile communication systems, in order to achieve a downlink transmission rate of 20Gbps and an uplink transmission rate of 10Gbps, high-frequency communications and the large-scale antenna technology will be introduced.

- [0004]** High-frequency communications can provide a wider system bandwidth, and the size of an antenna can also be smaller, which is more conducive for large-scale antennas to be deployed in base stations and user equipment (User Equipment, UE) or terminal equipment.

- 20 **[0005]** High-frequency communications has disadvantages of large pathloss, susceptibility to interference, and a fragile link, while large-scale antenna technology can provide a large antenna gain. Therefore, a combination of high-frequency communications and large-scale antennas is an inevitable trend of the future 5G mobile communication systems.

- 25 **[0006]** In the 5G systems, currently, a periodicity of a synchronization signal block (SSB) may be 5 ms, 10 ms, 20 ms, 40 ms, 80 ms, and 160 ms. In the case of different periodicities of the SSB, if the paging frame calculation method in existing LTE systems continues to be used, some UEs may need to wake up ahead of a relatively long time to wait to receive a paging message, which causes a problem of a waste of electricity.

- [0007]** In the LTE systems, a time-domain position where the paging message is transmitted by a network side or received by a terminal side are determined based on PF and PO:

- 30 **[0008]** Paging Frame (PF): a radio frame used to transmit a paging message, which may include one or more POs; and
Paging Occasion (PO): a paging slot, which may include a sub-frame where a terminal transmits a paging message.

- 35 **[0008]** When DRX (discontinuous reception) is enabled, in order to save power, the terminal only monitors its own PO per DRX cycle, and the frame number is from 0 to 1023.

[0009] UE_ID: IMSI MOD 1024, which is divided into 1024 groups according to IMSI.

[0010] T: a DRX cycle, which takes a value of rf32, rf64, rf128, or rf256, and the parameters are configured by the upper RRC layer.

[0011] i_s: a sub-frame mode, which is obtained by looking up a table.

- 40 **[0012]** nB: 4T, 2T, T, T/2, T/4, T/8, T/16, T/32. nB indicates how many POs are included in each DRX cycle, that is, 4T indicates that each radio frame has 4 sub-frames used for paging, and T/4 indicates that 4 radio frames have one sub-frame used for paging.

[0013] For a paging density, the greater the value is, the greater the paging density is. For example, nB=4T indicates that there are 4 POs in a radio frame. When the paging density is less than 1, it is controlled by N_s.

- 45 **[0014]** N: min(T, nB).

[0015] N_s: max(1, nB/T), which takes a value of 1, 2 or 4; N_s indicates how many POs are included in each radio frame, and the paging density configured as N_s=1 indicates that there is one paging PO in a radio frame, N_s=2 indicates that there are 2 POs in a radio frame, which is described as follows.

[0016] The PO definition rules in frequency division multiplexing (FDD) in the LTE systems are as follows:

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N _s	PO when i _s =0	PO when i _s =1	PO when i _s =2	PO when i _s =3
1	9	N/A	N/A	N/A
2	4	9	N/A	N/A
4	0	4	5	9

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[0017] If P-RNTI is transmitted on MPDCCH, and a system bandwidth is between 1.4 MHz and 3 MHz, and the PO definition rules are as follows:

Ns	PO when i _s =0	PO when i _s =1	PO when i _s =2	PO when i _s =3
1	5	N/A	N/A	N/A
2	5	5	N/A	N/A
4	5	5	5	5

[0018] The PO definition rules in time division multiplexing (TDD) in the LTE systems are as follows:

Ns	PO when i _s =0	PO when i _s =1	PO when i _s =2	PO when i _s =3
1	0	N/A	N/A	N/A
2	0	5	N/A	N/A
4	0	1	5	6

[0019] If P-RNTI is transmitted on MPDCCH, and the system bandwidth is between 1.4 MHz and 3MHz, and the PO definition rules are as follows:

Ns	PO when i _s =0	PO when i _s = 1	PO when i _s =2	PO when i _s =3
1	1	N/A	N/A	N/A
2	1	6	N/A	N/A
4	1	1	6	6

[0020] A relationship between the PF paging cycle and various parameters can be seen from the following PF determination formula, PF has a relationship with DRX cycle, and UE_ID, i.e., IMSI, as follows:

PF: $\text{SFN mod } T = (T \text{ div } N) * (\text{UE_ID mod } N)$.

[0021] It can be seen from the following PO determination formula that the sub-frame pattern of PO is related to the UE_ID of the terminal, that is the IMSI, and the Ns paging density:

PO: $i_s = \text{floor}(\text{UE ID}/N) \text{ mod } N_s$.

[0022] Relationship between other parameters:

nB: 4T, 2T, T, T/2, T/4, T/8, T/16, T/32;

N: min(T, nB);

Ns: max(1, nB/T);

UE_ID: IMSI mod 1024.

[0023] In LTE, a periodicity of the synchronization signal is 5 ms. In order to receive a paging message in an idle mode, the UE wakes up in advance for more than 5 ms to receive a synchronization signal, thereby achieving synchronization.

[0024] However, in NR, when a periodicity of an SSB is relatively large, if the UE wakes up for too short a time, the UE cannot achieve synchronization before receiving a paging message, which may cause a low paging reception success rate. If the UE wakes up in order to achieve synchronization, the UE may wake up for a long time, which results in a waste of UE power consumption.

SUMMARY

[0025] Embodiments of the present disclosure provide a method and a device for determining a position of a paging frame, and a terminal device, so as to solve a problem of a low paging reception success rate in the related technologies.

[0026] In a first aspect, embodiments of the present disclosure provide a method for determining a position of a paging frame, which includes:

determining a synchronization signal block in a discontinuous reception (DRX) cycle; and
determining the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

[0027] In a second aspect, embodiments of the present disclosure further provide a device for determining a position of a paging frame, which includes:

a first determining module, configured to determine a synchronization signal block in a discontinuous reception (DRX) cycle; and

a second determining module, configured to determine the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

[0028] In a third aspect, embodiments of the present disclosure provide a terminal device, which includes a processor configured to: determine a synchronization signal block in a discontinuous reception (DRX) cycle; and determine the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

[0029] In a fourth aspect, embodiments of the present disclosure provide a terminal device. The network device includes a processor, a memory, and a computer program that is stored in the memory and executable on the processor. When executing the computer program, the processor is configured to implement steps of the method for determining the position of the paging frame as described above.

[0030] In a fifth aspect, embodiments of the present disclosure provide a computer-readable storage medium. A computer program is stored on the computer-readable storage medium, and the computer program is executed by a processor to implement steps of the method for determining the position of the paging frame as described above.

[0031] In this way, based on the method for determining the position of the paging frame in the embodiments of the present disclosure, a synchronization signal block in a discontinuous reception (DRX) cycle is first determined; and then the position of the paging frame (PF) is determined after a position where the synchronization signal block in the DRX cycle is located. Thus, the terminal device can determine the position of the PF, thereby improving the paging reception success rate, and reducing power consumption of the terminal.

BRIEF DESCRIPTION OF THE DRAWINGS

[0032] In order to explain technical solutions of embodiments of the present disclosure more clearly, the drawings that need to be used in the description of the embodiments of the present disclosure will be briefly described hereinafter. Obviously, the drawings in the following description are only some embodiments of the present disclosure. Those of ordinary skill in the art can obtain other drawings based on these drawings without creative effort.

FIG. 1 shows a flowchart of a method for determining a position of a paging frame according to an embodiment of the present disclosure;

FIG. 2 shows a schematic diagram of a positional relationship between a synchronization signal block (SSB) and PF;

FIG. 3 shows another flowchart of a method for determining a position of a paging frame according to an embodiment of the present disclosure;

FIG. 4 shows another flowchart of a method for determining a position of a paging frame according to an embodiment of the present disclosure;

FIG. 5 shows a block diagram of modules of a device for determining a position of a paging frame according to an embodiment of the present disclosure; and

FIG. 6 shows a schematic structural diagram of a terminal device according to an embodiment of the present disclosure.

DETAILED DESCRIPTION

[0033] Exemplary embodiments of the present disclosure will be described below in more detail with reference to the accompanying drawings. Although the exemplary embodiments of the present disclosure are shown in the drawings, it should be understood that the present disclosure can be implemented in various forms and should not be limited by the embodiments set forth herein. Rather, these embodiments are provided to enable a more thorough understanding of the present disclosure and to fully convey the scope of the present disclosure to those skilled in the art.

[0034] Such terms as "first", "second" in the specification and claims of the present disclosure are used to distinguish similar objects without having to describe a particular order or sequence. It should be understood that data used in this manner may be interchangeable where appropriate so that the embodiments of the present application described herein can be implemented, for example, in a sequence other than those illustrated or described herein. Furthermore, such

terms as "including" and "having" and any variant thereof are intended to cover non-exclusive inclusion, for example, processes, methods, systems, products or devices that include a series of steps or units are not necessarily limited to those clearly listed, but may include other steps or units that are not clearly listed or inherent to these processes, methods, products or devices.

[0035] As shown in FIG. 1, a method for determining a position of a paging frame is provided according to an embodiment of the present disclosure and includes:

- step 11, determining a synchronization signal block in a discontinuous reception (DRX) cycle; and
- step 12, determining the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

[0036] In the method for determining the position of the paging frame in the embodiments of the present disclosure, a synchronization signal block in a discontinuous reception (DRX) cycle is first determined; and then the position of the paging frame (PF) is determined in a position after a position where the synchronization signal block in the DRX cycle is located. Thus, the terminal device can determine the position of the PF, thereby improving the paging reception success rate, and reducing power consumption of the terminal.

[0037] As shown in FIG. 2, in the above-mentioned embodiment of the present disclosure, the synchronization signal block (SSB) is a first SSB in the DRX cycle; or the SSB is any one or more SSBs of a plurality of SSBs in the DRX cycle.

[0038] As shown in FIG. 3, a method for determining a position of a paging frame is provided according to another embodiment of the present disclosure, and includes:

- step 31, determining one or more synchronization signal blocks (SSBs) in the discontinuous reception (DRX) cycle; and
- step 32, determining a position range of the PF in the DRX cycle, where the position range of the PF is a position range of a radio frame at a first predetermined offset after the first synchronization signal block, and determining the position of the PF within the position range of the PF.

[0039] The first predetermined offset herein is a value configured by a network device, or a value calculated according to a paging parameter configured by a network device. The paging parameter herein may be one or more of: UE_ID, a DRX cycle T, a sub-frame mode i_s , nB of how many POs are included in each DRX cycle, $N = \min(T, nB)$, or $N_s = \max(1, nB/T)$. When performing a specific configuration, the network device may perform configuration by using system information or a radio resource control (RRC) message.

[0040] In an embodiment, when step 32 is specifically implemented, step 32 may include:

- step 321, determining the position of the paging frame (PF) within the position range of the paging frame (PF) according to an order of UE_ID; or
- step 322, calculating a remainder after a terminal identifier divides a first value to obtain a first result, dividing the DRX cycle by the first value to obtain a second result, multiplying the first result by the second result to obtain a first product; determining a frame number of the PF, and determining the position of the PF within the position range of the PF according to an order of the frame number (SFN) of the PF, where a result of the frame number of the PF modulus the DRX cycle is equal to the first product, the first value is the smaller one of the DRX cycle and the second value, and the second value is the number of paging occasions (POs) included in each DRX cycle.

[0041] Specifically, within the position range of the PF, the frame number of the PF is obtained according to a formula: $SFN \bmod T = (T \div N) * (UE_ID \bmod N)$, and the position of the paging frame (PF) is determined according to an order of the frame number SFN of the PF, where T is the DRX cycle, N is $\min(T, nB)$, SFN is the frame number of the PF, UE_ID is the terminal identifier, which is specifically IMSI MOD 1024, div is a division operation, and a value of nB is any one of 4T, 2T, T, T/2, T/4, T/8, T/16, or T/32.

[0042] Optionally, step 32 may include step 323: determining the position of the PF within the position range of the PF, according to a paging parameter configured by the network device. The paging parameter herein may be one or more of: UE_ID, a DRX cycle T, a sub-frame mode i_s , nB of how many POs are included in each DRX cycle, $N = \min(T, nB)$, or $N_s = \max(1, nB/T)$.

[0043] Further, in an embodiment, the method may further include step 33: mapping, according to a mapping relationship, the determined position of the PF to a transmission position after the synchronization signal block, where the mapping relationship herein may be predetermined, or configured by a network, or determined according to a parameter configured by a network.

[0044] The specific mapping may be achieved in the following manners:

a first manner: mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of UE_ID; or

a second manner: mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of a frame number (SFN) of the PF, where the frame number of the PF is determined based on a second product of a third result and a fourth result, the third result is a remainder obtained by dividing the terminal identifier by the third value, and the fourth result is obtained by dividing the DRX cycle by the third value. A result of the frame number of the PF modulus the DRX cycle is equal to the second product, the third value is the smaller one of the DRX cycle and a fourth value, and the fourth value is the number of paging occasions (POs) included in each DRX cycle.

[0045] Specifically, the determined position of the paging frame (PF) is mapped to the transmission position after the synchronization signal block according to the order of the frame number (SFN) of the PF, and the frame number of the PF is determined by a formula: $SFN \bmod T = (T \div N) * (UE_ID \bmod N)$, where T is the DRX cycle, N is $\min(T, nB)$, SFN is the frame number of PF, UE_ID is the terminal identifier, which is specifically IMSI MOD 1024, div is a division operation, and a value of nB is any one of 4T, 2T, T, T/2, T/4, T/8, T/16, or T/32.

[0046] Alternatively, a third manner includes: mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to a paging parameter configured by a network device.

[0047] In the embodiments of the present disclosure, the paging frame (PF) is located in the position range of the radio frame at the first predetermined offset after the first SSB in the DRX cycle. Specifically, the position of the PF may be specifically determined by using the above step 321, or 322, or 323, and the determined position of the PF is mapped to the corresponding transmission position, so that the terminal device can determine the position of the PF, so as to improve the paging success rate.

[0048] As shown in FIG. 4, a method for determining a position of a paging frame is provided according to an embodiment of the present disclosure and includes:

step 41, determining a plurality of synchronization signal blocks (SSBs) in the discontinuous reception (DRX) cycle; and

step 42, determining, in the DRX cycle, a position of a radio frame at a second predetermined offset after a target synchronization signal block in the plurality of synchronization signal blocks is the position range of the paging frame (PF), as the position range of the PF, and determining the position of the PF within the position range of the PF.

[0049] The second predetermined offset is a value configured by the network device, or calculated according to a paging parameter configured by the network device. The paging parameter herein may be one or more of: UE_ID, a DRX cycle T, a sub-frame mode i_s, nB of how many POs are included in each DRX cycle, $N = \min(T, nB)$, or $N_s = \max(1, nB/T)$. When performing a specific configuration, the network device may perform configuration by using system information or a radio resource control (RRC) message.

[0050] In an embodiment, step 42 may specifically include: step 421, determining a position of a target synchronization signal block in the plurality of synchronization signal blocks in the DRX cycle.

[0051] In specific implementation, a result is obtained by calculating a remainder after a terminal identifier divides the number of PFs in the DRX cycle; a ratio of a multiple to the number of PFs in the DRX cycle is calculated, where the multiple is a multiple of the DRX cycle relative to a cycle of the synchronization signal block; and the position of the target synchronization signal block is determined according to a product of the result and the ratio, where a remainder obtained by dividing the position of the target synchronization signal block by the multiple is equal to the product.

[0052] Specifically, the position of the target synchronization signal block in the plurality of synchronization signal

blocks may be determined according to a formula: $X \bmod Y = \frac{Y}{N} * (UE_ID \bmod N)$, where X is the

position of the target synchronization signal block, the DRX cycle is Y times of the cycle of the synchronization signal block, N is the number of PFs in the DRX cycle, and UE_ID is the terminal identifier. N is a value configured by a network device, or a value calculated according to a paging parameter configured by a network device. When performing a specific configuration, the network device may perform configuration by using system information or a radio resource control (RRC) message.

[0053] Step 422 includes determining, as the position range of the paging frame (PF), the position of the radio frame at the second predetermined offset after the target synchronization signal block;

[0054] Step 423 includes determining the position of the paging frame (PF) within the position range of the paging frame (PF). When the step 423 is specifically implemented, it may include:

step 4231, determining the position of the paging frame (PF) within the position range of the paging frame (PF)

according to an order of UE_ID; or

step 4232, calculating a remainder after a terminal identifier divides a first value to obtain a first result, dividing the DRX cycle by the first value to obtain a second result, multiplying the first result by the second result to obtain a first product; determining a frame number of the PF, and determining the position of the PF within the position range of the PF according to an order of the frame number (SFN) of the PF, where a result of the frame number of the PF modulus the DRX cycle is equal to the first product, the first value is the smaller one of the DRX cycle and the second value, and the second value is the number of paging occasions (POs) included in each DRX cycle.

[0055] Specifically, within the position range of the PF, the frame number of the PF is obtained according to a formula: $SFN \bmod T = (T \div N) * (UE_ID \bmod N)$, and the position of the paging frame (PF) is determined according to an order of the frame number SFN of the PF, where T is the DRX cycle, N is $\min(T, nB)$, SFN is the frame number of the PF, UE_ID is the terminal identifier, which is specifically IMSI MOD 1024, div is a division operation, and a value of nB is any one of 4T, 2T, T, T/2, T/4, T/8, T/16, or T/32.

[0056] Optionally, step 423 may include step 4233: determining the position of the PF within the position range of the PF, according to a paging parameter configured by the network device. The paging parameter herein may be one or more of: UE_ID, a DRX cycle T, a sub-frame mode i_s, nB of how many POs are included in each DRX cycle, $N = \min(T, nB)$, or $N_s = \max(1, nB/T)$.

[0057] Further, in an embodiment, the method may further include step 43: mapping the determined position of the PF to a transmission position after the synchronization signal block according to a mapping relationship, where the mapping relationship herein may be predetermined, or configured by a network, or determined according to a parameter configured by a network.

[0058] The specific mapping can be achieved in the following manners:

a first manner: mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of UE_ID; or

a second manner: mapping the determined position of the paging frame the paging frame (PF) to the transmission position after the synchronization signal block according to an order of the frame number (SFN) of the PF, where the frame number of the PF is determined based on a second product of a third result and a fourth result, the third result is a remainder obtained by dividing the terminal identifier by the third value, and the fourth result is obtained by dividing the DRX cycle by the third value. A result of the frame number of the PF modulus the DRX cycle is equal to the second product, the third value is the smaller one of the DRX cycle and a fourth value, and the fourth value is the number of paging occasions (POs) included in each DRX cycle.

[0059] Specifically, the determined position of the paging frame (PF) is mapped to the transmission position after the synchronization signal block according to an order of the frame number SFN of the PF, and the frame number of the PF is obtained according to a formula: $SFN \bmod T = (T \div N) * (UE_ID \bmod N)$, and the position of the paging frame (PF) is determined according to an order of the frame number SFN of the PF, where T is the DRX cycle, N is $\min(T, nB)$, SFN is the frame number of the PF, UE_ID is the terminal identifier, which is specifically IMSI MOD 1024, div is a division operation, and a value of nB is any one of 4T, 2T, T, T/2, T/4, T/8, T/16, or T/32.

[0060] Alternatively, a third manner includes: mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to a paging parameter configured by the network device.

[0061] In the embodiments of the present disclosure, the paging frame (PF) is located in the position range of the radio frame at the second predetermined offset after any target SSB of the plurality of SSBs in the DRX cycle. Specifically, the position of the PF may be specifically determined by using the above step 4231, or 4232, or 4233, and the determined position of the PF is mapped to the corresponding transmission position, so that the terminal device can determine the position of the PF, thereby improving the paging success rate, and reducing the power consumption of the terminal.

[0062] The above-mentioned embodiments of the present disclosure can be applied in 5G, or eLTE, or other scenarios where a position of a similar message is determined.

[0063] As shown in FIG. 5, an embodiment of the present disclosure further provides a device 50 for determining a position of a paging frame, including: a first determining module 51, configured to determine a synchronization signal block in a discontinuous reception (DRX) cycle; and a second determining module 52, configured to determine the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

[0064] The synchronization signal block is a first synchronization signal block in the DRX cycle; or the synchronization signal block is any one or more synchronization signal blocks among a plurality of synchronization signal blocks in the DRX cycle.

[0065] In an embodiment of the present disclosure, the second determining module 52 is specifically configured to determine a position range of the paging frame (PF), where the position range of the paging frame (PF) is a position range of a radio frame at a first predetermined offset after the first synchronization signal block; and determine the

position of the paging frame (PF) within the position range of the paging frame (PF).

[0066] The first predetermined offset is a value configured by a network device, or a value calculated according to a paging parameter configured by a network device.

[0067] In an embodiment of the present disclosure, the second determining module 52 may also be specifically configured to: determine a position of a target synchronization signal block in the plurality of synchronization signal blocks; determine, as the position range of the paging frame (PF), a position of a radio frame at a second predetermined offset after the target synchronization signal block; and determine the position of the paging frame (PF) within the position range of the paging frame (PF).

[0068] When determining the position of the target synchronization signal block in the plurality of synchronization signal blocks, the second determining module 52 is specifically configured to: calculate a remainder after a terminal identifier divides the number of PFs in the DRX cycle to obtain a result; calculate a ratio of a multiple to the number of PFs in the DRX cycle, where the multiple is a multiple of the DRX cycle relative to the cycle of the synchronization signal block; and determine the position of the target synchronization signal block according to the product of the result and the ratio, where a remainder obtained by dividing the position of the target synchronization signal block by the multiple is equal to the product.

[0069] Specifically, the position of the target synchronization signal block in the plurality of synchronization signal blocks may be determined according to a formula: $X \bmod Y = \frac{Y}{N} * (UE_ID \bmod N)$, where X is the po-

sition of the target synchronization signal block, the DRX cycle is Y times of a cycle of the synchronization signal block, N is the number of PFs in the DRX cycle, and UE_ID is the terminal identifier. N is a value configured by a network device, or a value calculated according to a paging parameter configured by a network device.

[0070] In an embodiment of the present disclosure, when determining the position of the paging frame (PF) within the position range of the paging frame (PF), the second determining module 52 is specifically configured to: determine the position of the paging frame (PF) within the position range of the paging frame (PF) according to an order of UE_ID; or calculate a remainder after the terminal identifier divides a first value to obtain a first result, divide the DRX cycle by the first value to obtain a second result, multiply the first result by the second result to obtain a first product, determine a frame number of the PF, and determine the position of the PF within the position range of the PF according to an order of the frame number (SFN) of the PF, where a result of the frame number of the PF modulus the DRX cycle is equal to the first product, the first value is the smaller one of the DRX cycle and the second value, and the second value is the number of paging occasions (POs) included in each DRX cycle, where, specifically, within the position range of the PF, the frame number of the PF is obtained according to a formula: $SFN \bmod T = (T \div N) * (UE_ID \bmod N)$, and the position of the paging frame (PF) is determined according to an order of the frame number SFN of the PF, where T is the DRX cycle, N is $\min(T, nB)$, SFN is the frame number of the PF, UE_ID is the terminal identifier, which is specifically IMSI MOD 1024, div is a division operation, and a value of nB is any one of 4T, 2T, T, T/2, T/4, T/8, T/16, or T/32; or determine the position of the PF within the position range of the PF, according to a paging parameter configured by a network device.

[0071] In a specific embodiment of the present disclosure, the device for determining the position of the paging frame may further include a mapping module, configured to map, according to a mapping relationship, the determined position of the paging frame (PF) to a transmission position after the synchronization signal block. The mapping relationship herein may be predetermined, or configured by a network, or determined according to a parameter configured by a network.

[0072] The mapping module is specifically configured to: map the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of UE_ID; or map the determined position of the paging frame the paging frame (PF) to the transmission position after the synchronization signal block according to an order of the frame number (SFN) of the PF, where the frame number of the PF is determined based on a second product of a third result and a fourth result, the third result is a remainder obtained by dividing the terminal identifier by the third value, and the fourth result is obtained by dividing the DRX cycle by the third value. A result of the frame number of the PF modulus the DRX cycle is equal to the second product, the third value is the smaller one of the DRX cycle and a fourth value, and the fourth value is the number of paging occasions (POs) included in each DRX cycle, where, specifically, the determined position of the paging frame (PF) is mapped to the transmission position after the synchronization signal block according to an order of the frame number SFN of the PF, and the frame number of the PF is obtained according to a formula: $SFN \bmod T = (T \div N) * (UE_ID \bmod N)$, and the position of the paging frame (PF) is determined according to an order of the frame number SFN of the PF, where T is the DRX cycle, N is $\min(T, nB)$, SFN is the frame number of the PF, UE_ID is the terminal identifier, which is specifically IMSI MOD 1024, div is a division operation, and a value of nB is any one of 4T, 2T, T, T/2, T/4, T/8, T/16, or T/32; or map the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to a paging parameter configured by a network device.

[0073] It is worth noting that the device for determining the position of the paging frame in these embodiments of the present disclosure adopts the same implementation means as the above method. All the implementation manners in the above method are applicable to the embodiments of the device, and the same technical effects can be achieved.

[0074] It should be noted that division of the above modules should be understood as only a logical division, which may be integrated into a physical entity or physically separated from each other in practice. All these modules may be implemented in the form of software invoked by a processing element, or in the form of hardware; or some modules may be implemented in the form of software invoked by a processing element, while some other modules may be implemented in the form of hardware. For example, the determining module may be a separately arranged processing element, or may be integrated into a chip of the above device, or may be stored in a memory of the above device in the form of program code, where functions of the determining module are invoked and executed by a processing element of the above device. An implementation of other module is similar to that of the determining module. In addition, all or part of these modules can be integrated together or implemented independently. The processing element described herein may be an integrated circuit with signal processing capability. During implementations, various steps in the above-mentioned method or various modules may be completed by the integrated logic circuit in the form of hardware in the processing element or by instructions in the form of software.

[0075] For example, the above modules may be one or more integrated circuits configured to implement the above methods, such as one or more application specific integrated circuits (Application Specific Integrated Circuits, ASICs), one or more digital signal processors (Digital Signal Processors, DSPs), or one or more field programmable gate arrays (Field Programmable Gate Arrays, FPGAs). For another example, when one of the above modules is implemented in the form of a processing element invoking codes, the processing element may be a general purpose processor, such as a central processing unit (Central Processing Unit, CPU) or another processor that can invoke program codes. For another example, these modules may be integrated together in the form of system-on-a-chip (System-On-a-Chip, SOC).

[0076] In order to better achieve the above objectives, a terminal device 50 is further provided according to an embodiment of the present disclosure. The terminal device 50 includes a processor configured to: determine a synchronization signal block in a discontinuous reception (DRX) cycle; and determine the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

[0077] FIG. 6 is a schematic diagram of a hardware structure of a terminal device according to various embodiments of the present disclosure. The terminal device 60 includes but is not limited to: a radio frequency unit 61, a network module 62, an audio output unit 63, an input unit 64, a sensor 65, a display unit 66, a user input unit 67, an interface unit 68, a memory 69, a processor 610, a power supply 611, and other components.

[0078] Those skilled in the art may understand that the terminal structure shown in FIG. 6 does not constitute a limitation on the terminal, and the terminal may include more or fewer components than these as shown in FIG. 6, or may combine certain components, or may have different component arrangements. In the embodiments of the present disclosure, the terminal includes but is not limited to a mobile phone, a tablet computer, a notebook computer, a palmtop computer, an in-vehicle terminal, a wearable device, and a pedometer.

[0079] The processor 610 is configured to determine a synchronization signal block in a discontinuous reception (DRX) cycle; and determine the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

[0080] The terminal device according to the embodiments of the present disclosure determines a synchronization signal block in a discontinuous reception (DRX) cycle, and then determines the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located, so that the terminal device can determine the position of the PF, thereby improving the paging reception success rate, and reducing power consumption of the terminal.

[0081] It should be understood that, in the embodiments of the present disclosure, the radio frequency unit 61 may be configured to receive and send information, or to receive and send signals in a call. Specifically, the radio frequency unit 50 delivers the downlink information received from a base station to the processor 610. Generally, the radio frequency unit 61 includes, but is not limited to, an antenna, at least one amplifier, a transceiver, a coupler, a low noise amplifier, a duplexer, or the like. In addition, the radio frequency unit 61 may communicate with a network or other devices via a wireless communication system.

[0082] The terminal provides users with wireless broadband Internet access via the network module 62, such as helping users send and receive emails, browse web pages and access streaming media.

[0083] The audio output unit 63 may convert audio data received by the radio frequency unit 61 or the network module 62 or stored in the memory 69 into audio signals and output them as sound. Moreover, the audio output unit 63 may provide audio output (for example, call signal reception sound, message reception sound, etc.) related to a specific function performed by the terminal device 60. The audio output unit 63 includes a speaker, a buzzer, a receiver, or the like.

[0084] The input unit 64 is configured to receive audio or video signals. The input unit 64 may include a graphics processing unit (Graphics Processing Unit, GPU) 641 and a microphone 642. The graphics processor 641 is configured to process image data of still pictures or videos obtained via an image capture device (such as a camera) in an image

capture mode or a video capture mode. The processed image frames may be displayed on the display unit 66. The image frames processed by the graphics processor 641 may be stored in the memory 69 (or other storage medium) or transmitted via the radio frequency unit 61 or the network module 62. The microphone 642 may receive sound, and may process the sound into audio data. The processed audio data may be converted into a format that can be transmitted

5 to a mobile communication base station for outputting via the radio frequency unit 61 in the case of a telephone call mode. **[0085]** The terminal device 60 further includes at least one sensor 65, such as an optical sensor, a motion sensor, or other sensors. Specifically, the optical sensor includes an ambient light sensor and a proximity sensor. The ambient light sensor may adjust brightness of the display panel 661 according to the brightness of the ambient light, and the proximity sensor may close the display panel 661 and/or backlight when the terminal device 60 approaches to the ear.

10 As a type of motion sensor, an accelerometer sensor may detect the magnitude of acceleration in various directions (usually three-axis directions), and detect the magnitude and direction of gravity when in the stationary state. The accelerometer sensor may be applied to identify the pose of the terminal (such as switching of horizontal and vertical screen, a correlated game, magnetometer pose calibration), a function about vibration recognition (such as pedometer, tapping). The sensor 65 may also include a fingerprint sensor, a pressure sensor, an iris sensor, a molecular sensor, a gyroscope, a barometer, a hygrometer, a thermometer, an infrared sensors, or the like, which are not described herein.

15 **[0086]** The display unit 66 is configured to display information input by the user or information provided for the user. The display unit 66 may include a display panel 661. The display panel 661 may be configured in the form of a liquid crystal display (Liquid Crystal Display, LCD), an organic light-emitting diode (Organic Light-Emitting Diode, OLED), or the like.

20 **[0087]** The user input unit 67 may be configured to receive inputted numeric or character information, and generate key signal inputs related to user settings and function control of the terminal. Specifically, the user input unit 67 includes a touch panel 671 and other input device 672. The touch panel 671, also known as a touch screen, may collect a touch operation of a user thereon or thereby (for example, an operation on or around the touch panel 671 that is made by a user with a finger, a touch pen or any other suitable object or accessory). The touch panel 671 may include two parts: 25 a touch detection device and a touch controller. The touch detection device detects touch orientation of the user, detects a signal caused by the touch operation, and transmits the signal to the touch controller. The touch controller receives touch information from the touch detection device, converts the touch information into contact coordinates, and transmits the contact coordinates to the processor 610. The touch controller may receive a command from by the processor 610 and executes the command. In addition, the touch panel 671 may be implemented by various types such as a resistive panel, a capacitive panel, an infrared panel, or a surface acoustic wave panel. In addition to the touch panel 671, the user input unit 67 may include other input device 672. Specifically, the other input device 672 may include, but is not limited to, a physical keyboard, a function key (such as a volume control key, a switch key), a trackball, a mouse, or a joystick, which are not described herein.

30 **[0088]** Further, the display panel 661 may be covered by the touch panel 671. When the touch panel 671 detects a touch operation on or near the touch panel 671, the touch panel 671 transmits the touch operation to the processor 610 to determine the type of the touch event, and the processor 610 provides a corresponding visual output on the display panel 661 according to the type of touch event. Although the touch panel 671 and the display panel 661 are implemented as two independent components to implement the input and output functions of the terminal in FIG. 6, in some embodiments, the touch panel 671 and the display panel 661 may be integrated to implement the input and output functions 35 of the terminal, which are not limited herein.

40 **[0089]** The interface unit 68 is an interface through which an external device is connected to the terminal device 60. For example, the external device may include a wired or wireless headset port, an external power (or battery charger) port, a wired or wireless data port, a memory card port, a port for connecting a device with an identity module, an audio input/output (I/O) port, a video I/O port, a headphone port, etc. The interface unit 68 may be configured to receive input 45 (e.g., data information, power, etc.) from an external device and transmit the received input to one or more elements in the terminal device 60 or may be configured to transmit data between the terminal device 60 and the external device.

[0090] The memory 69 may be configured to store software programs and various data. The memory 69 may mainly include a program storage area and a data storage area, where the program storage area may store an operating system, an application program (such as a sound playback function, an image playback function, etc.) required for at least one 50 function; the data storage area may store data (such as audio data, a phone book, etc.) created according to the use of the mobile phone. In addition, the memory 69 may include a high-speed random access memory, and may further include a non-volatile memory, such as at least one magnetic disk storage device, a flash memory device, or other volatile solid-state storage device.

55 **[0091]** The processor 610 is a control center of the terminal, which uses various interfaces and lines to connect various parts of the entire terminal. The processor 610 runs or executes software programs and/or modules stored in the memory 69 and calls data stored in the memory 69, to execute various functions of the terminal and process data, so as to monitor the terminal as a whole. The processor 610 may include one or more processing units. Optionally, the processor 610 may integrate an application processor and a modem processor. The application processor mainly processes an operating

system, a user interface, an application program, etc., and the modem processor mainly processes wireless communications. It can be understood that the foregoing modem processor may not be integrated into the processor 610.

[0092] The terminal device 60 may further include a power supply 611 (such as a battery) for supplying power to various components. Optionally, the power supply 611 may be logically connected to the processor 610 through a power management system, so as to implement functions, such as management of charging and discharging, and power consumption management, via the power management system.

[0093] In addition, the terminal device 60 may include some functional modules that are not shown, which are not described herein again.

[0094] Optionally, embodiments of the present disclosure further provide a terminal device, including a processor 610, a memory 69, and a computer program stored on the memory 69 and executable by the processor 610. When executing the computer program, the processor 610 is configured to implement various processes of the above embodiments about the method for determining the position of the paging frame, and the same technical effect can be achieved. To avoid repetition, they will not be repeated herein. The terminal may be a wireless terminal or a wired terminal. The wireless terminal may be a device that provides data connectivity of voices and/or other services to a user, a handheld device with a wireless communication function, or other processing devices connected to a wireless modem. A wireless terminal may communicate with one or more core networks over a radio access network (Radio Access Network, RAN). The wireless terminal may be a mobile terminal such as a mobile phone (also referred to as a "cellular" phone) and a computer with a mobile terminal, which, for example, may be a portable, pocket-size, handheld, computer-built or vehicle-mounted mobile device. The wireless terminal exchanges language and/or data with a wireless access network, and may include a personal communication service (Personal Communication Service, PCS) phone, a cordless phone, a session initiation protocol (Session Initiation Protocol, SIP) phone, a wireless local loop (Wireless Local Loop, WLL) station, a personal digital assistant (Personal Digital Assistant, PDA), and other devices. The wireless terminal may also be: a system, a subscriber unit, a subscriber station, a mobile station, a remote station, a remote terminal, an access terminal, a user terminal, a user agent, a user device or a user equipment, which is not limited herein.

[0095] Embodiments of the present disclosure further provide a computer-readable storage medium. A computer program is stored on the computer-readable storage medium. The computer program is executed by a processor to implement various processes of the above embodiments about the method for determining the position of the paging frame, and the same technical effect can be achieved. To avoid repetition, they will not be repeated herein. The computer-readable storage medium may be, for example, a read-only memory (ROM for short), a random access memory (RAM for short), a magnetic disk or an optical disk, etc.

[0096] A person skilled in the art may be aware that, the exemplary units and algorithm steps described in connection with the embodiments disclosed in this specification may be implemented by electronic hardware or a combination of computer software and electronic hardware. Whether the functions are performed by hardware or software depends on particular applications and design constraint conditions of the technical solutions. A person skilled in the art may use different methods to implement the described functions for each particular application, but the implementation should not be considered as going beyond the scope of the present disclosure.

[0097] It may be clearly understood by a person skilled in the art that, for ease of description and conciseness, for a detailed working process of the foregoing system, device, and unit, reference may be made to a corresponding process in the foregoing method embodiments, and details are not described herein again.

[0098] In the several embodiments provided in the present application, it should be understood that the disclosed device and method may be implemented in other manners. For example, the described device embodiment is merely exemplary. For example, the unit division is merely logical function division and may be other division in actual implementation. For example, a plurality of units or components may be combined or integrated into another system, or some features may be neglected or not performed. In addition, the displayed or discussed mutual couplings or direct couplings or communication connections may be implemented through some interfaces. The indirect couplings or communication connections between the devices or units may be implemented in electric, mechanical, or other forms.

[0099] The units described as separate parts may or may not be physically separate, and parts displayed as units may or may not be physical units, that is, may be located in one position, or may be distributed on a plurality of network units. A part or all of the units may be selected according to actual needs to achieve the objectives of the solutions of the embodiments.

[0100] In addition, functional units in the embodiments of the present disclosure may be integrated into one processing unit, or each of the units may exist alone physically, or two or more units may be integrated into one unit.

[0101] If the functions are implemented in a form of a software functional unit and sold or used as an independent product, the functions may be stored in a computer-readable storage medium. Based on such an understanding, essential part or the part contributing to the prior art of the technical solutions of the present disclosure, or a part of the technical solutions may be implemented in a form of a software product. The software product is stored in a storage medium, and includes several instructions for instructing a computer device (which may be a personal computer, a server, or a network device) to perform all or a part of the steps of the methods described in the embodiments of the present disclosure. The

foregoing storage medium includes any medium that may store program code, such as a universal serial bus (USB) flash drive, a mobile hard disk, an ROM, an RAM, a magnetic disk, or an optical disc.

[0102] Further, it is noted, apparently, the components or steps in the apparatus and method of the present disclosure may be subdivided and/or recombined. These subdivisions and/or recombinations should be considered as equivalent solutions to the present disclosure. Moreover, the steps of the foregoing series of process may be performed naturally in a chronological order in which the steps are described, however, the steps are not necessarily performed in the chronological order. Instead, some steps may be performed concurrently or independently of each other. It is appreciated by a person of ordinary skill in the art that, all or any steps or components of the method and apparatus of the present disclosure may be implemented in form of hardware, firmware, software or a combination thereof in any computing device (including a processor, storage medium or the like) or a network of computing devices, which may be realized by a person of ordinary skill in the art, having read the description of the present disclosure, and using basic programming skills.

[0103] Therefore, the objective of the present disclosure may further be achieved by running a program or a group of programs on any computing device. The computing device may be a well-known and general-purpose device. Thus, the objective of the present disclosure may further be achieved by providing a program product including program codes configured to implement the method or apparatus. In other words, such a program product constitutes the present disclosure, and a storage medium storing such a program product also constitutes the present disclosure. Obviously, the storage medium may be any well-known storage medium or any storage medium to be developed in the future. It is also noted, apparently, the components or steps in the apparatus and method of the present disclosure may be subdivided and/or recombined. These subdivisions and/or recombinations should be considered as equivalent solutions to the present disclosure. Moreover, the steps of the foregoing series of process may be performed naturally in a chronological order in which the steps are described, however, the steps are not necessarily performed in the chronological order. Instead, some steps may be performed concurrently or independently of each other.

[0104] Optional implementations of the present disclosure are described above. It should be noted that, numerous modifications and improvements may be made by a person of ordinary skill in the art without departing from the principles of the present disclosure, and these modifications and improvements shall also fall within the protection scope of the present disclosure.

Claims

1. A method for determining a position of a paging frame, comprising:

determining a synchronization signal block in a discontinuous reception (DRX) cycle; and
determining the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

2. The method for determining the position of the paging frame according to claim 1, wherein the synchronization signal block is a first synchronization signal block in the DRX cycle; or
the synchronization signal block is any one or more synchronization signal blocks among a plurality of synchronization signal blocks in the DRX cycle.

3. The method for determining the position of the paging frame according to claim 2, wherein the determining the position of the paging frame (PF) comprises:

determining a position range of the paging frame (PF), wherein the position range of the paging frame (PF) is a position range of a radio frame at a first predetermined offset after the first synchronization signal block; and
determining the position of the paging frame (PF) within the position range of the paging frame (PF).

4. The method for determining the position of the paging frame according to claim 3, wherein the first predetermined offset is a value configured by a network device, or a value calculated according to a paging parameter configured by a network device.

5. The method for determining the position of the paging frame according to claim 2, wherein the determining the position of the paging frame (PF) comprises:

determining a position of a target synchronization signal block in the plurality of synchronization signal blocks;
determining, as the position range of the paging frame (PF), a position of a radio frame at a second predetermined

offset after the target synchronization signal block; and
determining the position of the paging frame (PF) within the position range of the paging frame (PF).

- 5 6. The method for determining the position of the paging frame according to claim 5, wherein the determining the position of the target synchronization signal block in the plurality of synchronization signal blocks comprises:

calculating a remainder after a terminal identifier divides the number of PFs in the DRX cycle to obtain a result;
calculating a ratio of a multiple to the number of PFs in the DRX cycle, wherein the multiple is a multiple of the
10 DRX cycle relative to a cycle of the synchronization signal block; and
determining the position of the target synchronization signal block according to the product of the result and the ratio, wherein a remainder obtained by dividing the position of the target synchronization signal block by the multiple is equal to the product.

- 15 7. The method for determining the position of the paging frame according to claim 6, wherein the number of PFs in the DRX cycle is a value configured by a network device, or a value determined according to a paging parameter configured by a network device; and
the second predetermined offset is a value configured by the network device, or a value determined according to a paging parameter configured by the network device.

- 20 8. The method for determining the position of the paging frame according to claim 3 or 5, wherein the determining the position of the paging frame (PF) within the position range of the paging frame (PF) comprises:

determining the position of the paging frame (PF) within the position range of the paging frame (PF) according to an order of UE_ID; or
25 calculating a remainder after a terminal identifier divides a first value to obtain a first result, dividing the DRX cycle by the first value to obtain a second result, multiplying the first result by the second result to obtain a first product, determining a frame number of the PF, and determining the position of the paging frame (PF) within the position range of the paging frame (PF) according to an order of the frame number (SFN) of the PF, wherein a result of the frame number of the PF modulus the DRX cycle is equal to the first product, the first value is a
30 smaller one of the DRX cycle and a second value, and the second value is the number of paging occasions (POs) comprised in each DRX cycle; or
determining the position of the paging frame (PF) within the position range of the paging frame (PF), according to a paging parameter configured by a network device.

- 35 9. The method for determining the position of the paging frame according to claim 3 or 5, further comprising:
mapping, according to a mapping relationship, the determined position of the paging frame (PF) to a transmission position after the synchronization signal block.

- 40 10. The method for determining the position of the paging frame according to claim 9, wherein the mapping relationship is predetermined, or is configured by a network, or is determined according to a parameter configured by a network.

11. The method for determining the position of the paging frame according to claim 9, wherein the mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block comprises:

45 mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of UE_ID; or
mapping the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of a frame number (SFN) of the PF, wherein the frame number of the PF is determined based on a second product of a third result and a fourth result, the third result is a remainder obtained
50 by dividing a terminal identifier by the third value, the fourth result is obtained by dividing the DRX cycle by the third value, a result of the frame number of the PF modulus the DRX cycle is equal to the second product, the third value is a smaller one of the DRX cycle and a fourth value, and the fourth value is the number of paging occasions (POs) comprised in each DRX cycle; or
mapping the determined position of the paging frame (PF) to the transmission position after the synchronization
55 signal block according to a paging parameter configured by a network device.

12. A device for determining a position of a paging frame, comprising:

a first determining module, configured to determine a synchronization signal block in a discontinuous reception (DRX) cycle; and
 a second determining module, configured to determine the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

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 13. The device for determining the position of the paging frame according to claim 12, wherein the synchronization signal block is a first synchronization signal block in the DRX cycle; or
 the synchronization signal block is any one or more synchronization signal blocks among a plurality of synchronization signal blocks in the DRX cycle.

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 14. The device for determining the position of the paging frame according to claim 13, wherein the second determining module is further configured to:

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 determine a position range of the paging frame (PF), wherein the position range of the paging frame (PF) is a position range of a radio frame at a first predetermined offset after the first synchronization signal block; and
 determine the position of the paging frame (PF) within the position range of the paging frame (PF).

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 15. The device for determining the position of the paging frame according to claim 13, wherein the second determining module is further configured to:

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 determine a position of a target synchronization signal block in the plurality of synchronization signal blocks;
 determine, as the position range of the paging frame (PF), a position of a radio frame at a second predetermined offset after the target synchronization signal block; and
 determine the position of the paging frame (PF) within the position range of the paging frame (PF).

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 16. The device for determining the position of the paging frame according to claim 15, wherein when determining the position of the target synchronization signal block in the plurality of synchronization signal blocks, the second determining module is further configured to:

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 calculate a remainder after a terminal identifier divides the number of PFs in the DRX cycle to obtain a result;
 calculate a ratio of a multiple to the number of PFs in the DRX cycle, wherein the multiple is a multiple of the DRX cycle relative to a cycle of the synchronization signal block; and
 determine the position of the target synchronization signal block according to the product of the result and the ratio, wherein a remainder obtained by dividing the position of the target synchronization signal block by the
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 multiple is equal to the product.

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 17. The device for determining the position of the paging frame according to claim 14 or 15, wherein in determining the position of the paging frame (PF) within the position range of the paging frame (PF), the second determining module is configured to:

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 determine the position of the paging frame (PF) within the position range of the paging frame (PF) according to an order of UE ID; or
 calculate a remainder after a terminal identifier divides a first value to obtain a first result, divide the DRX cycle by the first value to obtain a second result, multiply the first result by the second result to obtain a first product,
 45
 determine a frame number of the PF, and determine the position of the paging frame (PF) within the position range of the paging frame (PF) according to an order of the frame number (SFN) of the PF, wherein a result of the frame number of the PF modulus the DRX cycle is equal to the first product, the first value is a smaller one of the DRX cycle and a second value, and the second value is the number of paging occasions (POs) comprised in each DRX cycle; or
 50
 determine the position of the paging frame (PF) within the position range of the paging frame (PF), according to a paging parameter configured by a network device.

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 18. The device for determining the position of the paging frame according to claim 14 or 15, further comprising: a mapping module, configured to map, according to a mapping relationship, the determined position of the paging frame (PF) to a transmission position after the synchronization signal block.

19. The device for determining the position of the paging frame according to claim 18, wherein the mapping module is further configured to:

map the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of UE_ID; or

map the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to an order of a frame number (SFN) of the PF, wherein the frame number of the PF is determined based on a second product of a third result and a fourth result, the third result is a remainder obtained by dividing a terminal identifier by the third value, the fourth result is obtained by dividing the DRX cycle by the third value, a result of the frame number of the PF modulus the DRX cycle is equal to the second product, the third value is a smaller one of the DRX cycle and a fourth value, and the fourth value is the number of paging occasions (POs) comprised in each DRX cycle; or

map the determined position of the paging frame (PF) to the transmission position after the synchronization signal block according to a paging parameter configured by a network device.

20. A terminal device, comprising a processor configured to:

determine a synchronization signal block in a discontinuous reception (DRX) cycle; and
determine the position of the paging frame (PF) after a position where the synchronization signal block in the DRX cycle is located.

21. A terminal device, comprising a processor, a memory, and a computer program that is stored on the memory and executable on the processor, wherein when executing the computer program, the processor is configured to implement steps of the method for determining the position of the paging frame according to any one of claims 1 to 11.

22. A computer-readable storage medium, wherein a computer program is stored on the computer-readable storage medium, and the computer program is executed by a processor to implement steps of the method for determining the position of the paging frame according to any one of claims 1 to 11.

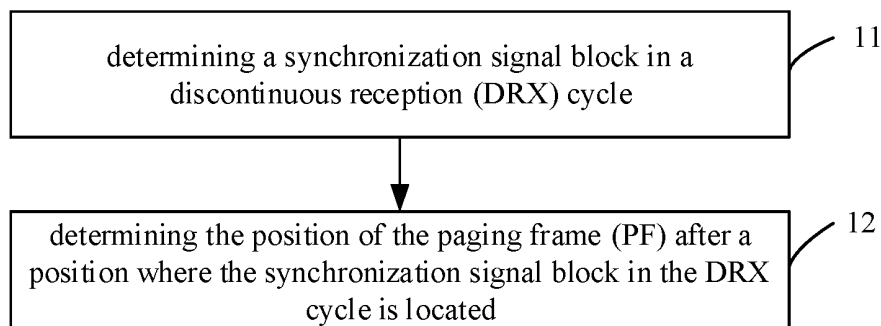


FIG. 1

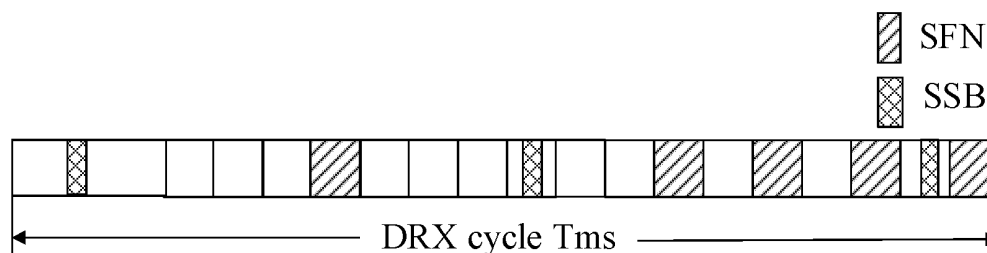


FIG. 2

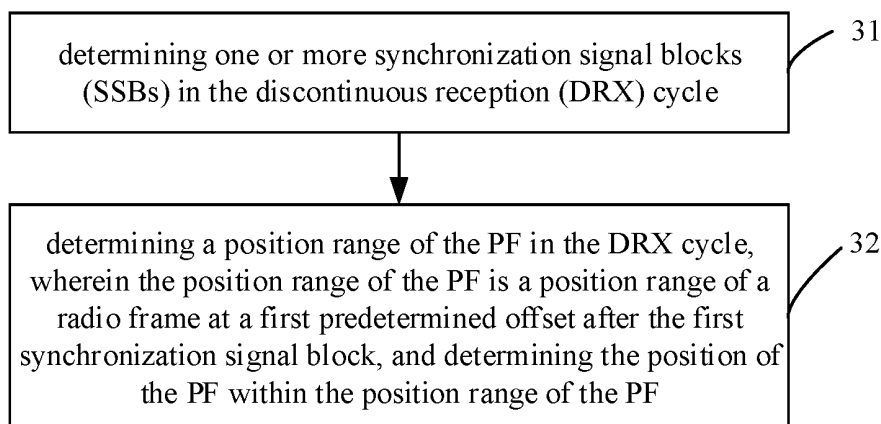


FIG. 3

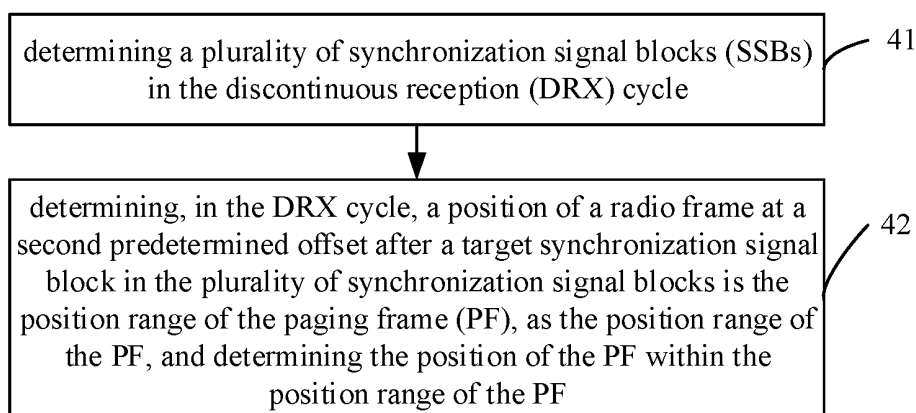


FIG. 4

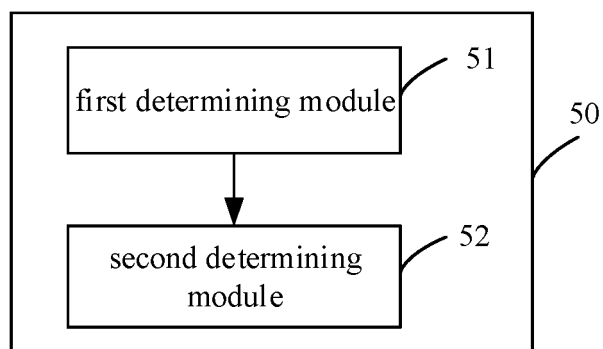


FIG. 5

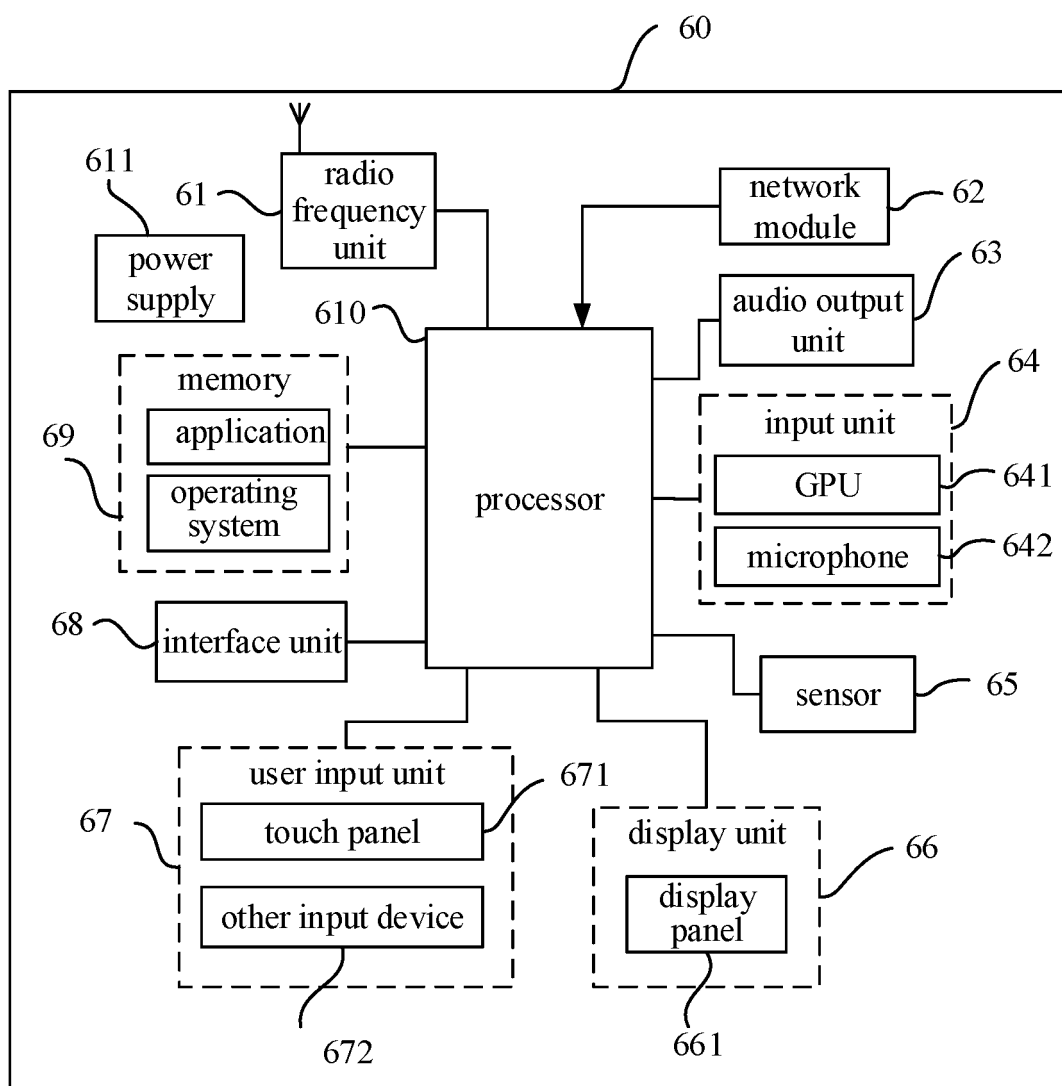


FIG. 6

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2019/074677

A. CLASSIFICATION OF SUBJECT MATTER

H04W 52/02(2009.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04W

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPI, EPODOC, CNPAT, CNKI, 3GPP: 同步信号块, 寻呼帧, 位置, synchron+ signal block, SSB, paging frame, PF, location, position

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2010087210 A1 (LEE, Y.D. ET AL.) 08 April 2010 (2010-04-08) description, paragraphs [0037]-[0045], and figures 5-6	1-2, 12-13, 20-22
A	CN 105208640 A (INTERDIGITAL TECHNOLOGY CORPORATION) 30 December 2015 (2015-12-30) entire document	1-22
A	US 2012079370 A1 (XEROX CORPORATION) 29 March 2012 (2012-03-29) entire document	1-22
A	US 2007008915 A1 (SAMSUNG ELECTRONICS CO., LTD.) 11 January 2007 (2007-01-11) entire document	1-22

☐ Further documents are listed in the continuation of Box C.
 ☒ See patent family annex.

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“T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

“X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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Date of the actual completion of the international search

18 March 2019

Date of mailing of the international search report

27 March 2019

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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2019/074677

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CN 105208640 A	30 December 2015	KR 20140141685 A	10 December 2014
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REFERENCES CITED IN THE DESCRIPTION

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