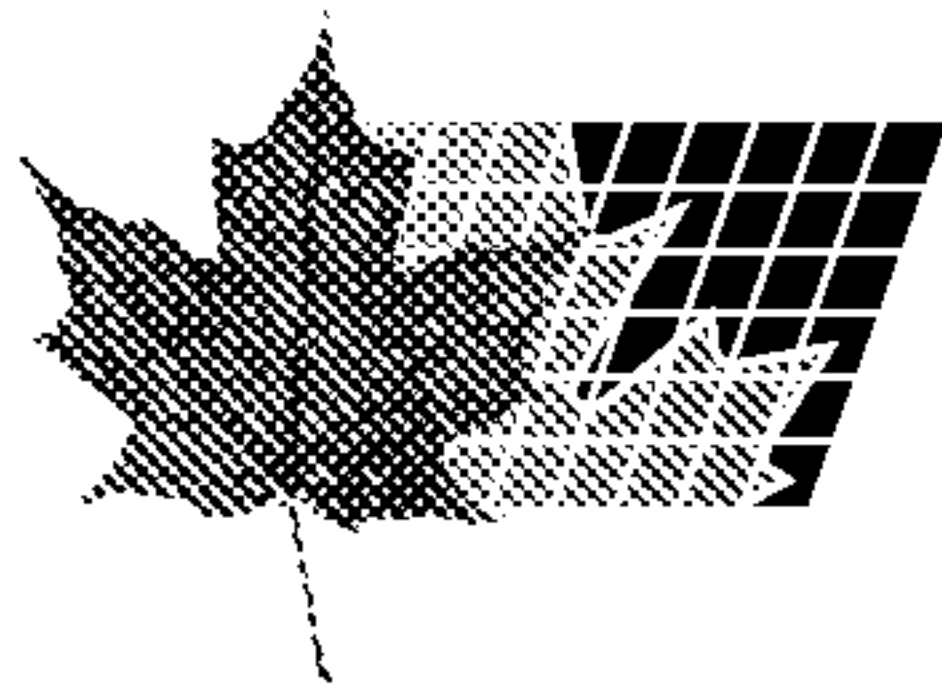




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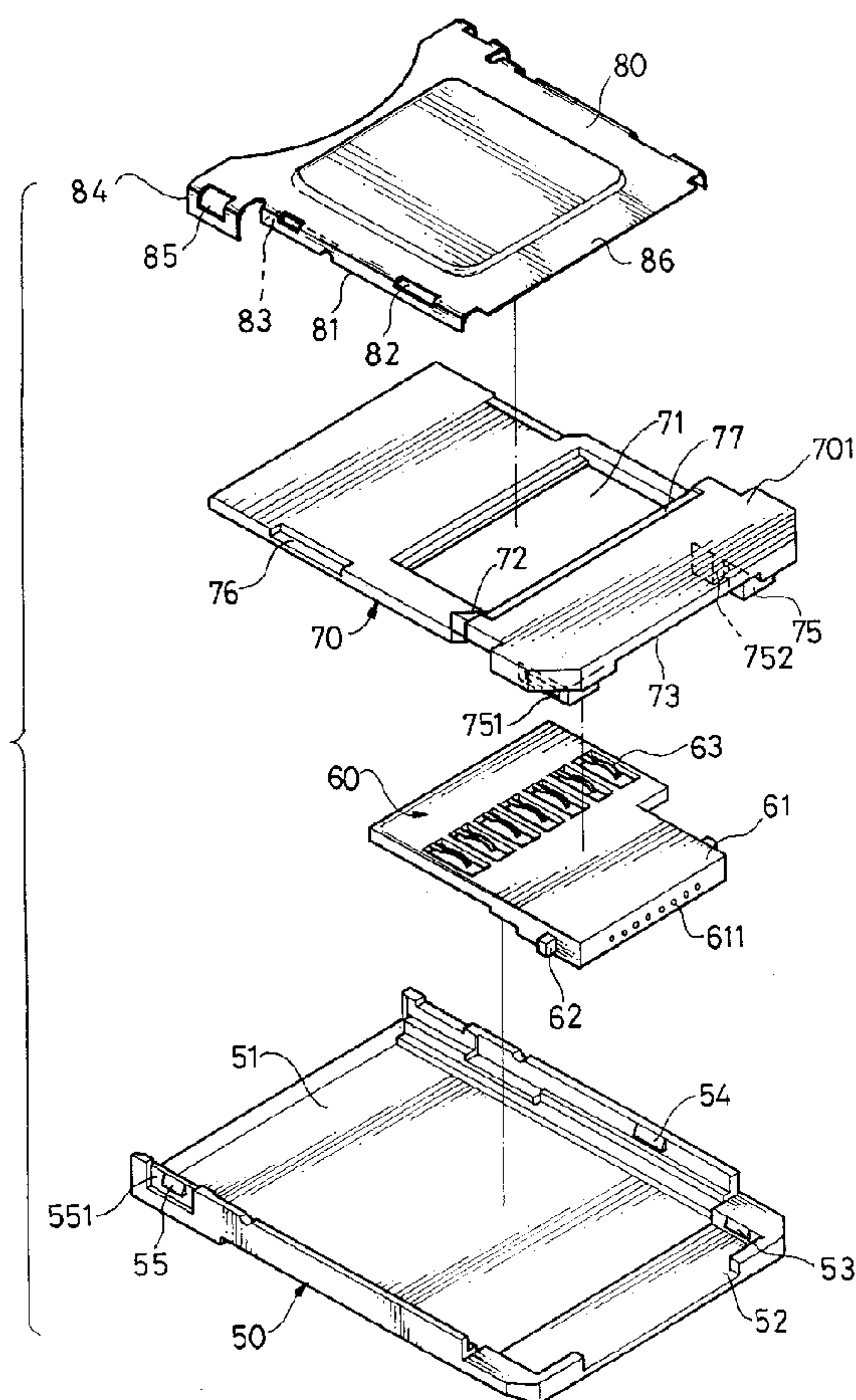
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(54) **ADAPTATEUR DE MEMOIRE**

(54) **MEMORY ADAPTER**



(57) A memory adapter having a first cover (50), a circuit board (60), a second cover (70) detachably connected the circuit board (60) to the first cover (50) and a third cover (80) detachably connected with the first cover (50) and abutted the second cover (70) toward the first cover (50) is used by users to adapt to different cassette memory of different sizes. With the adapter, users are able to use cassette memory of different dimensions in different electrical appliance readily.



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MEMORY ADAPTER

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ABSTRACT OF THE DISCLOSURE

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A memory adapter having a first cover (50), a circuit board (60), a second cover (70) detachably connected the circuit board (60) to the first cover (50) and a third cover (80) detachably connected with the first cover (50) and abutted the second cover (70) toward the first cover (50) is used by users to adapt to different cassette memory of different sizes. With the adapter, users are able to use cassette memory of different dimensions in different electrical appliance readily.

10

Fig. 2

1 MEMORY ADAPTER

2 BACKGROUND OF THE INVENTION

3 1. Field of the invention

4 The present invention relates to a memory adapter, and more particularly to
5 a memory adapter adapted to receive a cassette memory which is used to store
6 digital information therein. The memory adapter having an adapter received
7 therein is designed to be electrically connected with the cassette memory on a
8 first end thereof and a connector on a second end thereof, such that the memory
9 adapter is able to electrically connect the cassette memory of a smaller size with
10 a socket of a larger size.

11 2. Background

12 A cassette memory is used to store therein digital information which is
13 applicable in various fields, such as personal computer, digital recorder, digital
14 camera, etc. The introduction of different cassette memory facilitates the
15 exchange of information of different formats. Referring to Fig. 8, when the
16 cassette memory 20 is in use, it must securely attach to a connector 10 securely
17 and electrically connected with an electrical appliance, such as a personal
18 computer (not shown). The connector 10 has two substantially U-shaped,
19 oppositely designed tracks 11 respectively formed on each one of two opposed
20 arms (not numbered) and a plurality of pins 12 securely inserted through a
21 bottom thereof. The cassette memory 20 has two opposite flanges 21
22 respectively formed on each one of two opposed sides thereof and
23 corresponding to one of the tracks 11 and a plurality of through holes 22
24 defined in a bottom thereof and each corresponding to one of the pins 12 of the
25 connector 10. In combination with the connector 10, the memory cassette 20 is
26 able to transmit digital information stored therein to other electrical appliance
27 readily.

28 However, since the physical configurations of different memory cassettes
29 vary greatly, users often have to go through a lot of steps to transform the

1 digital information when needed. Person skilled in the art will appreciate that
2 there is no production standard for the size of memory cassettes, therefore, it is
3 almost impossible to find compatibility between two cassette memories of
4 different brands, which causes the users to go through a lot of steps and
5 procedures to transform the information. Due to the steps and procedures
6 necessary to transform the information, users will waste a lot of time and
7 energy to process the information before it becomes available.

8 The present invention provides an improved memory adapter to obviate
9 and/or mitigate the aforementioned problems.

10 SUMMARY OF THE INVENTION

11 The primary object of the invention is to provide an improved memory
12 adapter. The memory adapter has a top cover adapted to have two oppositely
13 disposed flanges each corresponding to one of the tracks of a connector and a
14 plurality of through holes defined in an end of a circuit board detachably
15 received within the adapter. With the arrangement as described above, cassette
16 memories of different sizes are able to be electrically connected with the
17 memory adapter and the information stored within the memory cassette is,
18 therefore, able to be transmitted directly to other electrical appliance without
19 going through endless procedures to transform the digital information of the
20 cassette memory.

21 Another object of the invention is to provide an orientation limit within the
22 adapter so that correct orientation of the cassette memory while being inserted
23 into the memory adapter is ensured.

24 Other novel features and advantages of the invention will become more
25 apparent from the following detailed description when taken in conjunction
26 with the accompanying drawings.

27 BRIEF DESCRIPTION OF THE DRAWINGS

28 Fig.1 is a schematic view showing the corporation of the present invention
29 with a conventional connector and a cassette memory;

1 Fig. 2 is a bottom exploded view of the memory adapter shown in Fig. 1;

2 Fig. 3 is a schematic plan view of the cassette memory shown in Fig. 1;

3 Fig. 4 through Fig. 7 are cross sectional views of the cassette memory
4 shown in Fig. 4; and

5 Fig. 8 is a schematic view showing the corporation of a conventional
6 memory cassette and a connector.

7 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT:

8 Referring to Figs. 1 and Fig. 2, it is noted that a memory adapter 40
9 constructed in accordance with the present invention is shown. The memory
10 cassette 40 has a top cover 50, a bottom cover 80 detachably connected with the
11 top cover 50, a cover sheet 70 detachably connected with the bottom cover 80
12 and a circuit board 60 detachably received between the top cover 50 and the
13 cover sheet 70. The top cover 50 has two opposed walls (not labeled), a first
14 cutout 501 defined in a front portion of each of the walls, a second cutout 52
15 defined in an end face thereof, a pair of indentations 551 defined in an outer
16 face of the two walls, a pair of first protrusions 53 respectively formed on an
17 inner face defining the first cutout 501, a pair of second protrusions 54
18 respectively formed on an inner face of the walls and a pair of third protrusions
19 55 respectively formed on an outer face defining the indentation 551. The
20 circuit board 60 has a front portion 61 adapted to be received within the second
21 cutout 52 of the top cover 50 and having a plurality of holes 611 defined in a
22 face thereof, a pair of limitations 62 respectively formed on opposite sides
23 thereof and a contacting area 63 electrically connected to each one of the holes
24 611. The cover sheet 70 has a front portion 701 adapted to mate with the first
25 cutout 501 of the top cover 50 when assembly and having two opposed legs 75
26 each provided with a depression 751 corresponding to one of the first
27 protrusions 53 of the top cover 50 and defined in an outer face thereof and a
28 channel 752 defined in an inner face thereof and corresponding to one of the
29 limitations 62 of the circuit board 60, a window 71 defined therein and

1 corresponding to the contacting area 63 of the circuit board 60, two recesses 76
2 defined in a face thereof. Furthermore, the cover sheet 70 has a cutout 73
3 defined to correspond to the second cutout 52 of the top cover 50, so that when
4 the cover sheet 70 and the top cover 50 are assembled, the cutout 73 of the
5 cover sheet 70 and the second cutout 52 of the top cover 50 define an opening.
6 The bottom cover 80 has two opposed flanges 81 extending downward
7 therefrom, and each of the flanges has a first opening 82 defined therein and
8 corresponding to one of the first protrusions 53 of the top cover 50, a bend 83
9 inward formed therewith and corresponding to one of the recess 76, an
10 extension 84 having two opposed ends each provided with a second opening 85
11 defined therein and corresponding to one of the third protrusions 55 of the top
12 cover 50. It is appreciated that the extension 84 has a width equal to a width of
13 the top cover 50 and a width between the opposed flanges 81 is equal to a width
14 between the inner faces of the two opposed walls, such that the extension 84 of
15 the bottom cover 80 is able to securely engage with the top cover 50 via the
16 connection between the second opening 85 and the pair of third protrusions 55
17 and the flanges 81 are able to be securely connected with the opposed wall of
18 the top cover 50 via the connection between the first opening 82 and the pair of
19 second protrusions 54 after the circuit board 60 and the cover sheet 70 are
20 assembled with the top cover 50.

21 When in assembly, referring to Figs. 3 to 7, the circuit board 60 is placed
22 within the space defined by the second cutout 52. Because a width of the front
23 portion 61 of the circuit board 60 is the same as a width of the second cutout 52,
24 the front portion 61 of the circuit board 60 and the second cutout 52 of the top
25 cover 50 are assembled flush. When mating the cover sheet 70 with the top
26 cover 50, the front portion 701 of the cover sheet 70 is adapted to be flush with
27 a height of the two opposed walls of the top cover 50 due to the provision of the
28 first cutout 501 of the top cover 50. The limitations 62 will be securely received
29 in the corresponding channels 752 of the cover sheet 70 and the opposed pair of

1 first protrusions 53 will be received in the corresponding depressions 751 of the
2 cover sheet 70 and the contacting area 63 of the circuit board 60 is exposed
3 through the window 71 when assembled with the top cover 50, the circuit board
4 60 and the cover sheet 70 is completed. Finally, when the bottom cover 80 is
5 assembled with the cover sheet 70, due to the width between the opposed
6 flanges 81 being equal to the width between the opposed inner faces of the two
7 opposed walls of the top cover 50 and the width of the extension 84 being equal
8 to the width between the outer faces of the opposed walls of the top cover 50,
9 so that the first protrusions 53 are able to be respectively received in one of the
10 corresponding first openings 82 and the third protrusions 55 are able to be
11 respectively received in one of the corresponding second openings 85.
12 Furthermore, the bottom cover 80 is able to force the cover sheet 70 to be
13 securely attached to the top cover 50 via the abutment of the pair of bends 83 at
14 the corresponding recesses 76 of the cover sheet 70.

15 It is to be understood, however, that even though numerous characteristics
16 and advantages of the present invention have been set forth in the foregoing
17 description, together with details of the structure and function of the invention,
18 the disclosure is illustrative only, and changes may be made in detail, especially
19 in matters of shape, size, and arrangement of parts within the principles of the
20 invention to the full extent indicated by the broad general meaning of the terms
21 in which the appended claims are expressed.

1 The embodiment of the invention in which an exclusive property or
2 privilege is claimed are defined as follows:

3 1. A memory adapter comprising:

4 a first cover having a pair of opposite walls each provided with a first
5 protrusion and a second protrusion formed on an inner face of the wall and a
6 third protrusion formed on an outer face of the wall, a first cutout defined in a
7 front portion thereof, a second cutout defined in an end face thereof and an
8 indentation defined in the outer face where the third protrusion is formed;

9 a circuit board adapted to be received within the first cover and a front
10 portion thereof being received in the second cutout and having a pair of
11 opposed limitations formed on opposite sides thereof, a plurality of holes
12 defined in an end face thereof and a contacting area electrically connected with
13 the holes; and

14 a second cover having a front portion configured to correspond to and mate
15 with the first cutout of the first cover, a window defined to correspond to the
16 contacting area of the circuit board, a cutout defined to correspond to the
17 second cutout of the first cover and two legs extending downward and each of
18 which has a depression defined in an outer face thereof and corresponding to
19 one of the limitations and a channel defined in an inner face thereof and
20 corresponding to one of the first protrusions of the first cover;

21 whereby a memory is able to be inserted into the adapter and electrically
22 connected with the contacting area of the circuit board.

23 2. The memory adapter as claimed in claim 1 further comprising a pair of
24 opposed recesses defined in a face of the second cover and a third cover
25 adapted to be detachably connected with the first cover and abutted the second
26 cover to the first cover.

27 3. The memory adapter as claimed in claim 2, wherein the third cover
28 comprises a pair of opposite flanges extending downward therefrom and having
29 a pair of first openings respectively defined to correspond to and detachably

1 connected with the second protrusions of the first cover and a pair of bends
2 respectively formed with one of the flanges and extending inward to correspond
3 to and abut the second cover via disposed in the recesses of the second cover.

4 4. The memory adapter as claimed in claim 2, wherein the third cover
5 comprises an extension formed therewith and having a pair of opposed second
6 openings respectively defined to correspond to and detachably receiving one of
7 the indentations of the first cover therein.

8 5. The memory adapter as claimed in claim 2, wherein a width between the
9 flanges is equal to a width between inner faces of the walls.

10 6. The memory adapter as claimed in claim 4, wherein a width of the
11 extension is equal to a width between outer faces of the walls.

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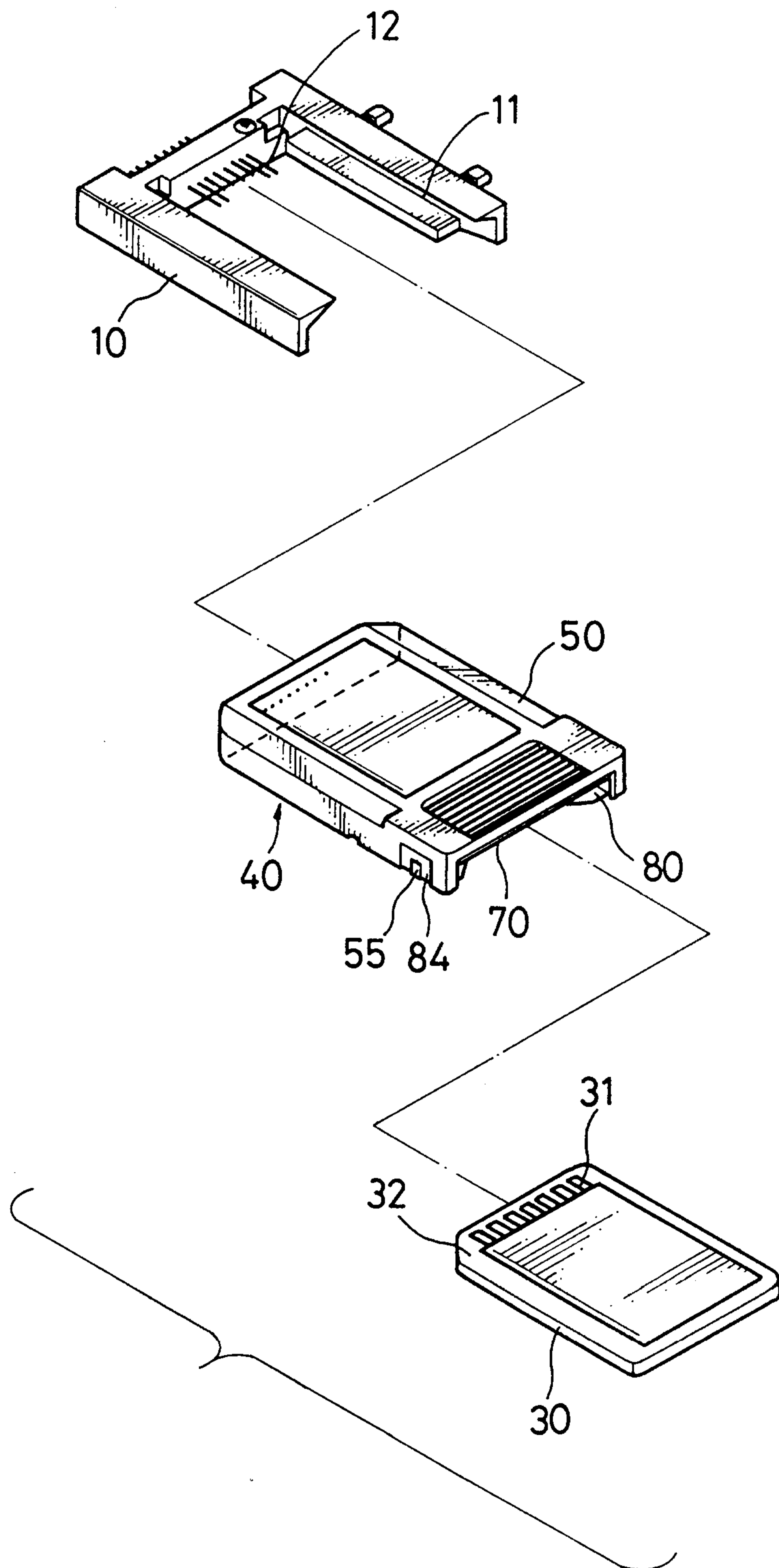
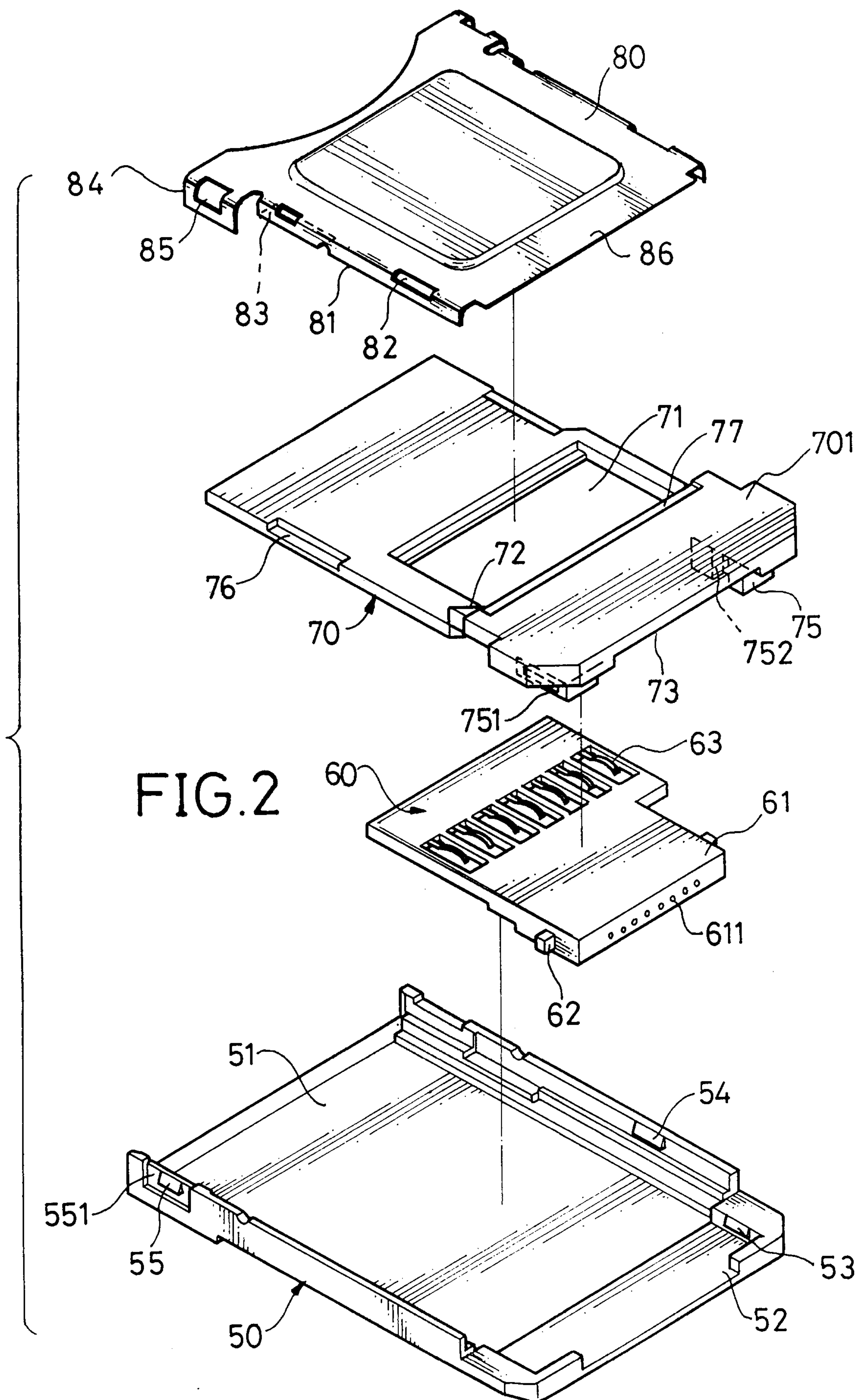
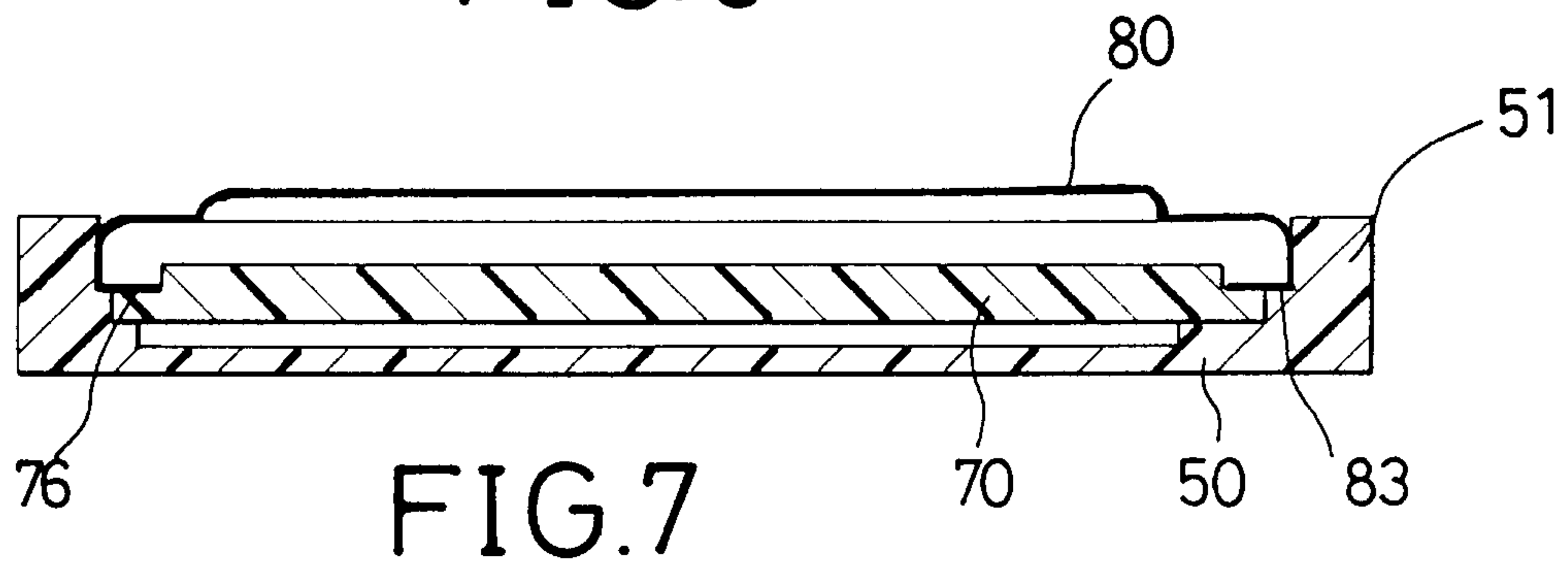
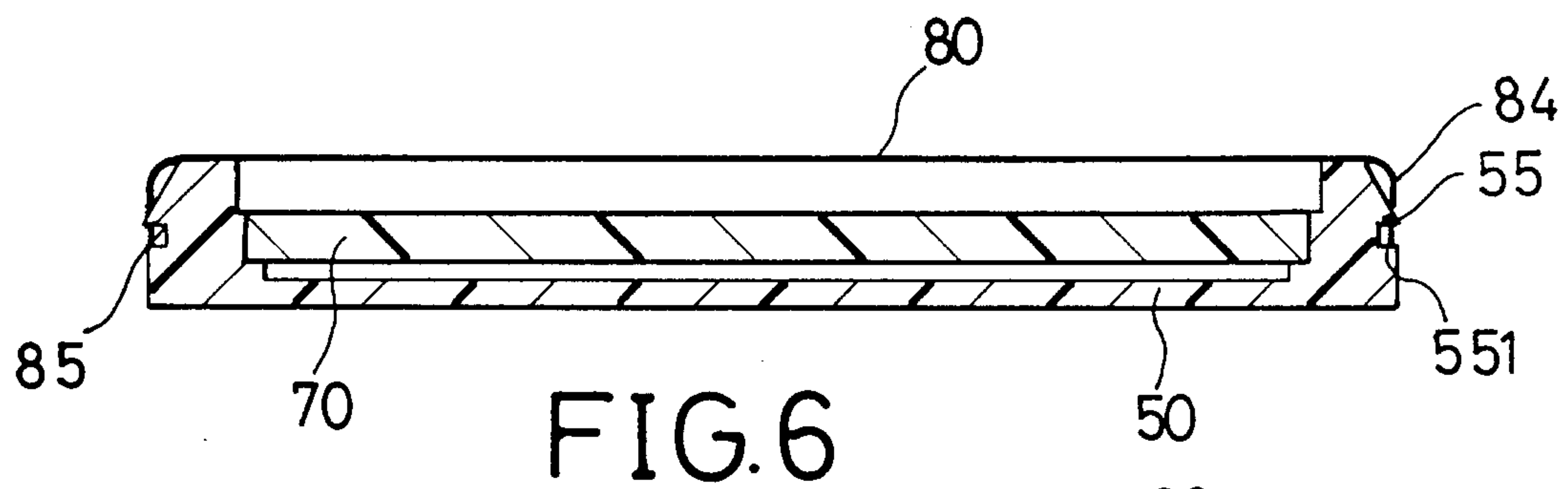
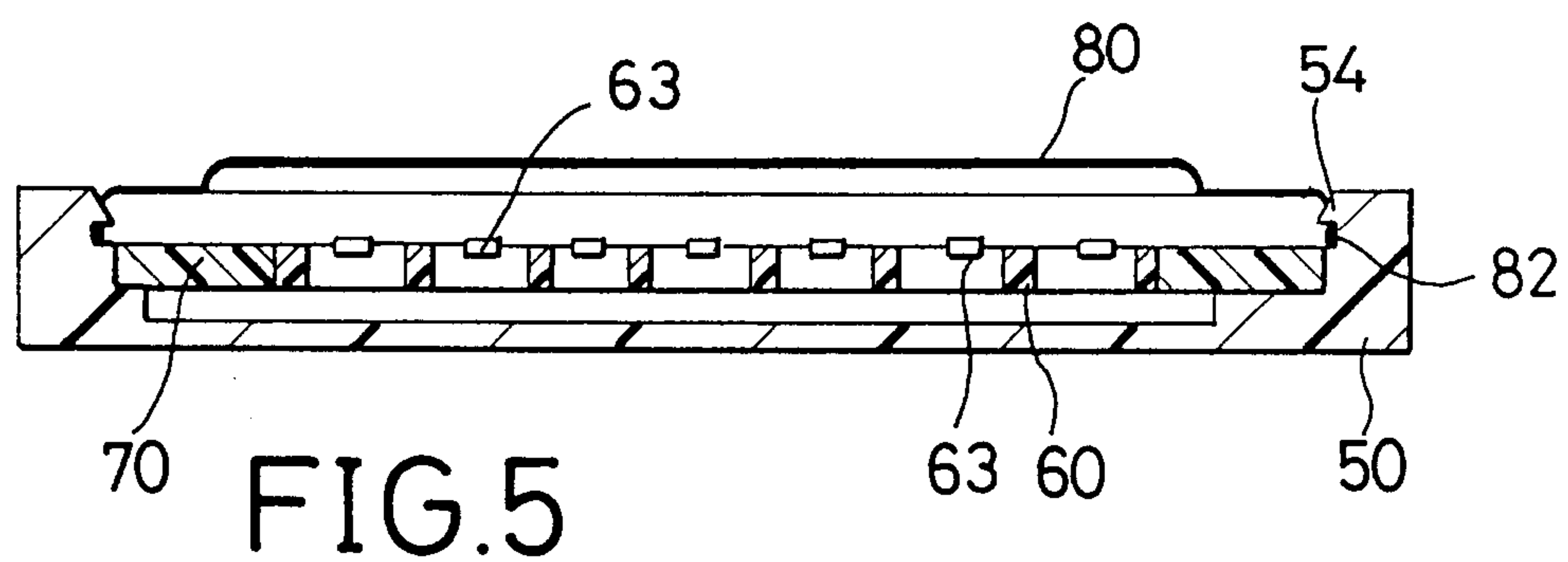
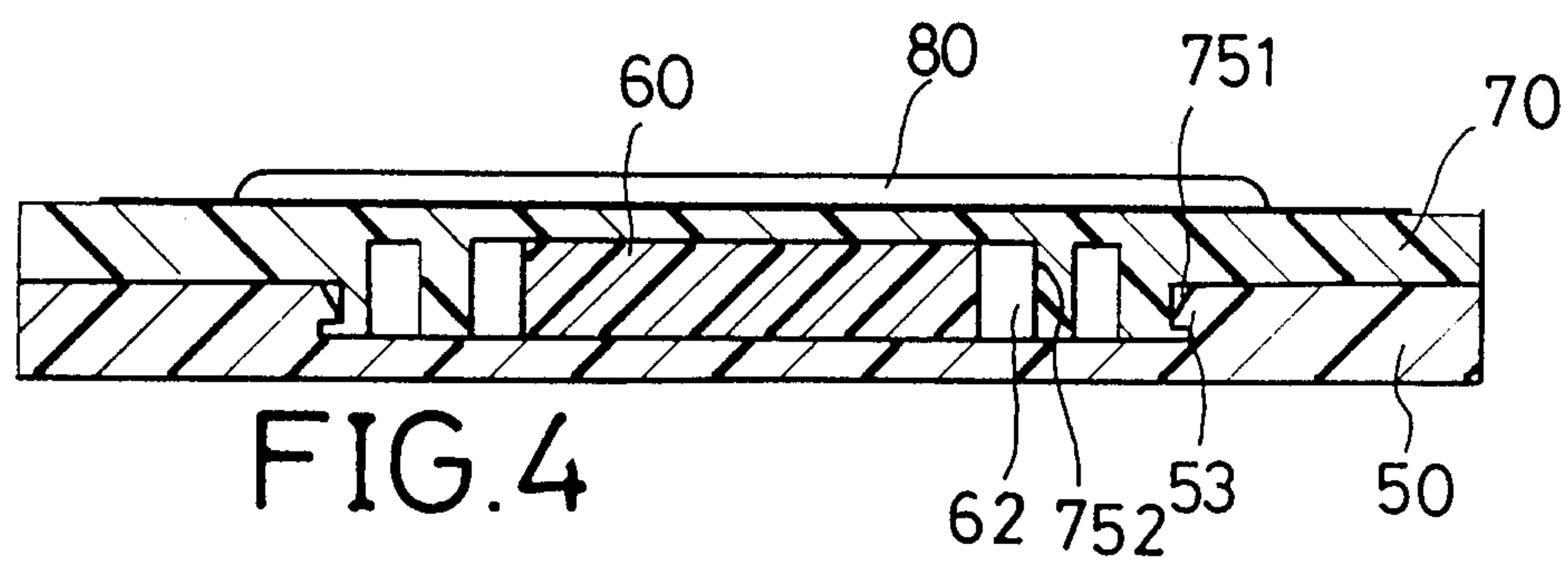


FIG.1





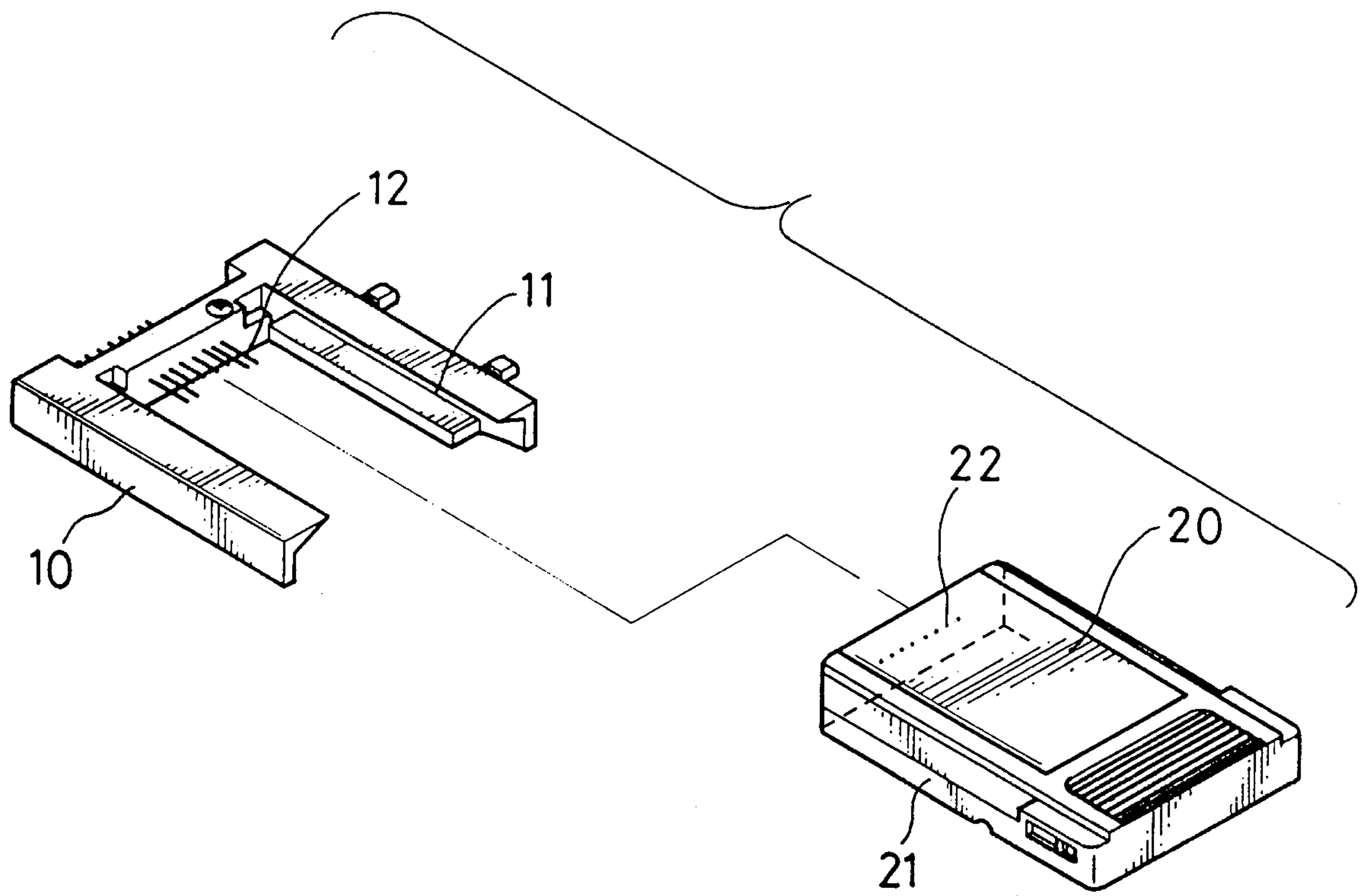


FIG.8

