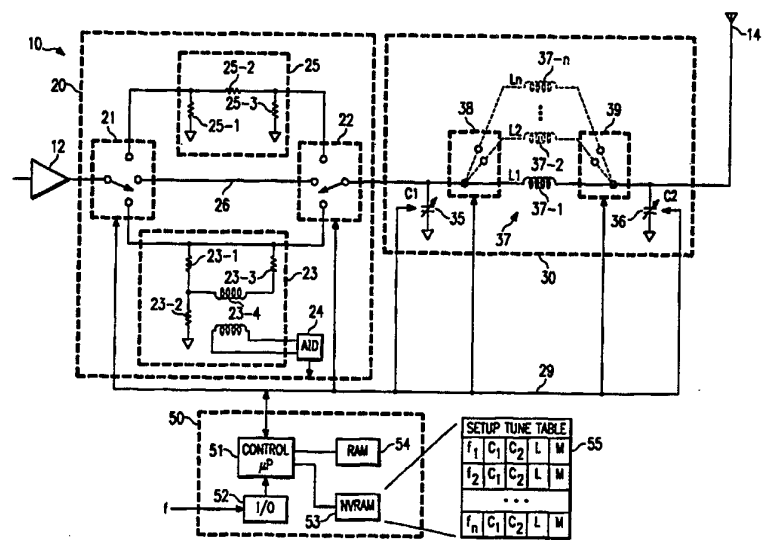




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<p>(21) International Application Number: PCT/US96/07428 (22) International Filing Date: 22 May 1996 (22.05.96) (30) Priority Data: 08/470,542 6 June 1995 (06.06.95) US (71) Applicant: FLASH COMM, INC. [US/US]; 300 A-1 North Drive, Melbourne, FL 32934 (US). (72) Inventors: BELCHER, Donald, K.; Route 1, Box 4860, Rogersville, TN 37857 (US). BAILEY, David, C.; 337 Dorset Drive, Cocoa Beach, FL 32931 (US). (74) Agents: YOUNG, Jessica, W. et al.; Vinson &amp; Elkins L.L.P., 2300 First City Tower, 1001 Fannin Street, Houston, TX 77002-6760 (US).</p>		<p>(81) Designated States: AM, AT, AU, BB, BG, BR, BY, CA, CH, CN, CZ, DE, DK, EE, ES, FI, GB, HU, JP, KE, KG, KP, KR, KZ, LK, LR, LT, LU, LV, MD, MG, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SI, SK, TJ, TT, UA, UZ, VN, ARIPO patent (KE, LS, MW, SD, SZ, UG), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG).</p> <p><b>Published</b> <i>With international search report.</i></p>

(54) Title: AUTOMATIC ANTENNA TUNER FOR LOW-COST MOBILE RADIO



(57) Abstract

An automatic antenna tuner (10) which conjugately matches the impedance of an electrically small antenna (14) to the impedance of a transmit amplifier output stage by using a pi-type impedance matching network (30) with adjustable capacitive circuit components (35, 36) which are preferably implemented using a low-cost escapement mechanism (40). During an initial set up tuning procedure, a resistive Wheatstone bridge detector (23) is disposed between the output of the power amplifier (12) and the input to the pi network, to feedback a measure a degree of match of the pi network to a controller (50). The capacitive elements (35, 36) in the legs of the pi network are then electromechanically adjusted under the operator of the controller (50) until an optimum setting is found. These initial impedance values are determined for each frequency in an operating range and then stored in a non-volatile memory (53). As a second step in the tuning procedure, a fine adjustment is made to the stored optimum impedance values just prior to transmission to accommodate changing environmental conditions and the like.

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AUTOMATIC ANTENNA TUNER FOR  
LOW-COST MOBILE RADIO

TECHNICAL FIELD OF THE INVENTION

This invention relates generally to antennas and in particular to a low cost automatic antenna tuner for high frequency (HF) mobile radio applications.

5

BACKGROUND OF THE INVENTION

For transmitting antennas having a narrow tuned bandwidth, it is imperative to achieve an optimum performance level such that maximum radio frequency energy is radiated. This is especially true for applications in mobile radio, where the radio transmitters are typically power limited. One way to maximize the effective radiated power is to ensure that the impedance of the antenna is conjugately matched to the output of a transmit amplifier output stage by using a so-called pi-type impedance matching network having shunt capacitive circuit elements and one or more series inductive circuit elements.

10

15

Where the transmitter always operates on a fixed carrier frequency, or even over a narrow range of carrier frequencies, the proper antenna tuning can be performed in advance, by selecting the appropriate inductive and capacitive circuit elements to match the antenna impedance.

20

25

However, this process becomes more involved when the transmit frequency is not known in advance. In such applications, certain techniques have been developed to

automatically tune the antenna to the desired transmit frequency.

5 One approach is to use a phase discriminator that compares the phase of a current or voltage at the driving point with the phase of a current or voltage in the main conductor of the antenna. The impedance value of an adjustable circuit element is then changed, depending upon the sign of the phase difference.

10 Other automatic antenna tuning systems attempt to minimize a standing wave ratio at the antenna by sensing a transmitted power and a reflected power. The ratio of these two power levels is then used to indicate a degree of match. Such a system is described in U.S. Patent 5,225,847 issued to Roberts, D.A. and DeWitt, B.T. and assigned to Antenna Research Associates, Inc.

15 Unfortunately, even such automatic antenna tuning systems have their disadvantages. For example, in certain applications the antenna element or elements are electrically small compared to the operating wavelength of the system. In one such application, namely for mobile radio transmission in the high frequency (HF) radio band, at operating frequencies as low as 3 MegaHertz (MHz), the radio wave length is 100 meters or 328 feet. Obviously, if the radio is to be used in automobiles or as a portable device, practical antennas are necessarily small.

20 Furthermore, in vehicular applications in particular, the antenna may be regularly exposed to varying environmental conditions such as earth constants, and the elements such as mud, snow, ice, salt, corrosion, and the like, which from time to time may change the impedance which must be matched.

25 Thus, it has heretofore been thought that mobile radio transmitters must make use of expensive phase or standing ratio adjustment techniques to enable automatic tuning.

DESCRIPTION OF THE INVENTIONSUMMARY OF THE INVENTION

Briefly, the invention is an automatic antenna tuner  
5 which conjugately matches the impedance of an  
electrically small antenna to the impedance of a transmit  
amplifier output stage by using a so-called pi-type  
impedance matching network having shunt capacitive  
10 circuit elements and one or more series inductive circuit  
elements.

A low cost resistive Wheatstone bridge detector is  
disposed between the output of the power amplifier and  
the input to the pi network. An output signal from the  
bridge is provided to a controller, such as a  
15 microprocessor, to feed back a measure of the degree of  
match of the pi network to the antenna impedance.

Preferably the capacitive elements in the legs of  
the pi network are electromechanically adjustable, by  
making use of an escapement mechanism operated under  
20 control of a pull-type solenoid. The escapement  
mechanism uses the linear motion provided by the solenoid  
against a spring to advance a ratchet arm against the  
teeth of a ratchet wheel which is attached to a rotary-  
type adjustable capacitor, to convert the linear motion  
25 to the rotary motion of the adjustable capacitor.

The controller then uses the output signal from the  
bridge detector to adjust the capacitance of one or more  
of the capacitive elements to maximize the degree of  
match to the antenna impedance.

30 As a result, an inexpensive mechanism is provided to  
match the impedance of the antenna, under control of a  
microprocessor. Indeed, the exact angular position of  
the capacitive element can be monitored at all times by  
counting the number of pulses that have been applied by  
35 the microprocessor to the solenoid.

Alternatively, a stepper motor may be used to operate the capacitive elements, but at a greater cost.

The inductance of the pi network may also be varied by switching in various inductive circuit elements also  
5 under microprocessor control.

Also in accordance with the invention, a two-step tuning procedure is used to determine the inductance values which includes both a set-up procedure.

The set-up procedure determines an initial starting  
10 position for each adjustable circuit element. This set-up procedure is typically performed during an installation procedure, at service time, or during re-initialization procedures. It is an exhaustive search whereby the response of every possible combination of impedance  
15 values is examined at each of the frequencies across a radio frequency band of interest. The optimum impedance values are then stored in a non-volatile random access memory (NVRAM) under control of the microprocessor.

The second, pre-transmit tuning procedure is  
20 performed just prior to attempting an actual transmission with the antenna. This process serves to fine-adjust the impedance values determined during the set-up procedure, to account for ambient environmental conditions which may be present as a result of deploying the antenna in a  
25 vehicle. This pre-transmit tuning procedure performs a shortened version of the initial set-up tuning procedure, where impedance values are examined for frequencies in the immediate vicinity of the values that were stored in the nonvolatile memory during the set-up procedure.

In the event that the pre-transmit tuning process  
30 fails to achieve a pre-established tuning quality level, the transmission may still be allowed to occur at a lower power level setting by switching in an attenuator disposed between the transmitter amplifier and tuning  
35 device.

The invention has several advantages.

A radio frequency transmitter amplifier designed to operate into a fixed impedance no longer exhibits an unpredictable or widely varying impedance presented by an electrically short antenna, such as one mounted on a vehicle or in a portable radio unit.

An exact impedance matching function is provided between the antenna and transmitter output, which maximizes the effective radiated power.

In applications such as mobile or portable radio devices this not only conserves battery energy, but also permits operation in crowded radio frequency bands such as the high frequency radio band, where the existence of many other transmitters in adjacent frequency channels requires precise tuning.

The automatic antenna tuning system is much less expensive to implement than prior systems which make use of phase or standing power measurement techniques, which are impractical in low cost applications.

#### BRIEF DESCRIPTION OF THE DRAWINGS

The above and further advantages of the invention may be better understood by referring to the following description in conjunction with the accompanying drawings in which:

Fig. 1 is a circuit diagram of an automatic antenna tuner according to the invention;

Fig. 2 is a mechanical schematic diagram of an escapement mechanism which is preferably used to implement the adjustable capacitive circuit elements;

Fig. 3 is a flowchart of a sequence of steps performed by a control microprocessor during an initial set-up tuning procedure; and

Fig. 4 is a flowchart of the steps performed during a secondary pre-transmit tuning procedure.

DETAILED DESCRIPTION OF THE INVENTION

Fig. 1 illustrates an automatic antenna tuner 10 according to the invention, which includes an input selector circuit 20, a pi-type impedance matching network 30 consisting of a pair of adjustable shunt capacitances 35 and 36 and series inductance 37, and a controller 50.

The tuner 10 finds application in a specific type of radio communication system in which mobile and/or portable field units transmit in the high frequency (HF) radio band. In such an application, a signal,  $f$ , is received by the controller 50 to tune to a particular frequency. The signal  $f$  may be received from network operation control center via a control channel over a radio link operating at HF or in other radio bands. The remote unit is then expected to tune to the commanded frequency and to begin transmission within several seconds.

To accomplish this, the antenna tuner 10 preferably makes use of a pulsed escapement mechanism to implement the adjustable capacitances 35 and 36. In a manner that is described in detail below, the escapement mechanism provides for automatic adjustment of the capacitances 35 and 36 via the controller 50.

The series inductance 37 can also be adjustable, if implemented as a number of physical inductors 37-1, 37-2, ..., 37-n which can be selectively switched into the pi network 30 by switches 38 and 39. The switches 38 and 39 may be ganged together.

More particularly, the tuner 10 conjugately matches the output impedance of a transmitter power amplifier 12 so that a maximum possible amount of power is transmitted to an antenna 14, despite the fact that the antenna 14 is electrically small. This is done by adjusting the impedances in the pi network, such as by operating the adjustable shunt capacitive elements 35 and 36, and/or



switching in various values for the series inductance 37.

It should be noted that the controller 50 adjusts these impedances 35, 36, and/or 37 by communicating with the adjustable capacitances 35 and 36, and the switches 38 and 39 over any suitable control interface such as a control bus 29.

The input selector circuit 20, operating in conjunction with the controller 50, determines exactly how much to adjust the impedances 35, 36, and/or 37 in the pi network 30. The input selector circuit 20 consists of a pair of switches 21 and 22, a Wheatstone bridge detector 23, an analog-to-digital (A/D) converter 24 and attenuator 25. The switches 21 and 22 are three position switches which are preferably ganged together and which may be operated under control of the controller 50. In a first position, the switches connect the Wheatstone bridge 23 between the amplifier 12 and pi network 30. This position is used when taking measurements of the degree of match between the pi network 30 and the antenna 14.

A second position of the switches 21 and 22 selects a short path 26 which directly connects the amplifier 12 to the pi network 30. This setting is used during radio frequency operation of the antenna 14.

A third position of the switches 21 and 22 is used to select an attenuator 25 when a predefined degree of match cannot be obtained.

The process of detecting the degree of match and thus the control over the setting of the switches 21, 22, 38 and 39, as well as adjustable capacitances 35 and 36, will become evident in the following discussion of the operation of the controller 50 in connection with Figs. 3 and 4.

The Wheatstone bridge 23 is preferably a resistive divider type network consisting of a pair of series divider resistors 23-1, 23-2. A resistor 23-3 and coil

23-4 are connected in parallel with the first divider resistor 23-1. The coil 23-4 provides a voltage for detecting a degree of match between the amplifier 12 and pi network 30. The analog-to-digital converter 24  
5 permits the controller 50 to sense the voltage at the coil 23-4.

The attenuator 25 may be any suitable attenuator such as the resistive pi network shown, in which consists of three resistors 25-1, 25-2, and 25-3.

10 Fig. 2 shows a detailed schematic view of an escapement mechanism 40 which is preferably used to implement the adjustable capacitances 35 and 36. The escapement mechanism consists of a central shaft 48 rotating about a pivot point, a ratchet wheel 41, a  
15 ratchet pawl 42 which may be spring-loaded, an anchoring spring 43, a ratchet arm 44, and a pull-type solenoid 45 which has a control connection back to the controller 50 via bus 29.

The ratchet wheel 41 may be formed from any  
20 suitable, low cost material such as molded plastic. The ratchet pawl 42 may be of a spring metal material such as beryllium copper, formed by stamping. The ratchet arm 44 may be similarly formed. The coil spring 43 is any convenient spring. The solenoid 45 may be any type of  
25 pull solenoid.

The spring loaded ratchet pawl 42 provides a bias against outer diameter of the ratchet wheel 41 so that the ratchet wheel 41 will rotate when the ratchet arm is momentarily lifted from contact with the ratchet wheel  
30 41. The central shaft 48 is in mechanical communication with the central shaft of a rotary-type adjustable capacitive circuit element (not shown).

Under command of the controller 50, the solenoid 45 may be operated to move the ratchet arm 44 in either of  
35 the directions indicated by the arrow A. As a result, the ratchet wheel 41 is moved in a counterclockwise

direction when the ratchet arm 44 is pulled, and movement of the ratchet wheel is halted when the ratchet arm is moved in the other direction. In this manner, any one of the many possible rotary positions of the ratchet wheel  
5 41 may be selected by the controller 50.

Alternatively, the adjustable capacitances 35 and 36 may be implemented using stepper motors in place of the escapement mechanism, albeit at increased cost.

Regardless of the implementation of the adjustable  
10 capacitances 35 and 36, because of their mechanical nature, it may take some time for the tuner 10 to reach a steady state after being commanded to tune from a frequency at one end of the high frequency band to a desired tuning position in another place in the high  
15 frequency band. Thus, in an effort to reduce the response time for the feedback loop created by the controller 50 in conjunction with the resistive bridge detector 23 and the adjustable capacitances 35 and 36 and/or switchable inductances 37, the invention makes use  
20 of a two stage tuning procedure.

Returning briefly to Fig. 1, the controller 50 is implemented using any suitable programmable processors such as a microprocessor 51, an input/output interface 52, and memory in the form of non-volatile random access  
25 memory (NVRAM) 53 and random access memory (RAM) 54.

The controller 50 reads data from the A/D converter 24 to determine a database 55 of optimum capacitance and inductance values for each of a number of desired  
30 frequencies to be tuned. These stored values are then used as an initial starting position for a second stage, pre-transmit tune process.

This two stage process permits the tuner 10 to quickly switch the capacitance 35 and 36 and inductance 17 to the approximately correct values just prior to transmission  
35 at a designated frequency or at least in the neighborhood of the desired frequency, while still accommodating

changes in ambient conditions or slight frequency mismatch.

The initial set-up tune procedure is an exhaustive search where all tuning conditions are examined at each of the frequencies at which the antenna is expected to operate. Since it may take many minutes or even several hours to perform the exhaustive search, the set-up procedure is typically only performed when the unit is first placed into service, or is requested to perform a re-initialization process, such as after a catastrophe failure.

As shown in Fig. 3, in the first step 300 of this set-up procedure, one of the frequencies to be used is selected.

The next step 301 is to select a value C1 for the capacitance 35, a value C2 for capacitance 36 and a value L for inductance 37.

In step 302 the capacitance values C1 and C2 are set by operating the escapement mechanism 40 associated with each of the capacitors 35 and 36. In addition, the inductance value L may be set by operating the switches 38 and 39 to select one or more of the inductors 37-1, 37-2, ..., 37-n.

In the next step 303, after a suitable waiting period for the response of the pi network to stabilize, the microprocessor 51 accesses the resistive bridge detector 23 via the A/D converter 24 to obtain a value indicating the match quality for the current setting of the capacitances C1 and C2 and inductance L.

In step 304, this match quality value is then compared to a best stored match quality value for the selected frequency.

In step 305, if the stored match value is not as good as the value most recently read in step 303, then control passes through step 306 where the current C1, C2

and L values are stored as the best values for the current frequency, f.

If, however, in step 305 the stored value is better than the most recently read value, then an update is not made.

In step 307 it is then determined if all variations of values C1, C2 and L have been tried. If this not the case, then control passes back to step 301 where another combination is tried.

If however, each of the capacitance and inductance values have already been already tried, then the set-up tune process is complete for the indicated frequency, and the next frequency in the band may be analyzed.

The stored values in the table 55 may be temporarily stored in a conventional volatile RAM 54 up to this point since a conventional RAM 54 operates more quickly. If the volatile RAM 54 has been used, then a step 308 is performed where the values are written into the non-volatile RAM 53. The use of an NVRAM 53 to store the final values in the set-up tune table 30 permits battery power from the unit 10 to be removed while the unit is not in operation. This permits the rather laborious set-up tune process to occur only once, while the unit is powered up and powered down many times over long periods of time.

Fig. 4 is a sequence of steps performed by the microprocessor 51 during the second stage of the tuning process referred to as a pre-transmit tune procedure. This sequence of steps 400-408 is performed when the microprocessor 51 receives a command to activate the power amplifier 12 and to tune the antenna 14 to a new operating frequency, f. The pre-transmit tune process accounts for variations in ambient antenna conditions such as road dirt, salt, earth constants, snow, ice, rain, water and so forth. Such varying environmental conditions are quite typical when the antenna is

installed in a vehicle and can adversely affect the operation efficiency of the antenna.

5 In a first step 400 the set-up table 55 is consulted to determine initial C1, C2 and L values for the chosen operating frequency, f.

10 In the next step 401 these values are used by the microprocessor 51 to set the capacitance of capacitors 35 and 36 and inductance 37 accordingly. The actual adjustment is performed by the microprocessor 51 sending an appropriate instruction over the bus 29 to operate the escapement mechanism 39 in each of the capacitances 35 and 36 by the appropriate number of pulls on the solenoid to set the desired capacitance, and to operate the switches 38 and 39 to select the proper inductance.

15 After a suitable waiting time for the response to settle, step 402 is executed where the match value is read from the bridge detector 23. This match value is then stored as a present best match value.

20 The next sequence of steps 403-408 is the actual fine tuning procedure.

In step 403 the sequence begins by making a predetermined number of attempts to fine-adjust to the capacitances C1 and C2 and inductance L.

25 In the first step, a new impedance value set is selected and the response of the bridge 23 is then read via the A/D converter 24 in step 405.

30 In step 406, if the most recently read match value is better than the match value that was previously stored, then the newly read value is saved as the stored value. If however, this is not the case, then control passes directly to step 408. In step 408, the sequence is then repeated for the next fine adjustment value of the impedances. At some point, the process terminates, at which time the stored values are then selected as the operating values, and the capacitors 35 and 36 are set.

35

The control microprocessor 51 may make use of a conventional random access memory (RAM) 54 to store the temporary values which are only used when the unit is in operation, and which are discarded each time that the unit is commanded to tune to a different frequency.

In a final step in the process, in the event that the pre-transmit tune procedure of Fig. 4 fails to achieve a pre-established tune quality in step 409, then the unit is instructed to operate at a low power level by activating a signal path to the antenna 14 through attenuator 25, by switching the switches 21 and 22 to the appropriate position.

What has been described is a low cost automatic antenna tuner that can accommodate the impedance presented by a electrically short vehicle mounted antenna such as a whip or loop antenna without the need for a banks phase shifters, standing wave ratio measurements, or expensive directional couplers.

The invention automatically accommodates changes in environmental conditions, such as can often be presented to a vehicle-mounted antenna, by a two step tuning process.

The invention also uses a nonvolatile RAM to avoid the need to perform a laborious tuning match procedure each time the unit is activated, which conserves battery power.

WHAT IS CLAIMED IS:

1. An antenna tuner for automatically matching the impedance of an electrically short antenna to the impedance of an output stage of a transmitter amplifier, the antenna tuner operating over a range of frequencies, the automatic antenna tuner comprising:

an impedance matching network consisting of a pair of shunt capacitive circuit elements and at least one inductive circuit element, the capacitance of at least one of the capacitive circuit elements being mechanically adjustable in increments over a range of incremental capacitance values, a first capacitive circuit element being disposed between a first node and a ground reference node, a second capacitive circuit element being disposed between a second node and the ground reference node, and a series inductive circuit element being disposed between the first and second nodes, the second node for connecting to the antenna;

a resistive bridge network, disposed between the output stage of the transmitter amplifier and the first node, the resistive bridge network providing a bridge output signal indicating a degree of match between an impedance presented by the output stage of the amplifier and the impedance matching network to the impedance presented by the antenna;

means for electromechanically adjusting the capacitance of at least one of the first or second capacitive elements, by reading a capacitance set signal indicating a capacitance value and by then adjusting the capacitance of said at least one adjustable capacitive circuit element to a selectable capacitance value in the range of incremental capacitance values;

non-volatile storage means, for storing a plurality of optimum capacitance values, with at least one optimum capacitance value being associated with one frequency in



the range of frequencies to which the antenna can be tuned; and

control means, for performing an antenna set-up procedure whereby a set of optimum capacitance values is determined for each frequency in the range of frequencies, the set of optimum capacitance values being then stored in the non-volatile storage means, the optimum capacitance value for each frequency being determined by serially selecting each one of the incremental capacitance values in the range of capacitance values, setting said at least one adjustable capacitive circuit element to that incremental capacitance value by operating the means for mechanically adjusting the capacitance, and then reading the output signal from the resistive bridge network, to determine which incremental capacitance value provides a best impedance match to the antenna, and upon receiving an instruction to tune the antenna to a particular frequency in the range of frequencies, reading the optimum capacitance value for the particular frequency, and for providing the capacitance set signal to the means for electromechanically adjusting.

2. An automatic antenna tuner as in claim 1 wherein the at least one adjustable capacitive circuit element is a rotary-shaft-type adjustable capacitor and the means for electromechanically adjusting the capacitance of said at least one capacitive circuit element additionally comprises:

an escapement mechanism consisting of a central shaft rotating about a pivot point which is coupled to a rotary shaft of said at least one capacitive circuit element, a ratchet wheel connect to the central shaft, the ratchet wheel engaging a ratchet pawl, a ratchet arm disposed adjacent the ratchet wheel to incrementally rotate the ratchet wheel upon application of a force in a linear direction thereto; and

a solenoid, for receiving the capacitance set signal and for applying linear force to the ratchet arm in response thereto to perform one or more incremental rotations of the ratchet wheel to thereby set the capacitance of the adjustable capacitive circuit element.

5           3. An antenna tuner as in claim 2 wherein a spring is disposed opposite the solenoid and ratchet arm to provide a rotary biasing force against the ratchet wheel.

10           4. An antenna tuner as in claim 1 wherein the at least one adjustable capacitive circuit element is a rotary-shaft-type adjustable capacitor and the means for electromechanically adjusting the capacitance of the at least one capacitive circuit element additionally comprises:

15           a stepper motor arranged to receive the capacitance set signal, and to rotate a rotary shaft of the at least one capacitive circuit element in response thereto, thereby setting the capacitance.

20           5. An antenna tuner as in claim 1 wherein each of the first and second adjustable capacitive circuit elements is mechanically adjustable over a range of impedance values, additionally comprising:

25           means for electromechanically adjusting a second one of the capacitive circuit elements in response to a second capacitance set signal indicating a second capacitance value, and then adjusting the capacitance of the second capacitive circuit element to a second selectable capacitance value in the range of incremental capacitance values.

30           6. An antenna tuner as in claim 5 wherein the non-volatile storage means additionally stores a plurality of optimum capacitance values for the second adjustable capacitive circuit element, and wherein the control means additionally determines an optimum second capacitance value for each frequency in the range of frequencies.

35

7. An antenna tuner as in claim 1 wherein the inductive circuit element comprises a plurality of inductors and associated switches, and wherein the non-volatile storage means stores a plurality of optimum impedance values for the inductive circuit element, and wherein the control means additionally determines an optimum inductance value for each frequency in the range of frequencies.

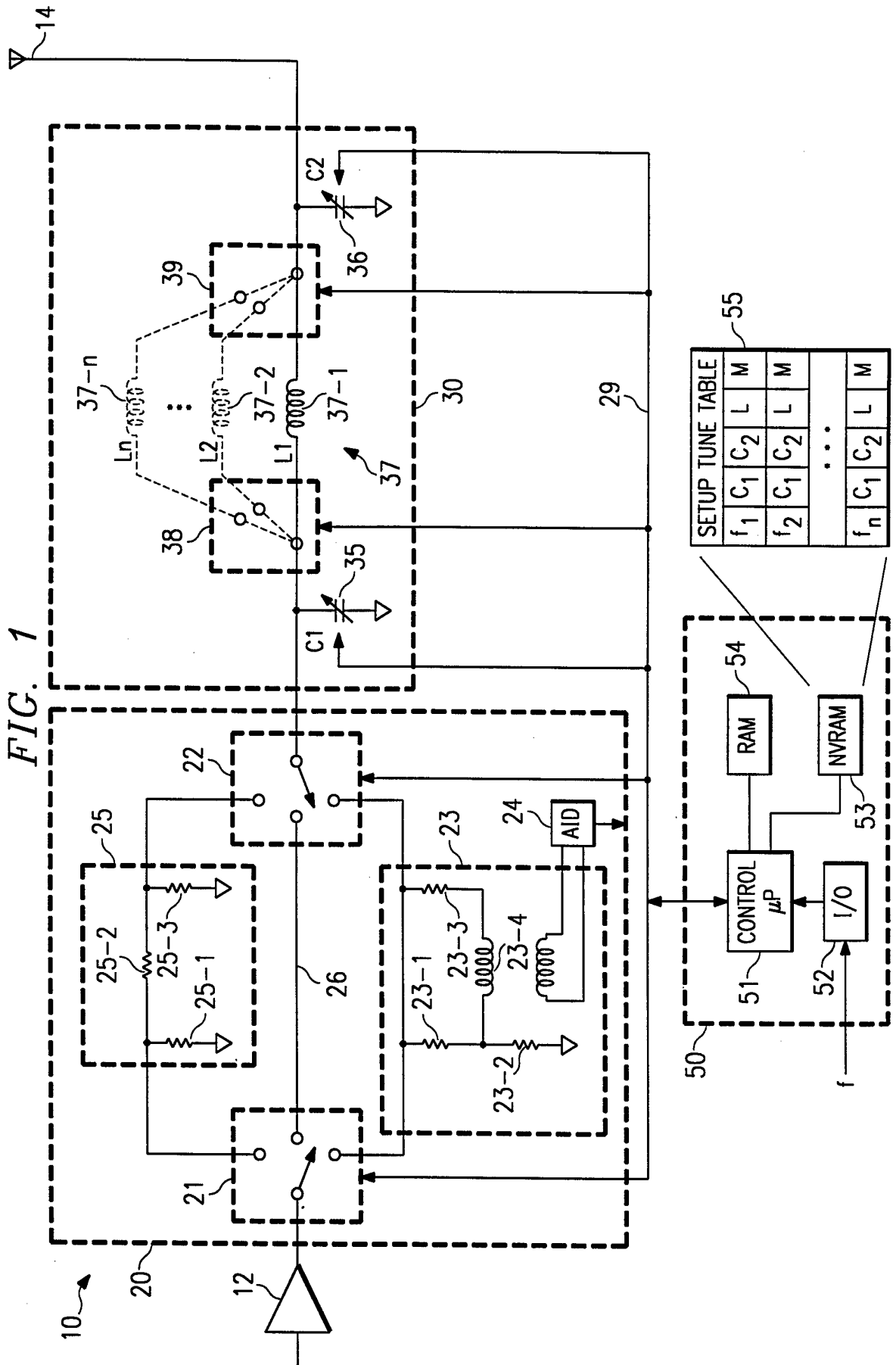
8. An antenna tuner as in claim 1 wherein the control means additionally performs a pre-transmit procedure upon receiving the instruction to tune the antenna to the particular frequency by reading the stored optimum capacitance value and then selecting a subset of the incremental capacitance values near the stored optimum capacitance value, and then serially setting the at least one adjustable capacitive circuit element to each of the incremental subset capacitance values by operating the means for mechanically adjusting the capacitance, and then reading the output signal from the resistive bridge network, to determine which of the incremental capacitance values in the subset provides a best impedance match to the antenna just prior to operating the antenna.

9. An antenna tuner as in claim 8 additionally comprising:

an attenuator disposed between the outer stage of the transmitter amplifier and the first node;

a switch arranged to operate the attenuator; and

wherein the control means additionally determines whether the pre-transmit tuning process fails achieves a pre-established tuning quality level, and if not, then operating the switch to enable the attenuator.



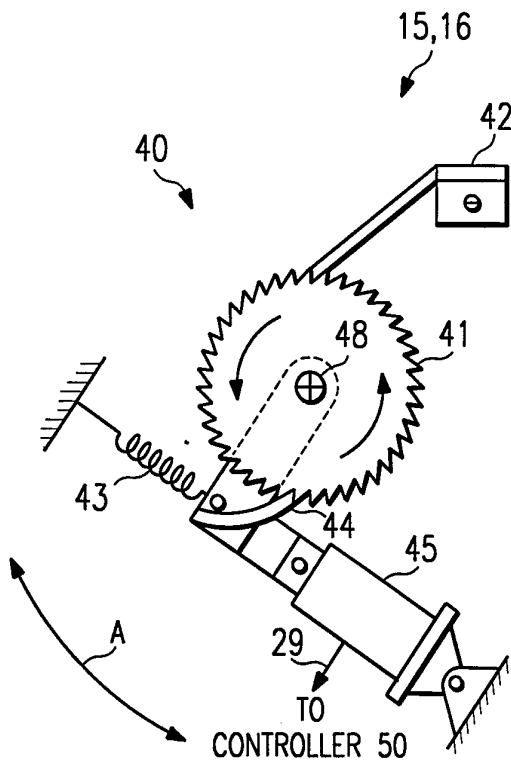


FIG. 2

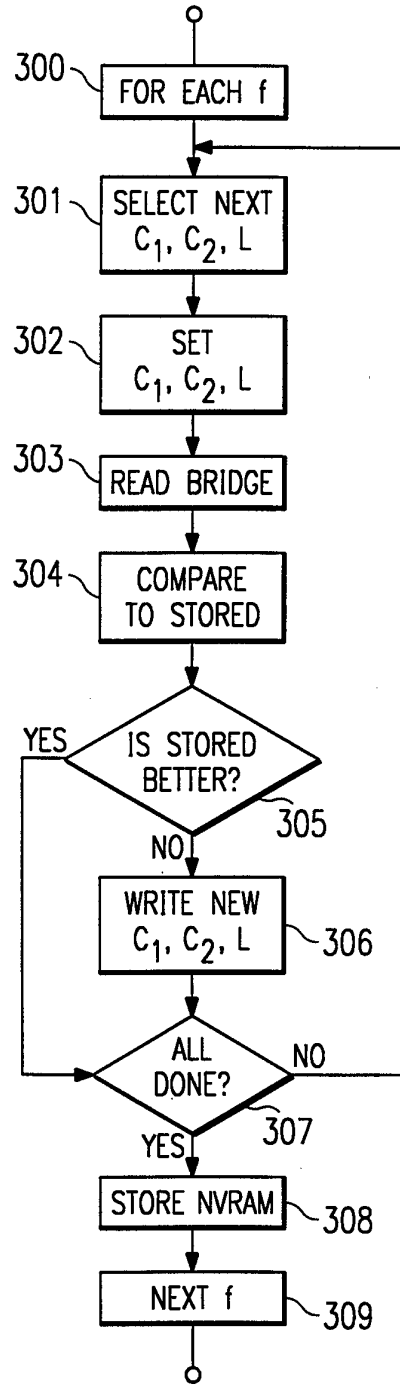


FIG. 3

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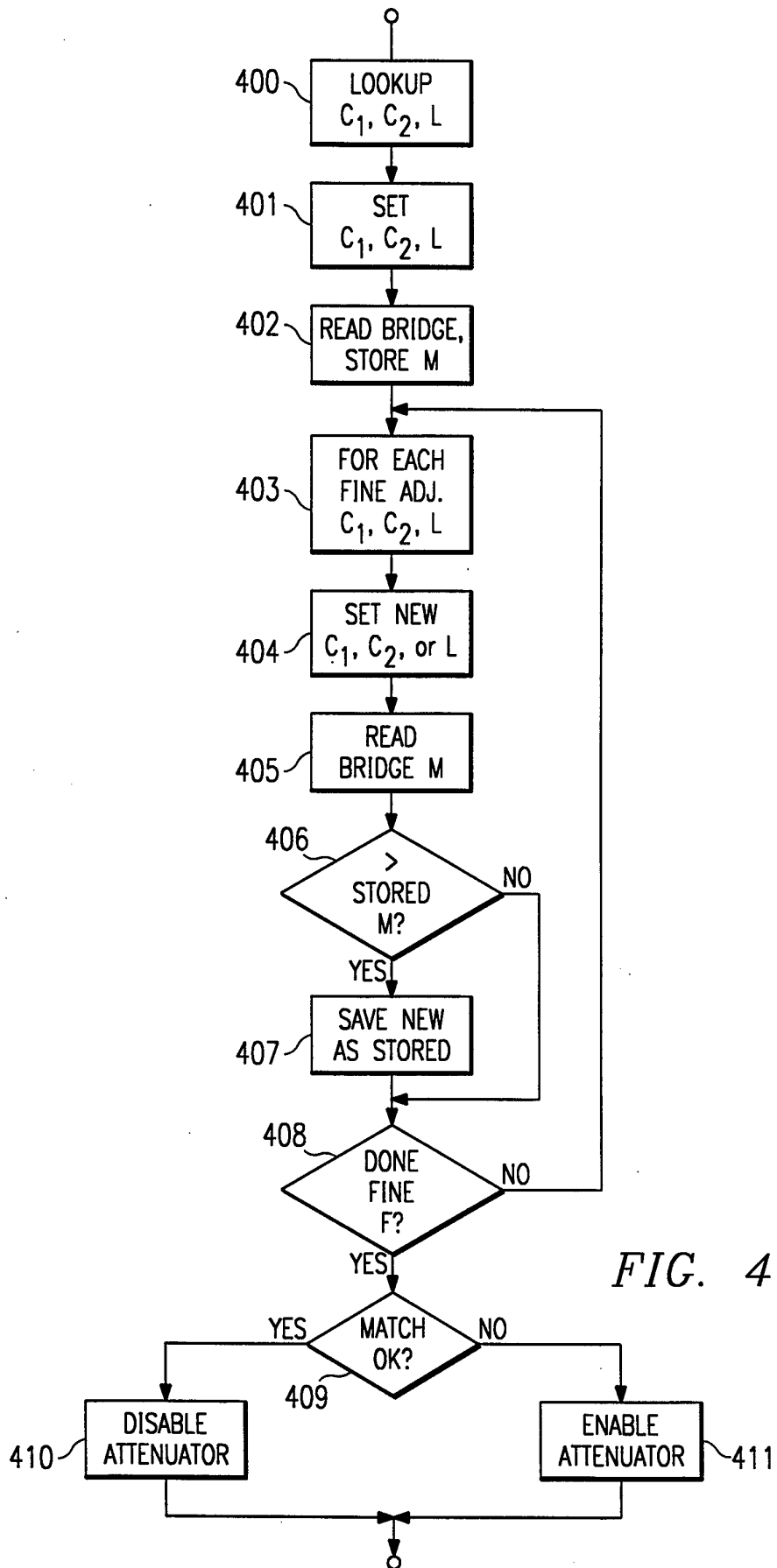


FIG. 4

**INTERNATIONAL SEARCH REPORT**

International application No.  
PCT/US96/07428

**A. CLASSIFICATION OF SUBJECT MATTER**  
 IPC(6) :H01Q 1/50; H03H 7/38  
 US CL : 343/850, 860,861; 333/32, 17.3; 455/123  
 According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 343/850, 852, 858, 860,861; 333/32, 17.3; 455/115, 123, 125

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
 NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
 APS (ANTENNA#, AMPLIFIER, IMPEDANCE MATCHING)

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US, A, 5,198,825 (SAKURAI ET AL) 30 MARCH 1993, FIGURES 1 AND 2.	1-9
A	US, A, 5,151,708 (NASAKE ET AL) 29 SEPTEMBER 1992, FIGURES 2-4.	1-9
A	US, A, 5,258,728 (TANIYOSHI ET AL) 02 NOVEMBER 1993, ENTIRE DOCUMENT.	1-9
A	US, A, 5,216,392 (FRASER ET AL) 01 JUNE 1993, ENTIRE DOCUMENT.	1-9
A	US, A, 5,212,492 (JESMAN ET AL) 18 MAY 1993, FIGURE 4.	1-9

Further documents are listed in the continuation of Box C.  See patent family annex.

* Special categories of cited documents:	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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Date of the actual completion of the international search 01 AUGUST 1996	Date of mailing of the international search report <b>16 AUG 1996</b>
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