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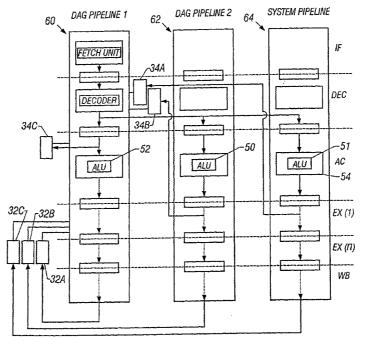
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(54) Title: METHOD AND APPARATUS FOR PROCESSING PROGRAM LOOPS IN PARALLEL



(57) **Abstract:** In one embodiment, a programmable processor is adapted to support hardware loops. The processor may include hardware such as a first set of registers, a second set registers, a first pipeline, and a second pipeline. Furthermore, the processor may include a control unit adapted to efficiently implement the hardware when performing a hardware loop.



#### INTERNATIONAL SEARCH REPORT

Insernational Application No PCT/US 01/50506

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 G06F9/32 G06F9/38 G06F9/30

According to International Patent Classification (IPC) or to both national classification and IPC

#### B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

#### EPO-Internal

C. DOCUM	ENTS CONSIDERED TO BE RELEVANT	
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
E	WO 02 37270 A (INTEL CORP ;ANALOG DEVICES INC (US)) 10 May 2002 (2002-05-10) the whole document	1-30
Х	EP 0 374 419 A (IBM) 27 June 1990 (1990-06-27) page 2, line 35 - line 55	1
A	page 8, line 25 - line 55	6,7,9, 10,13, 15,26, 27,30
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	column 6, line 29 -column 8, line 17	20 23
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χ Further documents are listed in the continuation of box C.	χ Patent family members are listed in annex.
Special categories of cited documents:      A* document defining the general state of the art which is not considered to be of particular relevance      E* earlier document but published on or after the international filing date      L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)      O* document referring to an oral disclosure, use, exhibition or other means      P* document published prior to the international filing date but later than the priority date claimed	<ul> <li>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</li> <li>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</li> <li>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</li> <li>"&amp;" document member of the same patent family</li> </ul>
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Name and mailing address of the ISA  European Patent Office, P.B. 5818 Patentlaan 2  NL - 2280 HV Rijswijk  Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  Fax: (+31-70) 340-3016	Authorized officer  Daskalakis, T

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