



- (51) International Patent Classification:
G06F 9/4401 (2018.01) G06F 11/07 (2006.01)
- (21) International Application Number:
PCT/US2017/018762
- (22) International Filing Date:
21 February 2017 (21.02.2017)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
15/055,448 26 February 2016 (26.02.2016) US
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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

(54) Title: MEMORY MANAGEMENT SYSTEM WITH MULTIPLE BOOT DEVICES AND METHOD OF OPERATION THEREOF

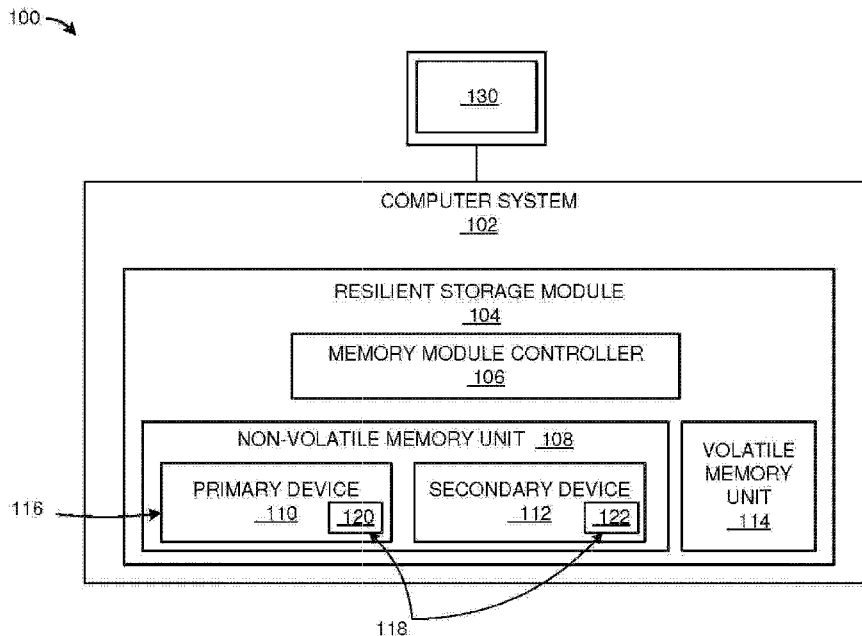


FIG. 1

(57) Abstract: A memory management system, and method of operation thereof, includes: a primary device of a resilient storage module configured as a boot device for booting a computer system; an operational status received from the computer system; a secondary device of the resilient storage module configured as the boot device based on the operational status indicating a non-operational state; and a memory module controller of the resilient storage module for initiating a reboot operation using the secondary device as the boot device.



Published:

- *with international search report (Art. 21(3))*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))*

(88) Date of publication of the international search report:

22 August 2019 (22.08.2019)

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2017/018762**A. CLASSIFICATION OF SUBJECT MATTER****G06F 9/4401(2018.01)i, G06F 11/07(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHEDMinimum documentation searched (classification system followed by classification symbols)
G06F 9/4401; G06F 1/26; G06F 12/00; G06F 13/00; G06F 9/06; G06F 9/44; G06F 11/07Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Korean utility models and applications for utility models
Japanese utility models and applications for utility modelsElectronic data base consulted during the international search (name of data base and, where practicable, search terms used)
eKOMPASS(KIPO internal) & Keywords: boot, switch, detect, signal, status, configure, power, failure, reboot**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2014-0181492 A1 (ENE TECHNOLOGY INC.) 26 June 2014 See paragraphs [0003], [0011], [0031]-[0033], [0038]-[0042]; and figures 1-2.	1-20
Y	US 2011-0161686 A1 (TAKESHI KUMAGAYA) 30 June 2011 See paragraphs [0021], [0046]; and figure 1.	1-20
Y	US 2009-0089514 A1 (DURGESH SRIVASTAVA et al.) 02 April 2009 See paragraphs [0023], [0026]; and figure 1.	2, 7, 12
Y	US 05666538 A (RICHARD M. DENICOLA) 09 September 1997 See column 9, line 65-column 10, line 12.	3, 8, 13
A	US 2012-0191965 A1 (PING-YING CHU) 26 July 2012 See paragraphs [0007], [0023]; and figure 4.	1-20

 Further documents are listed in the continuation of Box C. See patent family annex.

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Date of the actual completion of the international search

08 July 2019 (08.07.2019)

Date of mailing of the international search report

08 July 2019 (08.07.2019)

Name and mailing address of the ISA/KR

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2017/018762

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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