(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau

(43) International Publication Date
14 September 2017 (14.09.2017)

WIPO | PCT

(10) International Publication Number
WO 2017/156254 A1

(72) Inventors: CROUNSE, Kenneth R.; 1000 Technology Park Drive, Billerica, Massachusetts 01821-4165 (US). HOOGEBOOM, Christopher L.; 1000 Technology Park Drive, Billerica, Massachusetts 01821-4165 (US). TELFER, Stephen J.; 1000 Technology Park Drive, Billerica, Massachusetts 01821-4165 (US).

(54) Title: METHODS FOR DRIVING ELECTRO-OPTIC DISPLAYS
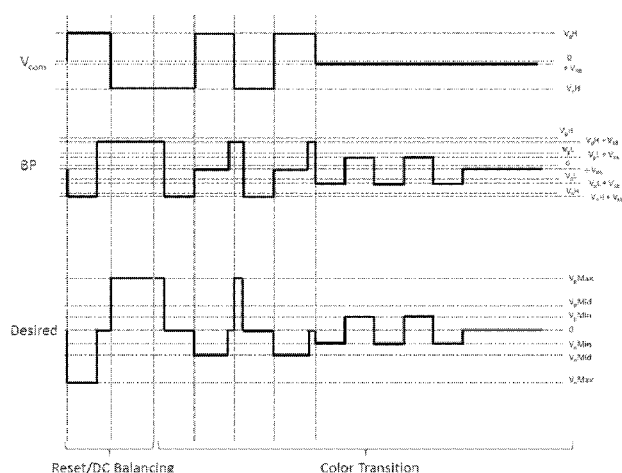
Fig. 11

(57) Abstract: A method for driving an electro-optic display comprising of applying a first driving phase to a display medium of the display. The first driving phase having a first signal and a second signal, the first signal having a first polarity, a first amplitude as a function of time, and a first duration, the second signal succeeding the first signal and having a second polarity opposite to the first polarity, a second amplitude as a function of time, and a second duration, such that the sum of the first amplitude as a function of time integrated over the first duration and the second amplitude as a function of time integrated over the second duration produces a first impulse offset.

METHODS FOR DRIVING ELECTRO-OPTIC DISPLAYS

[Para 1]   This application claims benefit of provisional Application Serial No. 62/305,833 filed March 09, 2016.

[Para 2]   This application is also related to co-pending Application Serial No. 14/849,658, filed September 10, 2015, and claiming benefit of Application Serial No. 62/048,591, filed September 10, 2014; of Application Serial No. 62/169,221, filed June 1, 2015; and of Application Serial No. 62/169,710, filed June 2, 2015. The entire contents of the aforementioned applications and of all U.S. patents and published and copending applications mentioned below are herein incorporated by reference.

[Para 3]   **BACKGROUND OF INVENTION**

[Para 4]   This invention relates to methods for driving electro-optic displays, especially but not exclusively electrophoretic displays capable of rendering more than two colors using a single layer of electrophoretic material comprising a plurality of colored particles.

[Para 5]   The term color as used herein includes black and white. White particles are often of the light scattering type.

[Para 6]   The term gray state is used herein in its conventional meaning in the imaging art to refer to a state intermediate two extreme optical states of a pixel, and does not necessarily imply a black-white transition between these two extreme states. For example, several of the E Ink patents and published applications referred to below describe electrophoretic displays in which the extreme states are white and deep blue, so that an intermediate gray state would actually be pale blue. Indeed, as already mentioned, the change in optical state may not be a color change at all. The terms black and white may be used hereinafter to refer to the two extreme optical states of a display, and should be understood as normally including extreme optical states which are not strictly black and white, for example the aforementioned white and dark blue states.

[Para 7]   The terms bistable and bistability are used herein in their conventional meaning in the art to refer to displays comprising display elements having first and second display states differing in at least one optical property, and such that after any given element has been driven, by means of an addressing pulse of finite duration, to assume either its first or second display state, after the addressing pulse has terminated, that state will persist for at least several times, for example at least four times, the minimum duration of the addressing pulse required to change the state of the display element. It is shown in U.S. Patent No. 7,170,670 that some particle-based electrophoretic displays capable of gray scale are stable not only in

their extreme black and white states but also in their intermediate gray states, and the same is true of some other types of electro-optic displays. This type of display is properly called multi-stable rather than bistable, although for convenience the term bistable may be used herein to cover both bistable and multi-stable displays.

[Para 8] The term impulse, when used to refer to driving an electrophoretic display, is used herein to refer to the integral of the applied voltage with respect to time during the period in which the display is driven.

[Para 9] A particle that absorbs, scatters, or reflects light, either in a broad band or at selected wavelengths, is referred to herein as a colored or pigment particle. Various materials other than pigments (in the strict sense of that term as meaning insoluble colored materials) that absorb or reflect light, such as dyes or photonic crystals, etc., may also be used in the electrophoretic media and displays of the present invention.

[Para 10] Particle-based electrophoretic displays have been the subject of intense research and development for a number of years. In such displays, a plurality of charged particles (sometimes referred to as pigment particles) move through a fluid under the influence of an electric field. Electrophoretic displays can have attributes of good brightness and contrast, wide viewing angles, state bistability, and low power consumption when compared with liquid crystal displays. Nevertheless, problems with the long-term image quality of these displays have prevented their widespread usage. For example, particles that make up electrophoretic displays tend to settle, resulting in inadequate service-life for these displays.

[Para 11] As noted above, electrophoretic media require the presence of a fluid. In most prior art electrophoretic media, this fluid is a liquid, but electrophoretic media can be produced using gaseous fluids; see, for example, Kitamura, T., et al., Electrical toner movement for electronic paper-like display, IDW Japan, 2001, Paper HCS1-1, and Yamaguchi, Y., et al., Toner display using insulative particles charged triboelectrically, IDW Japan, 2001, Paper AMD4-4). See also U.S. Patents Nos. 7,321,459 and 7,236,291. Such gas-based electrophoretic media appear to be susceptible to the same types of problems due to particle settling as liquid-based electrophoretic media, when the media are used in an orientation which permits such settling, for example in a sign where the medium is disposed in a vertical plane. Indeed, particle settling appears to be a more serious problem in gas-based electrophoretic media than in liquid-based ones, since the lower viscosity of gaseous suspending fluids as compared with liquid ones allows more rapid settling of the electrophoretic particles.

[Para 12] Numerous patents and applications assigned to or in the names of the Massachusetts Institute of Technology (MIT) and E Ink Corporation describe various technologies used in encapsulated electrophoretic and other electro-optic media. Such encapsulated media comprise numerous small capsules, each of which itself comprises an internal phase containing electrophoretically-mobile particles in a fluid medium, and a capsule wall surrounding the internal phase. Typically, the capsules are themselves held within a polymeric binder to form a coherent layer positioned between two electrodes. The technologies described in these patents and applications include:

(a)   Electrophoretic particles, fluids and fluid additives; see for example U.S. Patents Nos. 7,002,728 and 7,679,814;

(b)   Capsules, binders and encapsulation processes; see for example U.S. Patents Nos. 6,922,276 and 7,411,719;

(c)   Microcell structures, wall materials, and methods of forming microcells; see for example United States Patents Nos. 7,072,095 and 9,279,906;

(d)   Methods for filling and sealing microcells; see for example United States Patents Nos. 7,144,942 and 7,715,088;

(e)   Films and sub-assemblies containing electro-optic materials; see for example U.S. Patents Nos. 6,982,178 and 7,839,564;

(f)   Backplanes, adhesive layers and other auxiliary layers and methods used in displays; see for example U.S. Patents Nos. 7,116,318 and 7,535,624;

(g)   Color formation color adjustment; see for example U.S. Patents Nos. 6,017,584; 6,545,797; 6,664,944; 6,788,452; 6,864,875; 6,914,714; 6,972,893; 7,038,656; 7,038,670; 7,046,228; 7,052,571; 7,075,502***; 7,167,155; 7,385,751; 7,492,505; 7,667,684; 7,684,108; 7,791,789; 7,800,813; 7,821,702; 7,839,564***; 7,910,175; 7,952,790; 7,956,841; 7,982,941; 8,040,594; 8,054,526; 8,098,418; 8,159,636; 8,213,076; 8,363,299; 8,422,116; 8,441,714; 8,441,716; 8,466,852; 8,503,063; 8,576,470; 8,576,475; 8,593,721; 8,605,354; 8,649,084; 8,670,174; 8,704,756; 8,717,664; 8,786,935; 8,797,634; 8,810,899; 8,830,559; 8,873,129; 8,902,153; 8,902,491; 8,917,439; 8,964,282; 9,013,783; 9,116,412; 9,146,439; 9,164,207; 9,170,467; 9,170,468; 9,182,646; 9,195,111; 9,199,441; 9,268,191; 9,285,649; 9,293,511; 9,341,916; 9,360,733;

9,361,836; 9,383,623; and 9,423,666; and U.S. Patent Applications Publication Nos. 2008/0043318; 2008/0048970; 2009/0225398; 2010/0156780; 2011/0043543; 2012/0326957; 2013/0242378; 2013/0278995; 2014/0055840; 2014/0078576; 2014/0340430; 2014/0340736; 2014/0362213; 2015/0103394; 2015/0118390; 2015/0124345; 2015/0198858; 2015/0234250; 2015/0268531; 2015/0301246; 2016/0011484; 2016/0026062; 2016/0048054; 2016/0116816; 2016/0116818; and 2016/0140909;

(h)   Methods for driving displays; see for example U.S. Patents Nos. 5,930,026; 6,445,489; 6,504,524; 6,512,354; 6,531,997; 6,753,999; 6,825,970; 6,900,851; 6,995,550; 7,012,600; 7,023,420; 7,034,783; 7,061,166; 7,061,662; 7,116,466; 7,119,772; 7,177,066; 7,193,625; 7,202,847; 7,242,514; 7,259,744; 7,304,787; 7,312,794; 7,327,511; 7,408,699; 7,453,445; 7,492,339; 7,528,822; 7,545,358; 7,583,251; 7,602,374; 7,612,760; 7,679,599; 7,679,813; 7,683,606; 7,688,297; 7,729,039; 7,733,311; 7,733,335; 7,787,169; 7,859,742; 7,952,557; 7,956,841; 7,982,479; 7,999,787; 8,077,141; 8,125,501; 8,139,050; 8,174,490; 8,243,013; 8,274,472; 8,289,250; 8,300,006; 8,305,341; 8,314,784; 8,373,649; 8,384,658; 8,456,414; 8,462,102; 8,514,168; 8,537,105; 8,558,783; 8,558,785; 8,558,786; 8,558,855; 8,576,164; 8,576,259; 8,593,396; 8,605,032; 8,643,595; 8,665,206; 8,681,191; 8,730,153; 8,810,525; 8,928,562; 8,928,641; 8,976,444; 9,013,394; 9,019,197; 9,019,198; 9,019,318; 9,082,352; 9,171,508; 9,218,773; 9,224,338; 9,224,342; 9,224,344; 9,230,492; 9,251,736; 9,262,973; 9,269,311; 9,299,294; 9,373,289; 9,390,066; 9,390,661; and 9,412,314; and U.S. Patent Applications Publication Nos. 2003/0102858; 2004/0246562; 2005/0253777; 2007/0091418; 2007/0103427; 2007/0176912; 2008/0024429; 2008/0024482; 2008/0136774; 2008/0291129; 2008/0303780; 2009/0174651; 2009/0195568; 2009/0322721; 2010/0194733; 2010/0194789; 2010/0220121; 2010/0265561; 2010/0283804; 2011/0063314; 2011/0175875; 2011/0193840; 2011/0193841; 2011/0199671; 2011/0221740; 2012/0001957; 2012/0098740; 2013/0063333; 2013/0194250; 2013/0249782; 2013/0321278; 2014/0009817; 2014/0085355; 2014/0204012; 2014/0218277; 2014/0240210; 2014/0240373; 2014/0253425; 2014/0292830; 2014/0293398; 2014/0333685; 2014/0340734; 2015/0070744; 2015/0097877; 2015/0109283; 2015/0213749; 2015/0213765; 2015/0221257; 2015/0262255; 2015/0262551; 2016/0071465; 2016/0078820;

2016/0093253; 2016/0140910; and 2016/0180777 (these patents and applications may hereinafter be referred to as the MEDEOD (MEthods for Driving Electro-optic Displays) applications);

(i)   Applications of displays; see for example U.S. Patents Nos. 7,312,784 and 8,009,348; and

(j)   Non-electrophoretic displays, as described in U.S. Patents Nos. 6,241,921; and U.S. Patent Applications Publication Nos. 2015/0277160; and U.S. Patent Application Publications Nos. 2015/0005720 and 2016/0012710.

[Para 13] Many of the aforementioned patents and applications recognize that the walls surrounding the discrete microcapsules in an encapsulated electrophoretic medium could be replaced by a continuous phase, thus producing a so-called polymer-dispersed electrophoretic display, in which the electrophoretic medium comprises a plurality of discrete droplets of an electrophoretic fluid and a continuous phase of a polymeric material, and that the discrete droplets of electrophoretic fluid within such a polymer-dispersed electrophoretic display may be regarded as capsules or microcapsules even though no discrete capsule membrane is associated with each individual droplet; see for example, U.S. Patent No. 6,866,760. Accordingly, for purposes of the present application, such polymer-dispersed electrophoretic media are regarded as sub-species of encapsulated electrophoretic media.

[Para 14] A related type of electrophoretic display is a so-called microcell electrophoretic display. In a microcell electrophoretic display, the charged particles and the fluid are not encapsulated within microcapsules but instead are retained within a plurality of cavities formed within a carrier medium, typically a polymeric film. See, for example, U.S. Patents Nos. 6,672,921 and 6,788,449, both assigned to Sipix Imaging, Inc.

[Para 15] Although electrophoretic media are often opaque (since, for example, in many electrophoretic media, the particles substantially block transmission of visible light through the display) and operate in a reflective mode, many electrophoretic displays can be made to operate in a so-called shutter mode in which one display state is substantially opaque and one is light-transmissive. See, for example, U.S. Patents Nos. 5,872,552; 6,130,774; 6,144,361; 6,172,798; 6,271,823; 6,225,971; and 6,184,856. Dielectrophoretic displays, which are similar to electrophoretic displays but rely upon variations in electric field strength, can operate in a similar mode; see U.S. Patent No. 4,418,346. Other types of electro-optic displays may also be capable of operating in shutter mode. Electro-optic media operating in shutter mode can be used in multi-layer structures for full color displays; in such structures, at

least one layer adjacent the viewing surface of the display operates in shutter mode to expose or conceal a second layer more distant from the viewing surface.

[Para 16] An encapsulated electrophoretic display typically does not suffer from the clustering and settling failure mode of traditional electrophoretic devices and provides further advantages, such as the ability to print or coat the display on a wide variety of flexible and rigid substrates. (Use of the word printing is intended to include all forms of printing and coating, including, but without limitation: pre-metered coatings such as patch die coating, slot or extrusion coating, slide or cascade coating, curtain coating; roll coating such as knife over roll coating, forward and reverse roll coating; gravure coating; dip coating; spray coating; meniscus coating; spin coating; brush coating; air knife coating; silk screen printing processes; electrostatic printing processes; thermal printing processes; ink jet printing processes; electrophoretic deposition (See U.S. Patent No. 7,339,715); and other similar techniques.) Thus, the resulting display can be flexible. Further, because the display medium can be printed (using a variety of methods), the display itself can be made inexpensively.

[Para 17] As indicated above most simple prior art electrophoretic media essentially display only two colors. Such electrophoretic media either use a single type of electrophoretic particle having a first color in a colored fluid having a second, different color (in which case, the first color is displayed when the particles lie adjacent the viewing surface of the display and the second color is displayed when the particles are spaced from the viewing surface), or first and second types of electrophoretic particles having differing first and second colors in an uncolored fluid (in which case, the first color is displayed when the first type of particles lie adjacent the viewing surface of the display and the second color is displayed when the second type of particles lie adjacent the viewing surface). Typically the two colors are black and white. If a full color display is desired, a color filter array may be deposited over the viewing surface of the monochrome (black and white) display. Displays with color filter arrays rely on area sharing and color blending to create color stimuli. The available display area is shared between three or four primary colors such as red/green/blue (RGB) or red/green/blue/white (RGBW), and the filters can be arranged in one-dimensional (stripe) or two-dimensional (2x2) repeat patterns. Other choices of primary colors or more than three primaries are also known in the art. The three (in the case of RGB displays) or four (in the case of RGBW displays) sub-pixels are chosen small enough so that at the intended viewing distance they visually blend together to a single pixel with a uniform color stimulus ('color blending'). The inherent disadvantage of area sharing is that the colorants are always present,

and colors can only be modulated by switching the corresponding pixels of the underlying monochrome display to white or black (switching the corresponding primary colors on or off). For example, in an ideal RGBW display, each of the red, green, blue and white primaries occupy one fourth of the display area (one sub-pixel out of four), with the white sub-pixel being as bright as the underlying monochrome display white, and each of the colored sub-pixels being no lighter than one third of the monochrome display white. The brightness of the white color shown by the display as a whole cannot be more than one half of the brightness of the white sub-pixel (white areas of the display are produced by displaying the one white sub-pixel out of each four, plus each colored sub-pixel in its colored form being equivalent to one third of a white sub-pixel, so the three colored sub-pixels combined contribute no more than the one white sub-pixel). The brightness and saturation of colors is lowered by area-sharing with color pixels switched to black. Area sharing is especially problematic when mixing yellow because it is lighter than any other color of equal brightness, and saturated yellow is almost as bright as white. Switching the blue pixels (one fourth of the display area) to black makes the yellow too dark.

[Para 18] Multilayer, stacked electrophoretic displays are known in the art; see, for example, J. Heikenfeld, P. Drzaic, J-S Yeo and T. Koch, Journal of the SID, 19(2), 2011, pp. 129-156. In such displays, ambient light passes through images in each of the three subtractive primary colors, in precise analogy with conventional color printing. U.S. Patent No. 6,727,873 describes a stacked electrophoretic display in which three layers of switchable cells are placed over a reflective background. Similar displays are known in which colored particles are moved laterally (see International Application No. WO 2008/065605) or, using a combination of vertical and lateral motion, sequestered into microcells. In both cases, each layer is provided with electrodes that serve to concentrate or disperse the colored particles on a pixel-by-pixel basis, so that each of the three layers requires a layer of thin-film transistors (TFT's) (two of the three layers of TFT's must be substantially transparent) and a light-transmissive counter-electrode. Such a complex arrangement of electrodes is costly to manufacture, and in the present state of the art it is difficult to provide an adequately transparent plane of pixel electrodes, especially as the white state of the display must be viewed through several layers of electrodes. Multi-layer displays also suffer from parallax problems as the thickness of the display stack approaches or exceeds the pixel size.

[Para 19] U.S. Applications Publication Nos. 2012/0008188 and 2012/0134009 describe multicolor electrophoretic displays having a single back plane comprising independently

addressable pixel electrodes and a common, light-transmissive front electrode. Between the back plane and the front electrode is disposed a plurality of electrophoretic layers. Displays described in these applications are capable of rendering any of the primary colors (red, green, blue, cyan, magenta, yellow, white and black) at any pixel location. However, there are disadvantages to the use of multiple electrophoretic layers located between a single set of addressing electrodes. The electric field experienced by the particles in a particular layer is lower than would be the case for a single electrophoretic layer addressed with the same voltage. In addition, optical losses in an electrophoretic layer closest to the viewing surface (for example, caused by light scattering or unwanted absorption) may affect the appearance of images formed in underlying electrophoretic layers.

[Para 20] Attempts have been made to provide full-color electrophoretic displays using a single electrophoretic layer. For example, U.S. Patent Application Publication No. 2013/0208338 describes a color display comprising an electrophoretic fluid which comprises one or two types of pigment particles dispersed in a clear and colorless or colored solvent, the electrophoretic fluid being disposed between a common electrode and a plurality of pixel or driving electrodes. The driving electrodes are arranged to expose a background layer. U.S. Patent Application Publication No. 2014/0177031 describes a method for driving a display cell filled with an electrophoretic fluid comprising two types of charged particles carrying opposite charge polarities and of two contrast colors. The two types of pigment particles are dispersed in a colored solvent or in a solvent with non-charged or slightly charged colored particles dispersed therein. The method comprises driving the display cell to display the color of the solvent or the color of the non-charged or slightly charged colored particles by applying a driving voltage which is about 1 to about 20% of the full driving voltage. U.S. Patent Application Publication No. 2014/0092465 and 2014/0092466 describe an electrophoretic fluid, and a method for driving an electrophoretic display. The fluid comprises first, second and third type of pigment particles, all of which are dispersed in a solvent or solvent mixture. The first and second types of pigment particles carry opposite charge polarities, and the third type of pigment particles has a charge level being less than about 50% of the charge level of the first or second type. The three types of pigment particles have different levels of threshold voltage, or different levels of mobility, or both. None of these patent applications disclose full color display in the sense in which that term is used below.

[Para 21] U.S. Patent Application Publication No. 2007/0031031 describes an image processing device for processing image data in order to display an image on a display medium in which each pixel is capable of displaying white, black and one other color. U.S. Patent Applications Publication Nos. 2008/0151355; 2010/0188732; and 2011/0279885 describe a color display in which mobile particles move through a porous structure. U.S. Patent Applications Publication Nos. 2008/0303779 and 2010/0020384 describe a display medium comprising first, second and third particles of differing colors. The first and second particles can form aggregates, and the smaller third particles can move through apertures left between the aggregated first and second particles. U.S. Patent Application Publication No. 2011/0134506 describes a display device including an electrophoretic display element including plural types of particles enclosed between a pair of substrates, at least one of the substrates being translucent and each of the respective plural types of particles being charged with the same polarity, differing in optical properties, and differing in either in migration speed and/or electric field threshold value for moving, a translucent display-side electrode provided at the substrate side where the translucent substrate is disposed, a first back-side electrode provided at the side of the other substrate, facing the display-side electrode, and a second back-side electrode provided at the side of the other substrate, facing the display-side electrode; and a voltage control section that controls the voltages applied to the display-side electrode, the first back-side electrode, and the second back-side electrode, such that the types of particles having the fastest migration speed from the plural types of particles, or the types of particles having the lowest threshold value from the plural types of particles, are moved, in sequence by each of the different types of particles, to the first back-side electrode or to the second back-side electrode, and then the particles that moved to the first back-side electrode are moved to the display-side electrode. U.S. Patent Applications Publication Nos. 2011/0175939; 2011/0298835; 2012/0327504; and 2012/0139966 describe color displays which rely upon aggregation of multiple particles and threshold voltages. U.S. Patent Application Publication No. 2013/0222884 describes an electrophoretic particle, which contains a colored particle containing a charged group-containing polymer and a coloring agent, and a branched silicone-based polymer being attached to the colored particle and containing, as copolymerization components, a reactive monomer and at least one monomer selected from a specific group of monomers. U.S. Patent Application Publication No. 2013/0222885 describes a dispersion liquid for an electrophoretic display containing a dispersion medium, a colored electrophoretic particle group dispersed in the dispersion

medium and migrates in an electric field, a non-electrophoretic particle group which does not migrate and has a color different from that of the electrophoretic particle group, and a compound having a neutral polar group and a hydrophobic group, which is contained in the dispersion medium in a ratio of about 0.01 to about 1 mass % based on the entire dispersion liquid. U.S. Patent Application Publication No. 2013/0222886 describes a dispersion liquid for a display including floating particles containing: core particles including a colorant and a hydrophilic resin; and a shell covering a surface of each of the core particles and containing a hydrophobic resin with a difference in a solubility parameter of 7.95 $(J/cm^3)^{1/2}$ or more. U.S. Patent Applications Publication Nos. 2013/0222887 and 2013/0222888 describe an electrophoretic particle having specified chemical compositions. Finally, U.S. Patent Application Publication No. 2014/0104675 describes a particle dispersion including first and second colored particles that move in response to an electric field, and a dispersion medium, the second colored particles having a larger diameter than the first colored particles and the same charging characteristic as a charging characteristic of the first color particles, and in which the ratio (Cs/Cl) of the charge amount Cs of the first colored particles to the charge amount Cl of the second colored particles per unit area of the display is less than or equal to 5. Some of the aforementioned displays do provide full color but at the cost of requiring addressing methods that are long and cumbersome.

[Para 22] U.S. Patent Applications Publication Nos. 2012/0314273 and 2014/0002889 describe an electrophoresis device including a plurality of first and second electrophoretic particles included in an insulating liquid, the first and second particles having different charging characteristics that are different from each other; the device further comprising a porous layer included in the insulating liquid and formed of a fibrous structure. These patent applications are not full color displays in the sense in which that term is used below.

[Para 23] See also U.S. Patent Application Publication No. 2011/0134506 and the aforementioned Application Serial No. 14/277,107; the latter describes a full color display using three different types of particles in a colored fluid, but the presence of the colored fluid limits the quality of the white state which can be achieved by the display.

[Para 24] To obtain a high-resolution display, individual pixels of a display must be addressable without interference from adjacent pixels. One way to achieve this objective is to provide an array of non-linear elements, such as transistors or diodes, with at least one non-linear element associated with each pixel, to produce an "active matrix" display. An addressing or pixel electrode, which addresses one pixel, is connected to an appropriate

voltage source through the associated non-linear element. Typically, when the non-linear element is a transistor, the pixel electrode is connected to the drain of the transistor, and this arrangement will be assumed in the following description, although it is essentially arbitrary and the pixel electrode could be connected to the source of the transistor. Conventionally, in high resolution arrays, the pixels are arranged in a two-dimensional array of rows and columns, such that any specific pixel is uniquely defined by the intersection of one specified row and one specified column. The sources of all the transistors in each column are connected to a single column electrode, while the gates of all the transistors in each row are connected to a single row electrode; again the assignment of sources to rows and gates to columns is conventional but essentially arbitrary, and could be reversed if desired. The row electrodes are connected to a row driver, which essentially ensures that at any given moment only one row is selected, i.e., that there is applied to the selected row electrode a select voltage such as to ensure that all the transistors in the selected row are conductive, while there is applied to all other rows a non-select voltage such as to ensure that all the transistors in these non-selected rows remain non-conductive. The column electrodes are connected to column drivers, which place upon the various column electrodes voltages selected to drive the pixels in the selected row to their desired optical states. (The aforementioned voltages are relative to a common front electrode which is conventionally provided on the opposed side of the electro-optic medium from the non-linear array and extends across the whole display.) After a pre-selected interval known as the "line address time" the selected row is deselected, the next row is selected, and the voltages on the column drivers are changed so that the next line of the display is written. This process is repeated so that the entire display is written in a row-by-row manner.

[Para 25] Conventionally, each pixel electrode has associated therewith a capacitor electrode such that the pixel electrode and the capacitor electrode form a capacitor; see, for example, International Patent Application WO 01/07961. In some embodiments, N-type semiconductor (e.g., amorphous silicon) may be used to from the transistors and the "select" and "non-select" voltages applied to the gate electrodes can be positive and negative, respectively.

[Para 26]   Figure 10 of the accompanying drawings depicts an exemplary equivalent circuit of a single pixel of an electrophoretic display. As illustrated, the circuit includes a capacitor 10 formed between a pixel electrode and a capacitor electrode. The electrophoretic medium 20 is represented as a capacitor and a resistor in parallel. In some instances, direct or

indirect coupling capacitance 30 between the gate electrode of the transistor associated with the pixel and the pixel electrode (usually referred to a as a "parasitic capacitance") may create unwanted noise to the display. Usually, the parasitic capacitance 30 is much smaller than that of the storage capacitor 10, and when the pixel rows of a display is being selected or deselected, the parasitic capacitance 30 may result in a small negative offset voltage to the pixel electrode, also known as a "kickback voltage", which is usually less than 2 volts. In some embodiments, to compensate for the unwanted "kickback voltage", a common potential V_{com}, may be supplied to the top plane electrode and the capacitor electrode associated with each pixel, such that, when V_{com} is set to a value equal to the kickback voltage (V_{KB}), every voltage supplied to the display may be offset by the same amount, and no net DC-imbalance experienced.

[Para 27] Problems may arise, however, when V_{com} is set to a voltage that is not compensated for the kickback voltage. This may occur when it is desired to apply a higher voltage to the display than is available from the backplane alone. It is well-known in the art that, for example, the maximum voltage applied to the display may be doubled if the backplane is supplied with a choice of a nominal +V, 0, or -V, for example, while V_{com} is supplied with –V. The maximum voltage experienced in this case is +2V (i.e., at the backplane relative to the top plane), while the minimum is zero. If negative voltages are needed, the V_{com} potential must be raised at least to zero. Waveforms used to address a display with positive and negative voltages using top plane switching must therefore have particular frames allocated to each of more than one V_{com} voltage setting.

[Para 28] When (as described above) V_{com} is deliberately set to V_{KB}, a separate power supply may be used. It is costly and inconvenient, however, to use as many separate power supplies as there are V_{com} settings when top plane switching is used. Therefore, there is a need for methods to compensate for the DC-offset caused by the kickback voltage using the same power supply for the back plane and V_{com}.

[Para 29] **SUMMARY OF INVENTION**

[Para 30] Accordingly, this invention provides a method of driving an electro-optic display which is DC balanced despite the existence of kickback voltages and changes in the voltages applied to the front electrode.

[Para 31] Accordingly, in one aspect, this invention provides a method for driving an electro-optic display having a front electrode, a backplane and a display medium positioned between the front electrode and the backplane. The method including applying a first driving

phase to the display medium, the first driving phase having a first signal and a second signal, the first signal having a first polarity, a first amplitude as a function of time, and a first duration, the second signal succeeding the first signal and having a second polarity opposite to the first polarity, a second amplitude as a function of time, and a second duration, such that the sum of the first amplitude as a function of time integrated over the first duration and the second amplitude as a function of time integrated over the second duration produces a first impulse offset. The method further including applying a second driving phase to the display medium, the second driving phase produces a second impulse offset, where the sum of the first and second impulse offset is substantially zero.

[Para 32] In some other aspects, this invention also provides for a method for driving an electro-optic display having a front electrode, a backplane, and a display medium positioned between the front electrode and the backplane, the method including applying a reset phase and a color transition phase to the display. Where the reset phase including applying a first signal having a first polarity, a first amplitude as a function of time, and a first duration on the front electrode, applying a second signal having a second polarity opposite the first polarity, a second amplitude as a function of time, and a second duration during the first duration on the backplane; applying a third signal having the second polarity, a third amplitude as a function of time, and a third duration preceded by the first duration on the front electrode; applying a fourth signal having the first polarity, a fourth amplitude as a function of time, and a fourth duration preceded by the second duration on the backplane. Where the sum of the first amplitude as a function of time integrated over the first duration, and the second amplitude as a function of time integrated over the second duration, and the third amplitude as a function of time integrated over the third duration, and the fourth amplitude as a function of time integrated over the fourth duration produces an impulse offset designed to maintain a DC-balance on the display medium over the reset phase and the color transition phase.

[Para 33] The electrophoretic media used in the display of the present invention may be any of those described in the aforementioned Application Serial No. 14/849,658. Such media comprise a light-scattering particle, typically white, and three substantially non-light-scattering particles. The electrophoretic medium of the present invention may be in any of the forms discussed above. Thus, the electrophoretic medium may be unencapsulated, encapsulated in discrete capsules surrounded by capsule walls, or in the form of a polymer-dispersed or microcell medium.

[Para 34] **BRIEF DESCRIPTION OF DRAWINGS**

[Para 35] Figure 1 of the accompanying drawings is a schematic cross-section showing the positions of the various particles in an electrophoretic medium of the present invention when displaying black, white, the three subtractive primary and the three additive primary colors.

[Para 36] Figure 2 shows in schematic form the four types of pigment particle used in the present invention;

[Para 37] Figure 3 shows in schematic form the relative strengths of interactions between pairs of particles of the present invention;

[Para 38] Figure 4 shows in schematic form behavior of particles of the present invention when subjected to electric fields of varying strength and duration;

[Para 39] Figures 5A and 5B show waveforms used to drive the electrophoretic medium shown in Figure 1 to its black and white states respectively.

[Para 40] Figures 6A and 6B show waveforms used to drive the electrophoretic medium shown in Figure 1 to its magenta and blue states.

[Para 41] Figures 6C and 6D show waveforms used to drive the electrophoretic medium shown in Figure 1 to its yellow and green states.

[Para 42] Figures 7A and 7B show waveforms used to drive the electrophoretic medium shown in Figure 1 to its red and cyan states respectively.

[Para 43] Figures 8-9 illustrate waveforms which may be used in place of those shown in Figures 5A-5B, 6A-6D and 7A-7B to drive the electrophoretic medium shown in Figure 1 to all its color states.

[Para 44] Figure 10, as already mentioned, illustrates an exemplary equivalent circuit of a single pixel of an electrophoretic display.

[Para 45] Figure 11 is a schematic voltage against time diagram showing the variation with time of the front and pixel electrodes, and the resultant voltage across the electrophoretic medium, of a waveform used to generate one color in a drive scheme of the present invention.

[Para 46] Figure 12 is a schematic voltage against time diagram showing the variation with time of the front and pixel electrodes of the reset phase of the waveform shown in Figure 11, and also shows various parameters used in DC balance calculations described below.

[Para 47] Figure 13 is another schematic voltage against time diagram showing various parameters used in a DC balanced driving waveform.

[Para 48] DETAILED DESCRIPTION

[Para 49] As indicated above, the present invention may be used with an electrophoretic medium which comprises one light-scattering particle (typically white) and three other particles providing the three subtractive primary colors.

[Para 50] The three particles providing the three subtractive primary colors may be substantially non-light-scattering ("SNLS"). The use of SNLS particles allows mixing of colors and provides for more color outcomes than can be achieved with the same number of scattering particles. The aforementioned US 2012/0327504 uses particles having subtractive primary colors, but requires two different voltage thresholds for independent addressing of the non-white particles (i.e., the display is addressed with three positive and three negative voltages). These thresholds must be sufficiently separated for avoidance of cross-talk, and this separation necessitates the use of high addressing voltages for some colors. In addition, addressing the colored particle with the highest threshold also moves all the other colored par

[Para 51] Particles, and these other particles must subsequently be switched to their desired positions at lower voltages. Such a step-wise color-addressing scheme produces flashing of unwanted colors and a long transition time. The present invention does not require the use of a such a stepwise waveform and addressing to all colors can, as described below, be achieved with only two positive and two negative voltages (i.e., only five different voltages, two positive, two negative and zero are required in a display, although as described below in certain embodiments it may be preferred to use more different voltages to address the display).

[Para 52] As already mentioned, Figure 1 of the accompanying drawings is a schematic cross-section showing the positions of the various particles in an electrophoretic medium of the present invention when displaying black, white, the three subtractive primary and the three additive primary colors. In Figure 1, it is assumed that the viewing surface of the display is at the top (as illustrated), i.e., a user views the display from this direction, and light is incident from this direction. As already noted, in preferred embodiments only one of the four particles used in the electrophoretic medium of the present invention substantially scatters light, and in Figure 1 this particle is assumed to be the white pigment. Basically, this light-scattering white particle forms a white reflector against which any particles above the white particles (as illustrated in Figure 1) are viewed. Light entering the viewing surface of the display passes through these particles, is reflected from the white particles, passes back through these particles and emerges from the display. Thus, the particles above the white

particles may absorb various colors and the color appearing to the user is that resulting from the combination of particles above the white particles. Any particles disposed below (behind from the user's point of view) the white particles are masked by the white particles and do not affect the color displayed. Because the second, third and fourth particles are substantially non-light-scattering, their order or arrangement relative to each other is unimportant, but for reasons already stated, their order or arrangement with respect to the white (light-scattering) particles is critical.

[Para 53] More specifically, when the cyan, magenta and yellow particles lie below the white particles (Situation [A] in Figure 1), there are no particles above the white particles and the pixel simply displays a white color. When a single particle is above the white particles, the color of that single particle is displayed, yellow, magenta and cyan in Situations [B], [D] and [F] respectively in Figure 1. When two particles lie above the white particles, the color displayed is a combination of those of these two particles; in Figure 1, in Situation [C], magenta and yellow particles display a red color, in Situation [E], cyan and magenta particles display a blue color, and in Situation [G], yellow and cyan particles display a green color. Finally, when all three colored particles lie above the white particles (Situation [H] in Figure 1), all the incoming light is absorbed by the three subtractive primary colored particles and the pixel displays a black color.

[Para 54] It is possible that one subtractive primary color could be rendered by a particle that scatters light, so that the display would comprise two types of light-scattering particle, one of which would be white and another colored. In this case, however, the position of the light-scattering colored particle with respect to the other colored particles overlying the white particle would be important. For example, in rendering the color black (when all three colored particles lie over the white particles) the scattering colored particle cannot lie over the non-scattering colored particles (otherwise they will be partially or completely hidden behind the scattering particle and the color rendered will be that of the scattering colored particle, not black).

[Para 55] It would not be easy to render the color black if more than one type of colored particle scattered light.

[Para 56] Figure 1 shows an idealized situation in which the colors are uncontaminated (i.e., the light-scattering white particles completely mask any particles lying behind the white particles). In practice, the masking by the white particles may be imperfect so that there may be some small absorption of light by a particle that ideally would be completely masked.

Such contamination typically reduces both the lightness and the chroma of the color being rendered. In the electrophoretic medium of the present invention, such color contamination should be minimized to the point that the colors formed are commensurate with an industry standard for color rendition. A particularly favored standard is SNAP (the standard for newspaper advertising production), which specifies L^*, a^* and b^* values for each of the eight primary colors referred to above. (Hereinafter, "primary colors" will be used to refer to the eight colors, black, white, the three subtractive primaries and the three additive primaries as shown in Figure 1.)

[Para 57] Methods for electrophoretically arranging a plurality of different colored particles in "layers" as shown in Figure 1 have been described in the prior art. The simplest of such methods involves "racing" pigments having different electrophoretic mobilities; see for example U.S. Patent No. 8,040,594. Such a race is more complex than might at first be appreciated, since the motion of charged pigments itself changes the electric fields experienced locally within the electrophoretic fluid. For example, as positively-charged particles move towards the cathode and negatively-charged particles towards the anode, their charges screen the electric field experienced by charged particles midway between the two electrodes. It is thought that, while pigment racing is involved in the electrophoretic of the present invention, it is not the sole phenomenon responsible for the arrangements of particles illustrated in Figure 1.

[Para 58] A second phenomenon that may be employed to control the motion of a plurality of particles is hetero-aggregation between different pigment types; see, for example, the aforementioned US 2014/0092465. Such aggregation may be charge-mediated (Coulombic) or may arise as a result of, for example, hydrogen bonding or Van der Waals interactions. The strength of the interaction may be influenced by choice of surface treatment of the pigment particles. For example, Coulombic interactions may be weakened when the closest distance of approach of oppositely-charged particles is maximized by a steric barrier (typically a polymer grafted or adsorbed to the surface of one or both particles). In the present invention, as mentioned above, such polymeric barriers are used on the first, and second types of particles and may or may not be used on the third and fourth types of particles.

[Para 59] A third phenomenon that may be exploited to control the motion of a plurality of particles is voltage- or current-dependent mobility, as described in detail in the aforementioned Application Serial No. 14/277,107.

[Para 60] Figure 2 shows schematic cross-sectional representations of the four pigment types (1-4) used in preferred embodiments of the invention. The polymer shell adsorbed to the core pigment is indicated by the dark shading, while the core pigment itself is shown as unshaded. A wide variety of forms may be used for the core pigment: spherical, acicular or otherwise anisometric, aggregates of smaller particles (i.e., "grape clusters"), composite particles comprising small pigment particles or dyes dispersed in a binder, and so on as is well known in the art. The polymer shell may be a covalently-bonded polymer made by grafting processes or chemisorption as is well known in the art, or may be physisorbed onto the particle surface. For example, the polymer may be a block copolymer comprising insoluble and soluble segments. Some methods for affixing the polymer shell to the core pigments are described in the Examples below.

[Para 61] First and second particle types in one embodiment of the invention preferably have a more substantial polymer shell than third and fourth particle types. The light-scattering white particle is of the first or second type (either negatively or positively charged). In the discussion that follows it is assumed that the white particle bears a negative charge (i.e., is of Type 1), but it will be clear to those skilled in the art that the general principles described will apply to a set of particles in which the white particles are positively charged.

[Para 62] In the present invention the electric field required to separate an aggregate formed from mixtures of particles of types 3 and 4 in the suspending solvent containing a charge control agent is greater than that required to separate aggregates formed from any other combination of two types of particle. The electric field required to separate aggregates formed between the first and second types of particle is, on the other hand, less than that required to separate aggregates formed between the first and fourth particles or the second and third particles (and of course less than that required to separate the third and fourth particles).

[Para 63] In Figure 2 the core pigments comprising the particles are shown as having approximately the same size, and the zeta potential of each particle, although not shown, is assumed to be approximately the same. What varies is the thickness of the polymer shell surrounding each core pigment. As shown in Figure 2, this polymer shell is thicker for particles of types 1 and 2 than for particles of types 3 and 4 – and this is in fact a preferred situation for certain embodiments of the invention.

[Para 64] In order to understand how the thickness of the polymer shell affects the electric field required to separate aggregates of oppositely-charged particles, it may be helpful to

consider the force balance between particle pairs. In practice, aggregates may be composed of a great number of particles and the situation will be far more complex than is the case for simple pairwise interactions. Nevertheless, the particle pair analysis does provide some guidance for understanding of the present invention.

[Para 65] The force acting on one of the particles of a pair in an electric field is given by:

$$\vec{F}_{Total} = \vec{F}_{App} + \vec{F}_C + \vec{F}_{VW} + \vec{F}_D \qquad (1)$$

Where F_{App} is the force exerted on the particle by the applied electric field, F_C is the Coulombic force exerted on the particle by the second particle of opposite charge, F_{VW} is the attractive Van der Waals force exerted on one particle by the second particle, and F_D is the attractive force exerted by depletion flocculation on the particle pair as a result of (optional) inclusion of a stabilizing polymer into the suspending solvent.

[Para 66] The force F_{App} exerted on a particle by the applied electric field is given by:

$$\vec{F}_{App} = q\vec{E} = 4\pi\varepsilon_r\varepsilon_0(a+s)\varsigma\,\vec{E} \qquad (2)$$

where q is the charge of the particle, which is related to the zeta potential (ς) as shown in equation (2) (approximately, in the Huckel limit), where a is the core pigment radius, s is the thickness of the solvent-swollen polymer shell, and the other symbols have their conventional meanings as known in the art.

[Para 67] The magnitude of the force exerted on one particle by another as a result of Coulombic interactions is given approximately by:

$$F_C = \frac{4\pi\varepsilon_r\varepsilon_0(a_1+s_1)(a_2+s_2)\varsigma_1\varsigma_2}{(a_1+s_1+a_2+s_2)^2} \qquad (3)$$

for particles 1 and 2.

[Para 68] Note that the F_{App} forces applied to each particle act to separate the particles, while the other three forces are attractive between the particles. If the F_{App} force acting on one particle is higher than that acting on the other (because the charge on one particle is higher than that on the other) according to Newton's third law, the force acting to separate the pair is given by the weaker of the two F_{App} forces.

[Para 69] It can be seen from (2) and (3) that the magnitude of the difference between the attracting and separating Coulombic terms is given by:

$$F_{App} - F_C = 4\pi\varepsilon_r\varepsilon_0((a+s)\varsigma\,|\vec{E}|-\varsigma^2) \qquad (4)$$

if the particles are of equal radius and zeta potential, so making (*a*+*s*) smaller or ζ larger will make the particles more difficult to separate. Thus, in one embodiment of the invention it is preferred that particles of types 1 and 2 be large, and have a relatively low zeta potential, while particles 3 and 4 be small, and have a relatively large zeta potential.

[Para 70] However, the Van der Waals forces between the particles may also change substantially if the thickness of the polymer shell increases. The polymer shell on the particles is swollen by the solvent and moves the surfaces of the core pigments that interact through Van der Waals forces further apart. For spherical core pigments with radii (a_1, a_2) much larger than the distance between them (s_1+s_2),

$$F_{VW} = \frac{A a_1 a_2}{6(a_1 + a_2)(s_1 + s_2)^2} \qquad (5)$$

where *A* is the Hamaker constant. As the distance between the core pigments increases the expression becomes more complex, but the effect remains the same: increasing s_1 or s_2 has a significant effect on reducing the attractive Van der Waals interaction between the particles.

[Para 71] With this background it becomes possible to understand the rationale behind the particle types illustrated in Figure 2. Particles of types 1 and 2 have substantial polymeric shells that are swollen by the solvent, moving the core pigments further apart and reducing the Van der Waals interactions between them more than is possible for particles of types 3 and 4, which have smaller or no polymer shells. Even if the particles have approximately the same size and magnitude of zeta potential, according to the invention it will be possible to arrange the strengths of the interactions between pairwise aggregates to accord with the requirements set out above.

[Para 72] For fuller details of preferred particles for use in the display of Figure 2, the reader is referred to the aforementioned Application Serial No. 14/849,658.

[Para 73] Figure 3 shows in schematic form the strengths of the electric fields required to separate pairwise aggregates of the particle types of the invention. The interaction between particles of types 3 and 4 is stronger than that between particles of types 2 and 3. The interaction between particles of types 2 and 3 is about equal to that between particles of types 1 and 4 and stronger than that between particles of types 1 and 2. All interactions between pairs of particles of the same sign of charge as weak as or weaker than the interaction between particles of types 1 and 2.

[Para 74] Figure 4 shows how these interactions may be exploited to make all the primary colors (subtractive, additive, black and white), as was discussed generally with reference to Figure 1.

[Para 75] When addressed with a low electric field (Figure 4(A)), particles 3 and 4 are aggregated and not separated. Particles 1 and 2 are free to move in the field. If particle 1 is the white particle, the color seen viewing from the left is white, and from the right is black. Reversing the polarity of the field switches between black and white states. The transient colors between black and white states, however, are colored. The aggregate of particles 3 and 4 will move very slowly in the field relative to particles 1 and 2. Conditions may be found where particle 2 has moved past particle 1 (to the left) while the aggregate of particles 3 and 4 has not moved appreciably. In this case particle 2 will be seen viewing from the left while the aggregate of particles 3 and 4 will be seen viewing from the right. As is shown in the Examples below, in certain embodiments of the invention the aggregate of particles 3 and 4 is weakly positively charged, and is therefore positioned in the vicinity of particle 2 at the beginning of such a transition.

[Para 76] When addressed with a high electric field (Figure 4(B)), particles 3 and 4 are separated. Which of particles 1 and 3 (each of which has a negative charge) is visible when viewed from the left will depend upon the waveform (see below). As illustrated, particle 3 is visible from the left and the combination of particles 2 and 4 is visible from the right.

[Para 77] Starting from the state shown in Figure 4(B), a low voltage of opposite polarity will move positively charged particles to the left and negatively charged particles to the right. However, the positively charged particle 4 will encounter the negatively charged particle 1, and the negatively charged particle 3 will encounter the positively charged particle 2. The result is that the combination of particles 2 and 3 will be seen viewing from the left and particle 4 viewing from the right.

[Para 78] As described above, preferably particle 1 is white, particle 2 is cyan, particle 3 is yellow and particle 4 is magenta.

[Para 79] The core pigment used in the white particle is typically a metal oxide of high refractive index as is well known in the art of electrophoretic displays. Examples of white pigments are described in the Examples below.

[Para 80] The core pigments used to make particles of types 2-4, as described above, provide the three subtractive primary colors: cyan, magenta and yellow.

[Para 81] A display device may be constructed using an electrophoretic fluid of the invention in several ways that are known in the prior art. The electrophoretic fluid may be encapsulated in microcapsules or incorporated into microcell structures that are thereafter sealed with a polymeric layer. The microcapsule or microcell layers may be coated or embossed onto a plastic substrate or film bearing a transparent coating of an electrically conductive material. This assembly may be laminated to a backplane bearing pixel electrodes using an electrically conductive adhesive.

[Para 82] A first embodiment of waveforms used to achieve each of the particle arrangements shown in Figure 1 will now be described with reference to Figures 5-7. Hereinafter this method of driving will be referred to as the "first drive scheme" of the invention. In this discussion it is assumed that the first particles are white and negatively charged, the second particles cyan and positively charged, the third particles yellow and negatively charged, and the fourth particles magenta and positively charged. Those skilled in the art will understand how the color transitions will change if these assignments of particle colors are changed, as they can be provided that one of the first and second particles is white. Similarly, the polarities of the charges on all the particles can be inverted and the electrophoretic medium will still function in the same manner provided that the polarity of the waveforms (see next paragraph) used to drive the medium is similarly inverted.

[Para 83] In the discussion that follows, the waveform (voltage against time curve) applied to the pixel electrode of the backplane of a display of the invention is described and plotted, while the front electrode is assumed to be grounded (i.e., at zero potential). The electric field experienced by the electrophoretic medium is of course determined by the difference in potential between the backplane and the front electrode and the distance separating them. The display is typically viewed through its front electrode, so that it is the particles adjacent the front electrode which control the color displayed by the pixel, and if it is sometimes easier to understand the optical transitions involved if the potential of the front electrode relative to the backplane is considered; this can be done simply by inverting the waveforms discussed below.

[Para 84] These waveforms require that each pixel of the display can be driven at five different addressing voltages, designated $+V_{high}$, $+V_{low}$, 0, $-V_{low}$ and $-V_{high}$, illustrated as 30V, 15V, 0, -15V and -30V in Figures 5-7. In practice it may be preferred to use a larger number of addressing voltages. If only three voltages are available (i.e., $+V_{high}$, 0, and $-V_{high}$) it may

-22-

be possible to achieve the same result as addressing at a lower voltage (say, V_{high}/n where n is a positive integer > 1) by addressing with pulses of voltage V_{high} but with a duty cycle of $1/n$.

[Para 85] Waveforms used in the present invention may comprise three phases: a DC-balancing phase, in which a DC imbalance due to previous waveforms applied to the pixel is corrected, or in which the DC imbalance to be incurred in the subsequent color rendering transition is corrected (as is known in the art), a "reset" phase, in which the pixel is returned to a starting configuration that is at least approximately the same regardless of the previous optical state of the pixel, and a "color rendering" phase as described below. The DC-balancing and reset phases are optional and may be omitted, depending upon the demands of the particular application. The "reset" phase, if employed, may be the same as the magenta color rendering waveform described below, or may involve driving the maximum possible positive and negative voltages in succession, or may be some other pulse pattern, provided that it returns the display to a state from which the subsequent colors may reproducibly be obtained.

[Para 86] Figures 5A and 5B show, in idealized form, typical color rendering phases of waveforms used to produce the black and white states in displays of the present invention. The graphs in Figures 5A and 5B show the voltage applied to the backplane (pixel) electrodes of the display while the transparent, common electrode on the top plane is grounded. The x-axis represents time, measured in arbitrary units, while the y-axis is the applied voltage in Volts. Driving the display to black (Figure 5A) or white (Figure 5B) states is effected by a sequence of positive or negative impulses, respectively, preferably at voltage V_{low} because, as noted above, at the fields (or currents) corresponding to V_{low} the magenta and yellow pigments are aggregated together. Thus, the white and cyan pigments move while the magenta and yellow pigments remain stationary (or move with a much lower velocity) and the display switches between a white state and a state corresponding to absorption by cyan, magenta and yellow pigments (often referred to in the art as a "composite black"). The length of the pulses to drive to black and white may vary from about 10–1000 milliseconds, and the pulses may be separated by rests (at zero applied volts) of lengths in the range of 10–1000 milliseconds. Although Figure 5 shows pulses of positive and negative voltages, respectively, to produce black and white, these pulses being separated by "rests" where zero voltage is supplied, it is sometimes preferred that these "rest" periods comprise pulses of the opposite polarity to the drive pulses, but having lower impulse (i.e., having a shorter duration or a lower applied voltage than the principal drive pulses, or both).

[Para 87] Figures 6A-6D show typical color rendering phases of waveforms used to produce the colors magenta and blue (Figures 6A and 6B) and yellow and green (Figures 6C and 6D). In Figure 6A, the waveform oscillates between positive and negative impulses, but the length of the positive impulse (t_p) is shorter than that of the negative impulse (t_n), while the voltage applied in the positive impulse (V_p) is greater than that of the negative impulse (V_n). When:

$$V_p t_p = V_n t_n$$

the waveform as a whole is "DC-balanced". The period of one cycle of positive and negative impulses may range from about 30–1000 milliseconds.

[Para 88] At the end of the positive impulse, the display is in the blue state, while at the end of the negative impulse the display is in the magenta state. This is consistent with the change in optical density corresponding to motion of the cyan pigment being larger than the change corresponding to motion of the magenta or yellow pigments (relative to the white pigment). According to the hypotheses presented above, this would be expected if the interaction between the magenta pigment and the white pigment were stronger than that between the cyan pigment and the white pigment. The relative mobility of the yellow and white pigments (which are both negatively charged) is much lower that the relative mobility of the cyan and white pigments (which are oppositely charged). Thus, in a preferred waveform to produce magenta or blue, a sequence of impulses comprising at least one cycle of $V_p t_p$ followed by $V_n t_n$ is preferred, where $V_p > V_n$ and $t_p < t_n$. When the color blue is required, the sequence ends on V_p whereas when the color magenta is required the sequence ends on V_n.

[Para 89] Figure 6B shows an alternative waveform for the production of magenta and blue states using only three voltage levels. In this alternative waveform, at least one cycle of $V_p t_p$ followed by $V_n t_n$ is preferred, where $V_p = V_n = V_{high}$ and $t_n < t_p$. This sequence cannot be DC-balanced. When the color blue is required, the sequence ends on V_p whereas when the color magenta is required the sequence ends on V_n.

[Para 90] The waveforms shown in Figures 6C and 6D are the inverses of those shown in Figures 6A and 6B respectively, and produce the corresponding complementary colors yellow and green. In one preferred waveform to produce yellow or green, as shown in Fig. 6C, a sequence of impulses comprising at least one cycle of $V_p t_p$ followed by $V_n t_n$ is used, where $V_p < V_n$ and $t_p > t_n$. When the color green is required, the sequence ends on V_p whereas when the color yellow is required the sequence ends on V_n.

[Para 91] Another preferred waveform to produce yellow or green using only three voltage levels is shown in Figure 6D. In this case, at least one cycle of $V_p t_p$ followed by $V_n t_n$ is used,

where $V_p = V_n = V_{high}$ and $t_n > t_p$. This sequence cannot be DC-balanced. When the color green is required, the sequence ends on V_p whereas when the color yellow is required the sequence ends on V_n.

[Para 92] Figures 7A and 7B show color rendering phases of waveforms used to render the colors red and cyan on a display of the present invention. These waveforms also oscillate between positive and negative impulses, but they differ from the waveforms of Figures 6A-6D in that the period of one cycle of positive and negative impulses is typically longer and the addressing voltages used may be (but are not necessarily) lower. The red waveform of Figure 7A consists of a pulse ($+V_{low}$) that produces black (similar to the waveform shown in Figure 5A) followed by a shorter pulse ($-V_{low}$) of opposite polarity, which removes the cyan particles and changes black to red, the complementary color to cyan. The cyan waveform is the inverse of the red one, having a section that produces white ($-V_{low}$) followed by a short pulse (V_{low}) that moves the cyan particles adjacent the viewing surface. Just as in the waveforms shown in Figures 6A-6D, the cyan moves faster relative to white than either the magenta or yellow pigments. In contrast to the Figure 6 waveforms, however, the yellow pigment in the Figure 7 waveforms remains on the same side of the white particles as the magenta particles.

[Para 93] The waveforms described above with reference to Figures 5-7 use a five level drive scheme, i.e., a drive scheme in which at any given time a pixel electrode may be at any one of two different positive voltages, two different negative voltages, or zero volts relative to a common front electrode. In the specific waveforms shown in Figures 5-7, the five levels are 0, ± 15V and ± 30V. It has, however, in at least some cases been found to be advantageous to use a seven level drive scheme, which uses seven different voltages: three positive, three negative, and zero. This seven level drive scheme may hereinafter be referred to as the "second drive scheme" of the present invention. The choice of the number of voltages used to address the display should take account of the limitations of the electronics used to drive the display. In general, a larger number of drive voltages will provide greater flexibility in addressing different colors, but complicates the arrangements necessary to provide this larger number of drive voltages to conventional device display drivers. The present inventors have found that use of seven different voltages provides a good compromise between complexity of the display architecture and color gamut.

[Para 94] The general principles used in production of the eight primary colors (white, black, cyan, magenta, yellow, red, green and blue) using this second drive scheme applied to

a display of the present invention (such as that shown in Figure 1) will now be described. As in Figures 5-7, it will be assumed that the first pigment is white, the second cyan, the third yellow and the fourth magenta. It will be clear to one of ordinary skill in the art that the colors exhibited by the display will change if the assignment of pigment colors is changed.

[Para 95] The greatest positive and negative voltages (designated ± Vmax in Figure 8) applied to the pixel electrodes produce respectively the color formed by a mixture of the second and fourth particles (cyan and magenta, to produce a blue color – cf. Figure 1E and Figure 4B viewed from the right), or the third particles alone (yellow – cf. Figure 1B and Figure 4B viewed from the left – the white pigment scatters light and lies in between the colored pigments). These blue and yellow colors are not necessarily the best blue and yellow attainable by the display. The mid-level positive and negative voltages (designated ± Vmid in Figure 8) applied to the pixel electrodes produce colors that are black and white, respectively (although not necessarily the best black and white colors attainable by the display – cf. Figure 4A).

[Para 96] From these blue, yellow, black or white optical states, the other four primary colors may be obtained by moving only the second particles (in this case the cyan particles) relative to the first particles (in this case the white particles), which is achieved using the lowest applied voltages (designated ± Vmin in Figure 8). Thus, moving cyan out of blue (by applying -Vmin to the pixel electrodes) produces magenta (cf. Figures 1E and 1D for blue and magenta respectively); moving cyan into yellow (by applying +Vmin to the pixel electrodes) provides green (cf. Figures 1B and 1G for yellow and green respectively); moving cyan out of black (by applying -Vmin to the pixel electrodes) provides red (cf. Figures 1H and 1C for black and red respectively), and moving cyan into white (by applying +Vmin to the pixel electrodes) provides cyan (cf. Figures 1A and 1F for white and cyan respectively).

[Para 97] While these general principles are useful in the construction of waveforms to produce particular colors in displays of the present invention, in practice the ideal behavior described above may not be observed, and modifications to the basic scheme are desirably employed.

[Para 98] A generic waveform embodying modifications of the basic principles described above is illustrated in Figure 8, in which the abscissa represents time (in arbitrary units) and the ordinate represents the voltage difference between a pixel electrode and the common front electrode. The magnitudes of the three positive voltages used in the drive scheme illustrated in Figure 8 may lie between about +3V and +30V, and of the three negative voltages between

about -3V and -30V. In one empirically preferred embodiment, the highest positive voltage, +Vmax, is +24V, the medium positive voltage, +Vmid, is 12V, and the lowest positive voltage, +Vmin, is 5V. In a similar manner, negative voltages –Vmax, -Vmid and –Vmin are; in a preferred embodiment -24V, -12V and -9V. It is not necessary that the magnitudes of the voltages |+V| = |-V| for any of the three voltage levels, although it may be preferable in some cases that this be so.

[Para 99] There are four distinct phases in the generic waveform illustrated in Figure 8. In the first phase ("A" in Fig. 8), there are supplied pulses (wherein "pulse" signifies a monopole square wave, i.e., the application of a constant voltage for a predetermined time) at +Vmax and -Vmax that serve to erase the previous image rendered on the display (i.e., to "reset" the display). The lengths of these pulses (t_1 and t_3) and of the rests (i.e., periods of zero voltage between them (t_2 and t_4) may be chosen so that the entire waveform (i.e., the integral of voltage with respect to time over the whole waveform as illustrated in Figure 8) is DC balanced (i.e., the integral is substantially zero). DC balance can be achieved by adjusting the lengths of the pulses and rests in phase A so that the net impulse supplied in this phase is equal in magnitude and opposite in sign to the net impulse supplied in the combination of phases B and C, during which phases, as described below, the display is switched to a particular desired color.

[Para 100] The waveform shown in Figure 8 is purely for the purpose of illustration of the structure of a generic waveform, and is not intended to limit the scope of the invention in any way. Thus, in Figure 8 a negative pulse is shown preceding a positive pulse in phase A, but this is not a requirement of the invention. It is also not a requirement that there be only a single negative and a single positive pulse in phase A.

[Para 101] As described above, the generic waveform is intrinsically DC balanced, and this may be preferred in certain embodiments of the invention. Alternatively, the pulses in phase A may provide DC balance to a series of color transitions rather than to a single transition, in a manner similar to that provided in certain black and white displays of the prior art; see for example U.S. Patent No. 7,453,445 and the earlier applications referred to in column 1 of this patent.

[Para 102] In the second phase of the waveform (phase B in Figure 8) there are supplied pulses that use the maximum and medium voltage amplitudes. In this phase the colors white, black, magenta, red and yellow are preferably rendered in the manner previously described with reference to Figures 5-7. More generally, in this phase of the waveform the colors

corresponding to particles of type 1 (assuming that the white particles are negatively charged), the combination of particles of types 2, 3, and 4 (black), particles of type 4 (magenta), the combination of particles of types 3 and 4 (red) and particles of type 3 (yellow), are formed.

[Para 103]As described above (see Figure 5B and related description), white may be rendered by a pulse or a plurality of pulses at -Vmid. In some cases, however, the white color produced in this way may be contaminated by the yellow pigment and appear pale yellow. In order to correct this color contamination, it may be necessary to introduce some pulses of a positive polarity. Thus, for example, white may be obtained by a single instance or a repetition of instances of a sequence of pulses comprising a pulse with length T_1 and amplitude +Vmax or +Vmid followed by a pulse with length T_2 and amplitude –Vmid, where $T_2 > T_1$. The final pulse should be a negative pulse. In Figure 8 there are shown four repetitions of a sequence of +Vmax for time t_5 followed by –Vmid for time t_6. During this sequence of pulses, the appearance of the display oscillates between a magenta color (although typically not an ideal magenta color) and white (i.e., the color white will be preceded by a state of lower L* and higher a* than the final white state). This is similar to the pulse sequence shown in Fig. 6A, in which an oscillation between magenta and blue was observed. The difference here is that the net impulse of the pulse sequence is more negative than the pulse sequence shown in Fig. 6A, and thus the oscillation is biased towards the negatively charged white pigment.

[Para 104]As described above (see Figure 5A and related description), black may be obtained by a rendered by a pulse or a plurality of pulses (separated by periods of zero voltage) at +Vmid.

[Para 105]As described above (see Figures 6A and 6B and related description), magenta may be obtained by a single instance or a repetition of instances of a sequence of pulses comprising a pulse with length T_3 and amplitude +Vmax or +Vmid, followed by a pulse with length T_4 and amplitude -Vmid, where $T_4 > T_3$. To produce magenta, the net impulse in this phase of the waveform should be more positive than the net impulse used to produce white. During the sequence of pulses used to produce magenta, the display will oscillate between states that are essentially blue and magenta. The color magenta will be preceded by a state of more negative a* and lower L* than the final magenta state.

[Para 106]As described above (see Figure 7A and related description), red may be obtained by a single instance or a repetition of instances of a sequence of pulses comprising a pulse

with length T_5 and amplitude +Vmax or +Vmid, followed by a pulse with length T_6 and amplitude -Vmax or -Vmid. To produce red, the net impulse should be more positive than the net impulse used to produce white or yellow. Preferably, to produce red, the positive and negative voltages used are substantially of the same magnitude (either both Vmax or both Vmid), the length of the positive pulse is longer than the length of the negative pulse, and the final pulse is a negative pulse. During the sequence of pulses used to produce red, the display will oscillate between states that are essentially black and red. The color red will be preceded by a state of lower L*, lower a*, and lower b* than the final red state.

[Para 107]Yellow (see Figures 6C and 6D and related description) may be obtained by a single instance or a repetition of instances of a sequence of pulses comprising a pulse with length T_7 and amplitude +Vmax or +Vmid, followed by a pulse with length T_8 and amplitude -Vmax. The final pulse should be a negative pulse. Alternatively, as described above, the color yellow may be obtained by a single pulse or a plurality of pulses at -Vmax.

[Para 108]In the third phase of the waveform (phase C in Figure 8) there are supplied pulses that use the medium and minimum voltage amplitudes. In this phase of the waveform the colors blue and cyan are produced following a drive towards white in the second phase of the waveform, and the color green is produced following a drive towards yellow in the second phase of the waveform. Thus, when the waveform transients of a display of the present invention are observed, the colors blue and cyan will be preceded by a color in which b* is more positive than the b* value of the eventual cyan or blue color, and the color green will be preceded by a more yellow color in which L* is higher and a* and b* are more positive than L*, a* and b* of the eventual green color. More generally, when a display of the present invention is rendering the color corresponding to the colored one of the first and second particles, that state will be preceded by a state that is essentially white (i.e., having C* less than about 5). When a display of the present invention is rendering the color corresponding to the combination of the colored one of the first and second particles and the particle of the third and fourth particles that has the opposite charge to this particle, the display will first render essentially the color of the particle of the third and fourth particles that has the opposite charge to the colored one of the first and second particles.

[Para 109]Typically, cyan and green will be produced by a pulse sequence in which +Vmin must be used. This is because it is only at this minimum positive voltage that the cyan pigment can be moved independently of the magenta and yellow pigments relative to the

white pigment. Such a motion of the cyan pigment is necessary to render cyan starting from white or green starting from yellow.

[Para 110]Finally, in the fourth phase of the waveform (phase D in Figure 8) there is supplied a zero voltage.

[Para 111]Although the display of the invention has been described as producing the eight primary colors, in practice, it is preferred that as many colors as possible be produced at the pixel level. A full color gray scale image may then be rendered by dithering between these colors, using techniques well known to those skilled in imaging technology. For example, in addition to the eight primary colors produced as described above, the display may be configured to render an additional eight colors. In one embodiment, these additional colors are: light red, light green, light blue, dark cyan, dark magenta, dark yellow, and two levels of gray between black and white. The terms "light" and "dark" as used in this context refer to colors having substantially the same hue angle in a color space such as CIE L*a*b* as the reference color but a higher or lower L*, respectively.

[Para 112]In general, light colors are obtained in the same manner as dark colors, but using waveforms having slightly different net impulse in phases B and C. Thus, for example, light red, light green and light blue waveforms have a more negative net impulse in phases B and C than the corresponding red, green and blue waveforms, whereas dark cyan, dark magenta, and dark yellow have a more positive net impulse in phases B and C than the corresponding cyan, magenta and yellow waveforms. The change in net impulse may be achieved by altering the lengths of pulses, the number of pulses, or the magnitudes of pulses in phases B and C.

[Para 113]Gray colors are typically achieved by a sequence of pulses oscillating between low or mid voltages.

[Para 114]It will be clear to one of ordinary skill in the art that in a display of the invention driven using a thin-film transistor (TFT) array the available time increments on the abscissa of Figure 8 will typically be quantized by the frame rate of the display. Likewise, it will be clear that the display is addressed by changing the potential of the pixel electrodes relative to the front electrode and that this may be accomplished by changing the potential of either the pixel electrodes or the front electrode, or both. In the present state of the art, typically a matrix of pixel electrodes is present on the backplane, whereas the front electrode is common to all pixels. Therefore, when the potential of the front electrode is changed, the addressing of

all pixels is affected. The basic structure of the waveform described above with reference to Figure 8 is the same whether or not varying voltages are applied to the front electrode.

[Para 115]The generic waveform illustrated in Figure 8 requires that the driving electronics provide as many as seven different voltages to the data lines during the update of a selected row of the display. While multi-level source drivers capable of delivering seven different voltages are available, many commercially-available source drivers for electrophoretic displays permit only three different voltages to be delivered during a single frame (typically a positive voltage, zero, and a negative voltage). Herein the term "frame" refers to a single update of all the rows in the display. It is possible to modify the generic waveform of Figure 8 to accommodate a three level source driver architecture provided that the three voltages supplied to the panel (typically +V, 0 and –V) can be changed from one frame to the next. (i.e., such that, for example, in frame *n* voltages (+Vmax, 0, –Vmin) could be supplied while in frame *n+1* voltages (+Vmid, 0 , -Vmax) could be supplied).

[Para 116]Since the changes to the voltages supplied to the source drivers affect every pixel, the waveform needs to be modified accordingly, so that the waveform used to produce each color must be aligned with the voltages supplied. Fig. 9 shows an appropriate modification to the generic waveform of Figure 8. In phase A, no change is necessary, since only three voltages (+Vmax, 0, –Vmax) are needed. Phase B is replaced by subphases B1 and B2 are defined, of lengths L_1 and L_2, respectively, during each of which a particular set of three voltages are used. In Figure 9, in phase B1 voltages +Vmax, 0, -Vmax) are available, while in phase B2 voltages +Vmid, 0, -Vmid are available. As shown in Fig 9, the waveform requires a pulse of +Vmax for time t_5 in subphase B1. Subphase B1 is longer than time t_5 (for example, to accommodate a waveform for another color in which a pulse longer than t_5 might be needed), so a zero voltage is supplied for a time $L_1 - t_5$. The location of the pulse of length t_5 and the zero pulse or pulses of length $L_1 - t_5$ within subphase B1 may be adjusted as required (i.e., subphase B1 does not necessarily begin with the pulse of length t_5 as illustrated). By subdividing the phases B and C in to subphases in which there is a choice of one of the three positive voltages, one of the three negative voltages and zero, it is possible to achieve the same optical result as would be obtained using a multilevel source driver, albeit at the expense of a longer waveform (to accommodate the necessary zero pulses).

[Para 117] Sometimes it may be desirable to use a so-called "top plane switching" driving scheme to control an electrophoretic display. In a top plane switching driving scheme, the top plane common electrode can be switched between –V, 0 and +V, while the voltages

applied to the pixel electrodes can also vary from –V, 0 to +V with pixel transitions in one direction being handled when the common electrode is at 0 and transitions in the other direction being handled when the common electrode is at +V.

[Para 118] When top plane switching is used in combination with a three-level source driver, the same general principles apply as described above with reference to Fig. 9. Top plane switching may be preferred when the source drivers cannot supply a voltage as high as the preferred Vmax. Methods for driving electrophoretic displays using top plane switching are well known in the art.

[Para 119] A typical waveform according to the second drive scheme of the invention is shown below in Table 3, where the numbers in parentheses correspond to the number of frames driven with the indicated backplane voltage (relative to a top plane assumed to be at zero potential).

[Para 120]   **Table 3**

	Reset Phase		High/Mid V Phase (N repetitions of frame sequence below)			Low/Mid V phase	
K	-Vmax(60+Δ_K)	Vmax(60-Δ_K)	Vmid(5)	Zero(9)		Zero(50)	
B	-Vmax(60+Δ_B)	Vmax(60-Δ_B)	Vmax(2)	Zero(5)	-Vmid(7)	Vmid(40)	Zero(10)
R	-Vmax(60+Δ_R)	Vmax(60-Δ_R)	Vmax(7)	Zero(3)	-Vmax(4)	Zero(50)	
M	-Vmax(60+Δ_M)	Vmax(60-Δ_M)	Vmax(4)	Zero(3)	-Vmid(7)	Zero(50)	
G	-Vmax(60+Δ_G)	Vmax(60-Δ_G)	Vmid(7)	Zero(3)	-Vmax(4)	Vmin(40)	Zero(10)
C	-Vmax(60+Δ_C)	Vmax(60-Δ_C)	Vmax(2)	Zero(5)	-Vmid(7)	Vmin(40)	Zero(10)
Y	-Vmax(60+Δ_Y)	Vmax(60-Δ_Y)	Vmid(7)	Zero(3)	-Vmax(4)	Zero(50)	
W	-Vmax(60+Δ_W)	Vmax(60-Δ_W)	Vmax(2)	Zero(5)	-Vmid(7)	Zero(50)	

[Para 121]In the reset phase, pulses of the maximum negative and positive voltages are provided to erase the previous state of the display. The number of frames at each voltage are offset by an amount (shows as Δ_x for color x) that compensates for the net impulse in the High/Mid voltage and Low/Mid voltage phases, where the color is rendered. To achieve DC balance, Δ_x is chosen to be half that net impulse. It is not necessary that the reset phase be implemented in precisely the manner illustrated in the Table; for example, when top plane switching is used it is necessary to allocate a particular number of frames to the negative and positive drives. In such a case, it is preferred to provide the maximum number of high voltage pulses consistent with achieving DC balance (i.e., to subtract $2\Delta_x$ from the negative or positive frames as appropriate).

[Para 122]In the High/Mid voltage phase, as described above, a sequence of N repetitions of a pulse sequence appropriate to each color is provided, where N can be 1-20. As shown, this sequence comprises 14 frames that are allocated positive or negative voltages of magnitude Vmax or Vmid, or zero. The pulse sequences shown are in accord with the discussion given above. It can be seen that in this phase of the waveform the pulse sequences to render the colors white, blue and cyan are the same (since blue and cyan are achieved in this case starting from a white state, as described above). Likewise, in this phase the pulse sequences to render yellow and green are the same (since green is achieved starting from a yellow state, as described above).

[Para 123]In the Low/Mid voltage phase the colors blue and cyan are obtained from white, and the color green from yellow.

[Para 124]The foregoing discussion of the waveforms shown in Figures 5-9, and specifically the discussion of DC balance, ignores the question of kickback voltage. In practice, as previously, every backplane voltage is offset from the voltage supplied by the power supply by an amounts equal to the kickback voltage V_{KB}. Thus, if the power supply used provides the three voltages +V, 0, and -V, the backplane would actually receive voltages $V+V_{KB}$, V_{KB}, and $-V+ V_{KB}$ (note that V_{KB}, in the case of amorphous silicon TFTs, is usually a negative number). The same power supply would, however, supply +V, 0, and -V to the front electrode without any kickback voltage offset. Therefore, for example, when the front electrode is supplied with -V the display would experience a maximum voltage of $2V+ V_{KB}$ and a minimum of V_{KB}. Instead of using a separate power supply to supply V_{KB} to the front electrode, which can be costly and inconvenient, a waveform may be divided into sections where the front electrode is supplied with a positive voltage, a negative voltage, and V_{KB}.

[Para 125]As discussed above, in some of the waveforms described in the aforementioned Application Serial No. 14/849,658, seven different voltages can be applied to the pixel electrodes: three positive, three negative, and zero; as presented in the discussion of Figures 8 and 9 above. Preferably, the maximum voltages used in these waveforms are higher than that can be handled by amorphous silicon thin-film transistors in the current state of the art. In such cases, high voltages can be obtained by the use of top plane switching, and the driving waveforms can be configured to compensate for the kickback voltage and can be intrinsically DC-balanced by the methods of the present invention. Figure 11 depicts schematically one such waveform used to display a single color. As shown in Figures 11, the waveforms for every color have the same basic form: i.e., the waveform is intrinsically DC-balanced and can comprise two sections or phases: (1) a preliminary series of frames that is used to provide a "reset" of the display to a state from which any color may reproducibly be obtained and during which a DC imbalance equal and opposite to the DC imbalance of the remainder of the waveform is provided, and (2) a series of frames that is particular to the color that is to be rendered; cf. Sections A and B of the waveform shown in Figure 8.

[Para 126]During the first "reset" phase, the reset of the display ideally erases any memory of a previous state, including remnant voltages and pigment configurations specific to previously-displayed colors. Such an erasure is most effective when the display is addressed at the maximum possible voltage in the "reset/DC balancing" phase. In addition, sufficient frames may be allocated in this phase to allow for balancing of the most imbalanced color transitions. Since some colors require a positive DC-balance in the second section of the waveform and others a negative balance, in approximately half of the frames of the "reset/DC balancing" phase, the front electrode voltage V_{com} is set to V_pH (allowing for the maximum possible negative voltage between the backplane and the front electrode), and in the remainder, V_{com} is set to V_nH (allowing for the maximum possible positive voltage between the backplane and the front electrode). Empirically it has been found preferable to precede the $V_{com} = V_nH$ frames by the $V_{com} = V_pH$ frames.

[Para 127]The "desired" waveform (i.e., the actual voltage against time curve which is desired to apply across the electrophoretic medium) is illustrated at the bottom of Figure 11, and its implementation with top plane switching is shown above, where the potentials applied to the front electrode (V_{com}) and to the backplane (BP) are illustrated. It is assumed that a five-level column driver is used connected to a power supply capable of supplying the following voltages: V_pH, V_nH (the highest positive and negative voltages, typically in the

range of ± 10–15 V), V_pL, V_nL (lower positive and negative voltages, typically in the range of ± 1-10 V), and zero. In addition to these voltages, a kickback voltage V_{KB} (a small value that is specific to the particular backplane used, measured as described, for example, in U.S. Patent No. 7,034,783) can be supplied to the front electrode by an additional power supply.

[Para 128]As shown in Figure 11, every backplane voltage is offset by V_{KB} (shown as a negative number) from the voltage supplied by the power supply while the front electrode voltages are not so offset, except when the front electrode is explicitly set to V_{KB}, as described above.

[Para 129]DC-balancing can be achieved in the following way:

[Para 130]Assume the color transition of a waveform (second section or portion or phase as described above), without the reset/DC-balancing section or portion or phase) has n frames. Let

$$I_u = \sum_{i=1}^{n} \left(V_B^i - V_{COM}^i \right) + n V_{KB}$$

be the total impulse of the color transition section due to the kickback voltage, where V_B^i is the voltage on the backplane and V_{COM}^i is the front electrode voltage at frame i. The overall impulse of the "reset" phase should to be $-I_u$ to maintain an overall DC balance for the entire waveform.

[Para 131]Now an impulse offset σ may be chosen, which will be the bias of the DC-balancing, so a value of $\sigma = 0$ corresponds to exact DC-balance. One can also choose a reset duration, d_r (the overall duration of the reset phase) and two reset voltages of opposite signs given by:

$$V_{1p} = V_B^{r1} - V_T^{r1}$$

$$V_{2p} = V_B^{r2} - V_{com}^{r2}$$

See Figure 12.

[Para 132]Then the durations of d_1 and d_2, the sub-sections of the reset phase shown in Figure 12, can be determined by the following formulas:

$$d_1 = \frac{\left(V_{2p} + V_{KB} \right) d_r - \sigma}{V_{2p} - V_{1p}}$$

$$d_2 = d_r - d_1$$

[Para 133]Subsequently, one may compute for a parameter d_{2s}, which specifies the duration for which $V_B = V_{COM}$ during the second half of the reset, such that

$$d_{2s} = \frac{1}{V_2}(V_1 d_1 - \sigma + I_u + d_r V_{KB} + V_2 d_2)$$

[Para 134]Note that one requires that $0 \le d_{2s} \le d_2$. The reset duration d_r and the reset voltages V_1, V_2 must be large enough to account for the total impulse of the update. If d_{2s} falls outside this constraint, one can simply set it to the closest bound. For example, if $d_{2s} < 0$, then set it to 0, and if $d_{2s} > d_2$, then set it to d_2. In this case, the resulting balance/reset will not effectively DC-balance the update, but will come as close as possible within the given voltages/duration of the reset.

[Para 135]Once d_{2s} is computed, one can finish computing the rest of the balancing parameters, such that:

$$d_{1p} = \frac{1}{V_1}(\sigma - I_u - d_r V_{KB} - V_2 d_2 + V_2 d_{2s})$$

$$d_{1s} = d_1 - d_{1p}$$

$$d_{2p} = d_2 - d_{2s}$$

[Para 136]Once these parameters are computed, the reset/balancing portion of the update is created as shown in Fig. 12. The V_{com} is driven at V_{COM}^{r1} for duration d_1, followed by V_{COM}^{r2} for duration d_2. The backplane is driven at V_B^{r1} for duration d_{1p}, then at 0 for duration d_{1s}, then at V_B^{r2} for duration d_{2p}, and finally at 0 for duration d_{2s}.

[Para 137]In some embodiments, a "zero" voltages V_{jz} for the reset phase (i.e., the actual voltages across the electrophoretic layer when the front and back electrodes are nominally at the same voltage) may be computed, such that:

$$V_{jz} = V_B^{zj} - V_{com}^{rj}, \quad j = 1,2$$

where V_B^{zj} is the backplane voltage during the "zero" portions of the reset phase and should be chosen to be whichever voltage minimizes

$$\left| V_B - V_T^{rj} + V_{KB} \right|$$

[Para 138]Now the durations (d_{1p}, d_{1z}), (d_{2p}, d_{2z}) of the sub-phases of the reset phase may also be calculated such that each pulse is split between driving and zero sub-phases, where

$$d_{2z} = \min\left(\max\left(\frac{(V_{1p} - V_{1z})d_1 - \gamma}{V_{2p} - V_{2z}}, 0 \right), d_2 \right)$$

$$d_{1p} = \min\left(\max\left(\frac{\gamma - (V_{2z} - V_{2p})d_{2z}}{V_{1p} - V_{1z}}, 0 \right), d_1 \right)$$

$$d_{2p} = d_2 - d_{2z}$$

$$d_{1z} = d_1 - d_{1p}$$

where

$$\gamma = \sigma - I_u - V_{KB}d_r - V_{1z}d_1 - V_{2p}d_2$$

[Para 139]Note that if the impulse of the update is large enough that d_{2p} would fall outside the range $[0, d_2]$, then the transition will not be DC-balanced, but will come as close as possible within the voltages/duration of the first phase.

[Para 140]Once the values of d_{1p}, d_{1z}, d_{2p} and d_{2z}), and hence of d_1 and d_2 are thus computed, the front electrode is driven at (See Figure 12)

1. V_{com}^{r1} for duration d_1, where $V_{com}^{r1} = V_pH$

2. V_{com}^{r2} for duration d_2, where $V_{com}^{r2} = V_nH$

and the backplane is driven at:

1. V_B^{r1} for duration d_{1p}, where $V_B^{r1} = V_nH$

2. V_B^{z1} for duration d_{1z}, where $V_B^{z1} = V_pH$

3. V_B^{r2} for duration d_{2p}, where $V_B^{r2} = V_pH$

4. V_B^{z2} for duration d_{2z}, where $V_B^{r2} = V_nH$

[Para 141]As described above, the backplane is addressed by scanning though the gate lines (rows) during each frame. Thus, each row is refreshed at a slightly different time. When top plane switching is used, however, the reset of V_{com} to a different voltage occurs at one particular time. During the frame in which the V_{com} switch occurs all rows but one experience a slightly incorrect impulse, as illustrated in Fig. 13.

[Para 142]As described above, the backplane is addressed by scanning though the gate lines (rows) during each frame. Thus, each row is refreshed at a slightly different time. When top plane switching is used, however, the reset of V_{com} to a different voltage occurs at one particular time. During the frame in which the V_{com} switch occurs all rows but one experience a slightly incorrect impulse, as illustrated in Fig. 13.

[Para 143]Shown in Fig. 13 is a case in which V_{com} is adjusted from V_{KB} to a negative voltage for three frames, then to a positive voltage for three frames, returning to V_{KB}. It is desired to maintain approximately zero potential throughout this series of transitions. It is assumed that the switch of V_{com} occurs at the beginning of a frame (i.e., at backplane row 1, BP_1). For the entire time that V_{com} is not set to V_{KB}, as described above, the potential difference across the display is V_{KB}. The top plane switches a little before the scanning backplane reaches row BP_x. Thus, for a period that can be almost as long as one frame, some rows of the image may receive an impulse offset from what is desired. It can be seen, however, that compensatory offsets occur in later frames as the V_{com} setting is adjusted again. The scanning of the backplane thus does not affect the net DC-balancing achieved by the present invention.

[Para 144]At first glance it might appear that the sequential scanning of the various rows of an active matrix display might upset the above calculations designed to ensure accurate DC balancing of waveforms and drive schemes, because when the voltage of the front electrode is changed (typically between successive scans of the active matrix), each pixel of the display will experience an "incorrect" voltage until the scan reaches the relevant pixel and the voltage on its pixel electrode is adjusted to compensate for the change in the front electrode voltage, and the period between the change in front plane voltage and the time when the scan reaches the relevant pixel varies depending upon the row in which the relevant is located. However, further investigation will show that the actual "error" in the impulse applied to the pixel is proportional to the change in front plane voltage times the period between the front plane voltage change and the time the scan reaches the relevant pixel. The latter period is fixed, assuming no change in scan rate, so that for any series of changes in front plane voltage which leaves the final front plane voltage equal to the initial one, the sum total of the "errors" in impulse will be zero, and the overall DC balance of the drive scheme will not be affected.

CLAIMS

1.   A method for driving an electro-optic display having a front electrode, a backplane and a display medium positioned between the front electrode and the backplane, the method comprising:

applying a first driving phase to the display medium, the first driving phase having a first signal and a second signal, the first signal having a first polarity, a first amplitude as a function of time, and a first duration, the second signal succeeding the first signal and having a second polarity opposite to the first polarity, a second amplitude as a function of time, and a second duration, such that the sum of the first amplitude as a function of time integrated over the first duration and the second amplitude as a function of time integrated over the second duration produces a first impulse offset;

and applying a second driving phase to the display medium, the second driving phase produces a second impulse offset;

wherein the sum of the first and second impulse offset is substantially zero.

2.   The method of claim 1, wherein the first polarity is a negative voltage and the second polarity is a positive voltage.

3.   The method of claim 1, wherein the first polarity is a positive voltage and the second polarity is a negative voltage.

4.   The method of claim 1, wherein the duration of the first driving phase is different from that of the second driving phase.

5.   The method of claim 1, wherein the first duration is determined by a ratio between the amount of second impulse offset the second driving phases produces and the amplitude difference between the first amplitude and the second amplitude.

6.   The method of claim 1 wherein the display medium is an electrophoretic medium.

7.   The method of claim 6 wherein the display medium is an encapsulated electrophoretic display medium.

8.   The method of claim 6 wherein the electrophoretic display medium comprises an electrophoretic medium comprising a liquid and at least one particle disposed within said liquid and capable of moving therethrough on application of an electric field to the medium.

9.   A method for driving an electro-optic display having a front electrode, a backplane, and a display medium positioned between the front electrode and the backplane, the method comprising:

applying a reset phase and a color transition phase to the display, the reset phase comprising:

applying a first signal having a first polarity, a first amplitude as a function of time, and a first duration on the front electrode;

applying a second signal having a second polarity opposite the first polarity, a second amplitude as a function of time, and a second duration during the first duration on the backplane;

applying a third signal having the second polarity, a third amplitude as a function of time, and a third duration preceded by the first duration on the front electrode;

applying a fourth signal having the first polarity, a fourth amplitude as a function of time, and a fourth duration preceded by the second duration on the backplane;

wherein the sum of the first amplitude as a function of time integrated over the first duration, and the second amplitude as a function of time integrated over the second duration, and the third amplitude as a function of time integrated over the third duration, and the fourth amplitude as a function of time integrated over the fourth duration produces an impulse offset designed to maintain a DC-balance on the display medium over the reset phase and the color transition phase.

10.   The method of claim 9 wherein the reset phase erases previous optical properties rendered on the display.

11.   The method of claim 9 wherein the color transition phase substantially changes the optical property displayed by the display.

12.   The method of claim 9 wherein the first polarity is a negative voltage.

13.   The method of claim 9 wherein the first polarity is a positive voltage.

14.   The method of claim 9 wherein the impulse offset is proportional to a kickback voltage experienced by the display medium.

15.   The method of claim 9 wherein the first duration and the second duration initiate at the same time.

16.   The method of claim 9 wherein the fourth duration occurs during the third duration.

17.   The method of claim 16 wherein the third duration and the fourth duration initiate at the same time.

White	Magenta	Cyan	Magenta	Cyan	Yellow	Cyan
Yellow	Yellow	Magenta	Magenta	Cyan	Cyan	Magenta
						Yellow
White	**White**	**White**	**White**	**White**	**White**	**White**
Cyan	Cyan	Cyan	Yellow	Magenta	Magenta	
Magenta	Magenta	Yellow		Yellow		
Yellow						

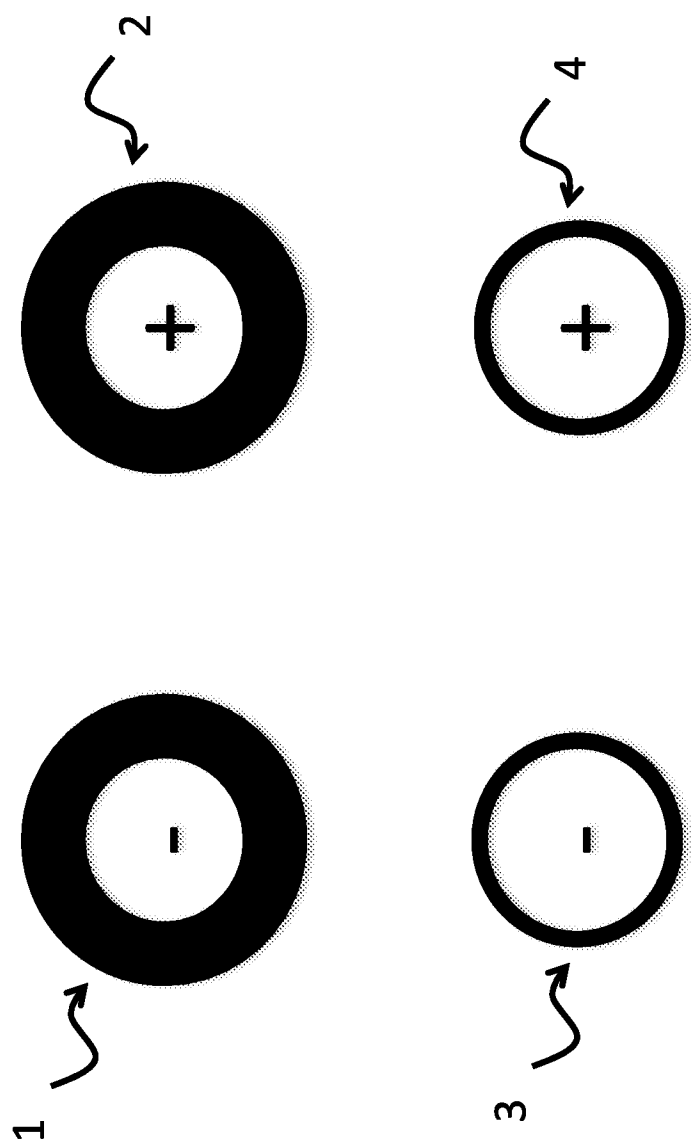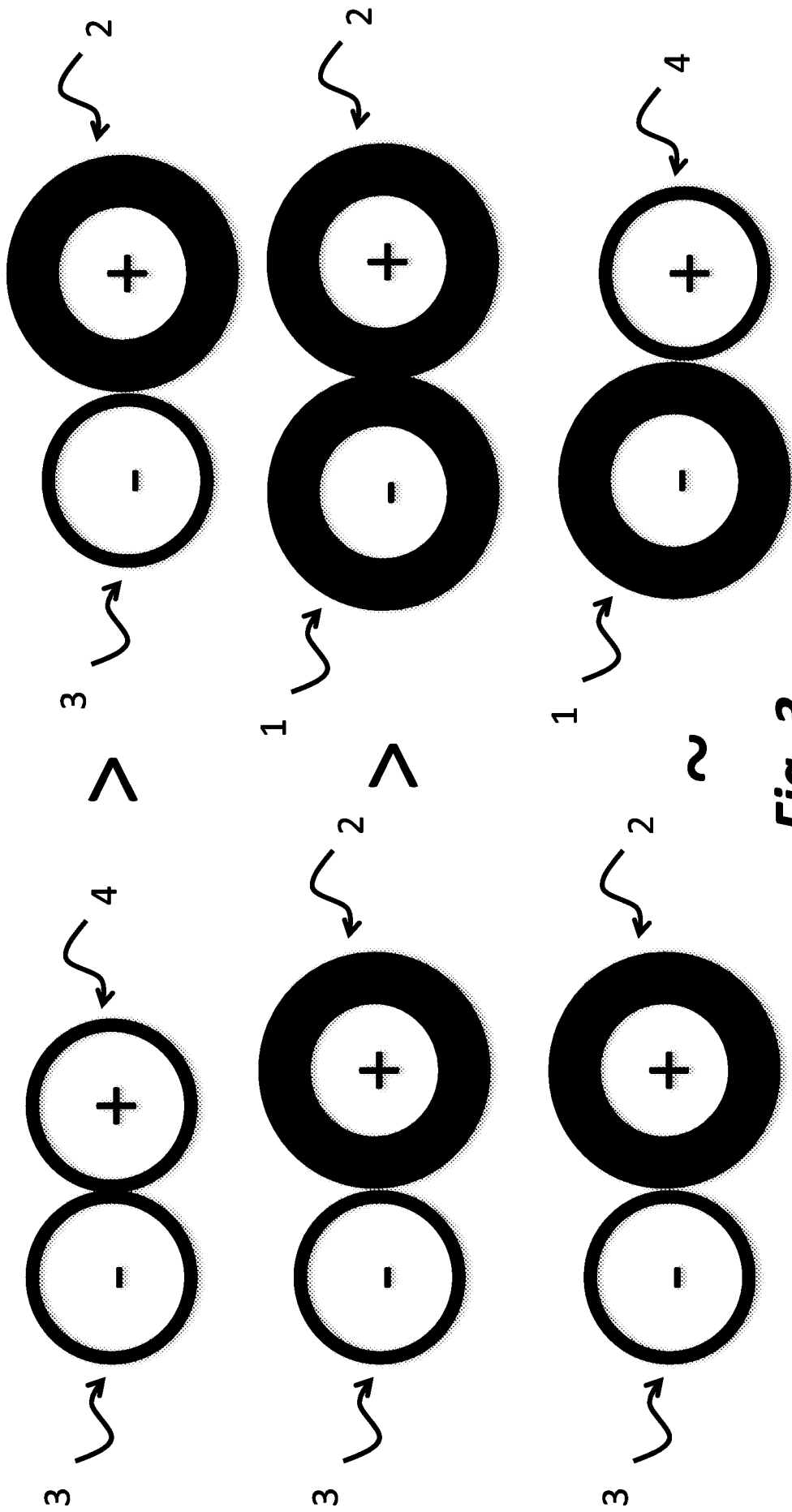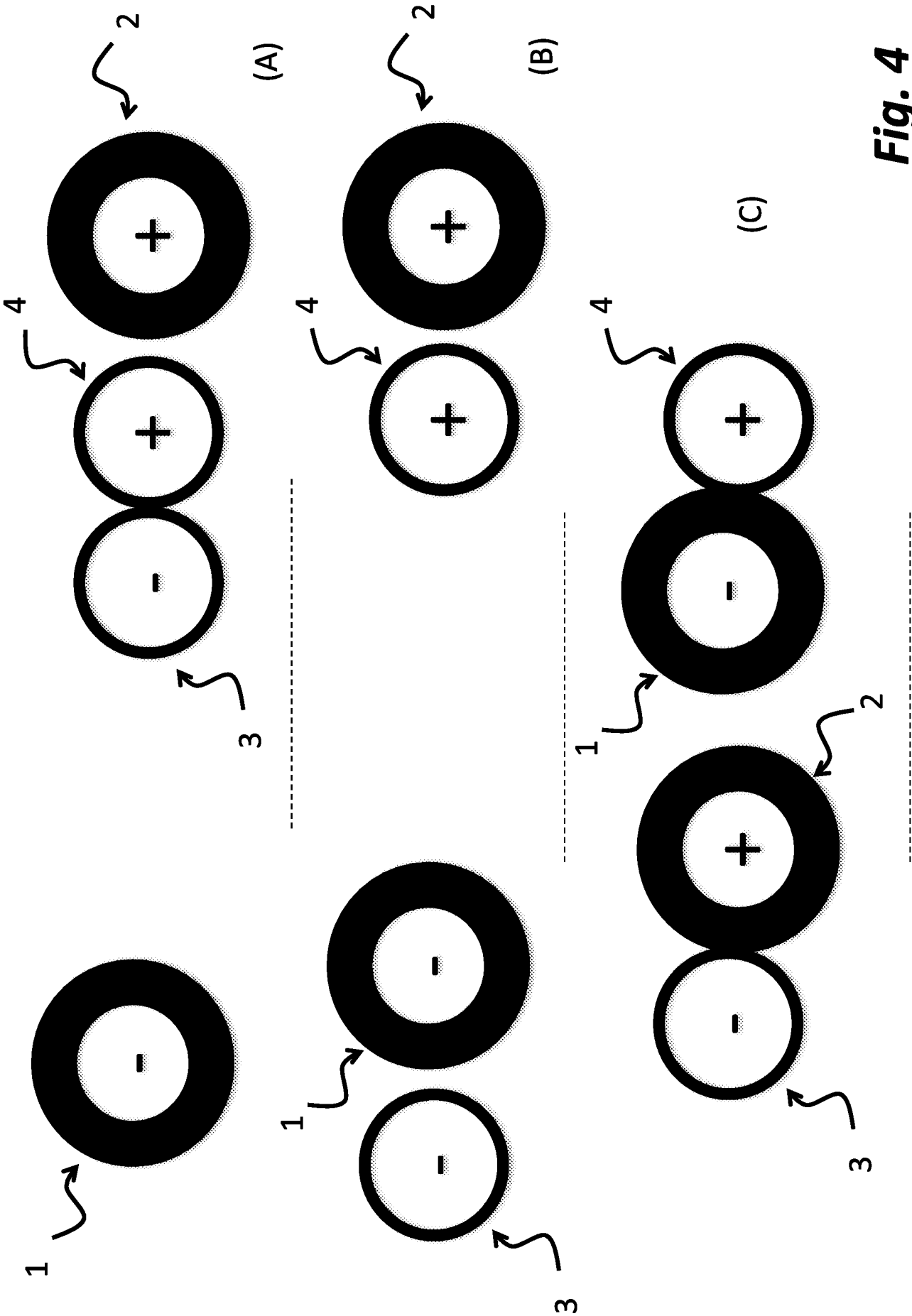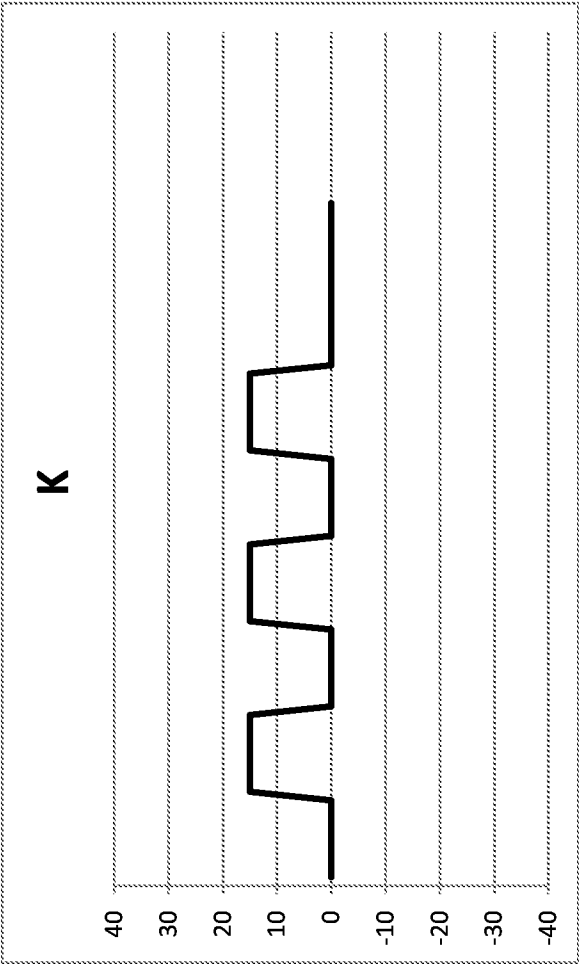| **White**	**Yellow**	**Red**	**Magenta**	**Blue**	**Cyan**	**Green**	**Black**
[A]	[B]	[C]	[D]	[E]	[F]	[G]	[H]

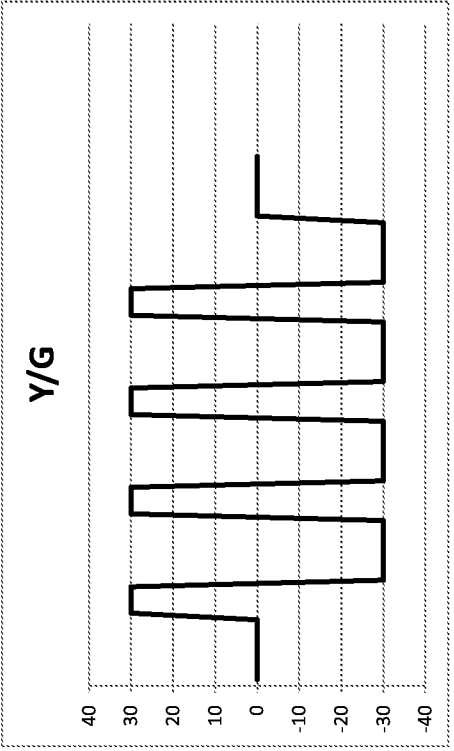*Fig. 1*

Fig. 2

Fig. 3

Fig. 4

Fig. 5A
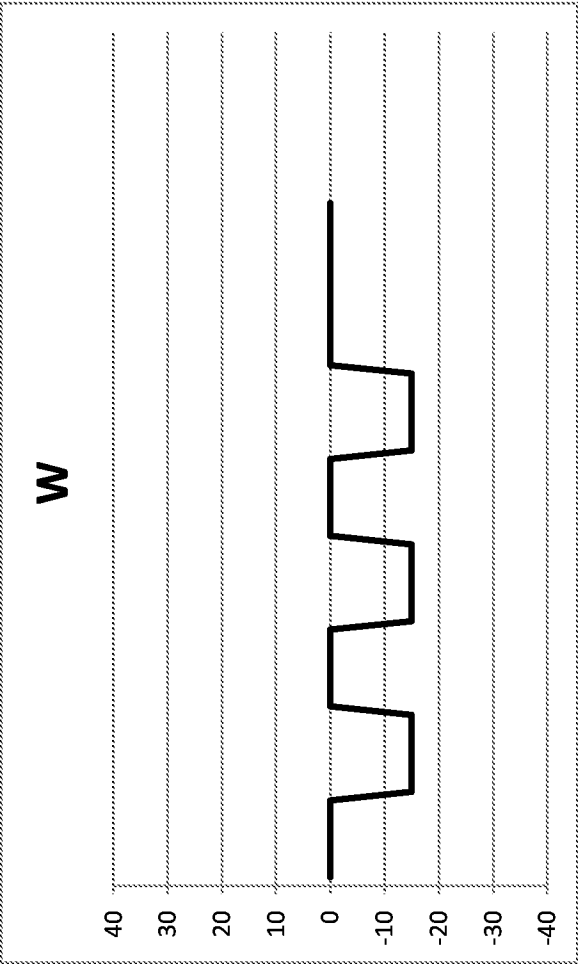
Fig. 5B
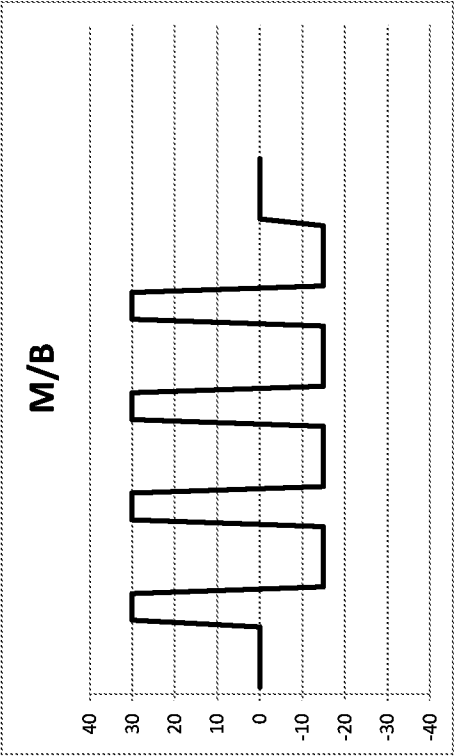
Fig. 6B

Fig. 6D

Fig. 6A
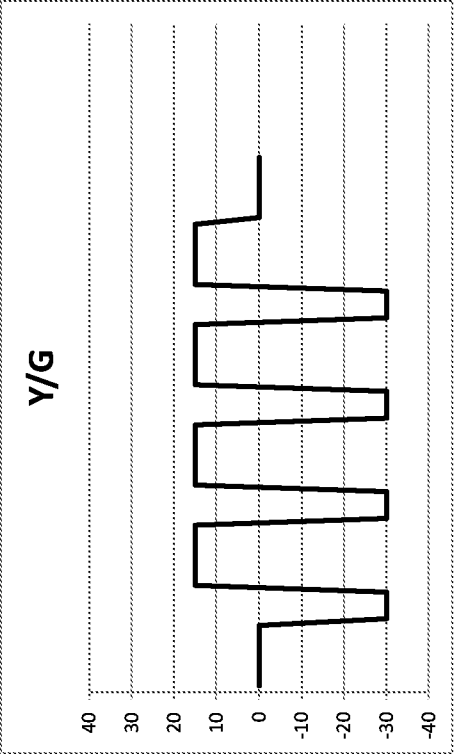
Fig. 6C
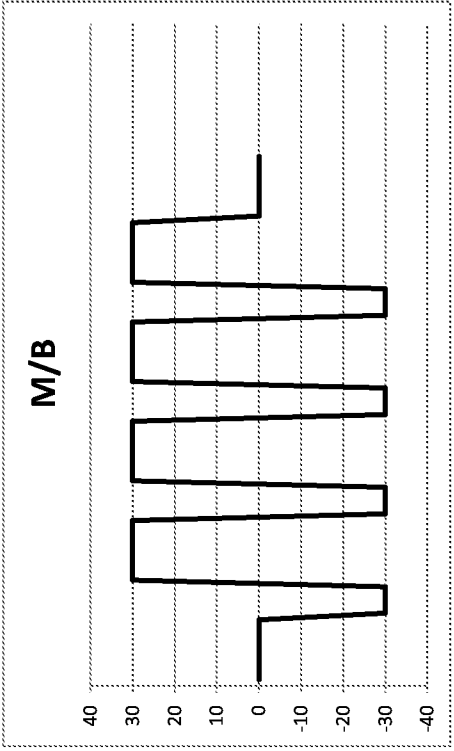
Fig. 7A
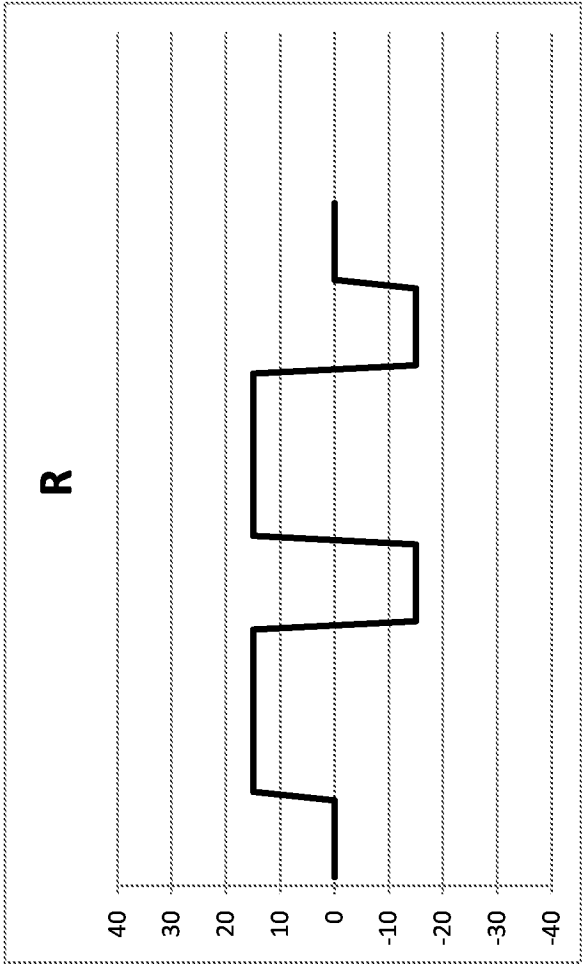
Fig. 7B
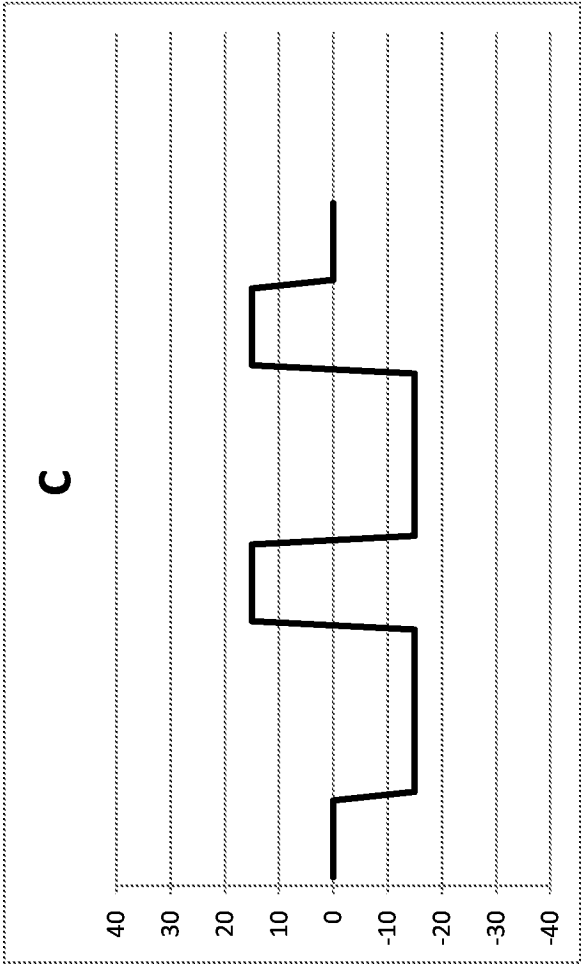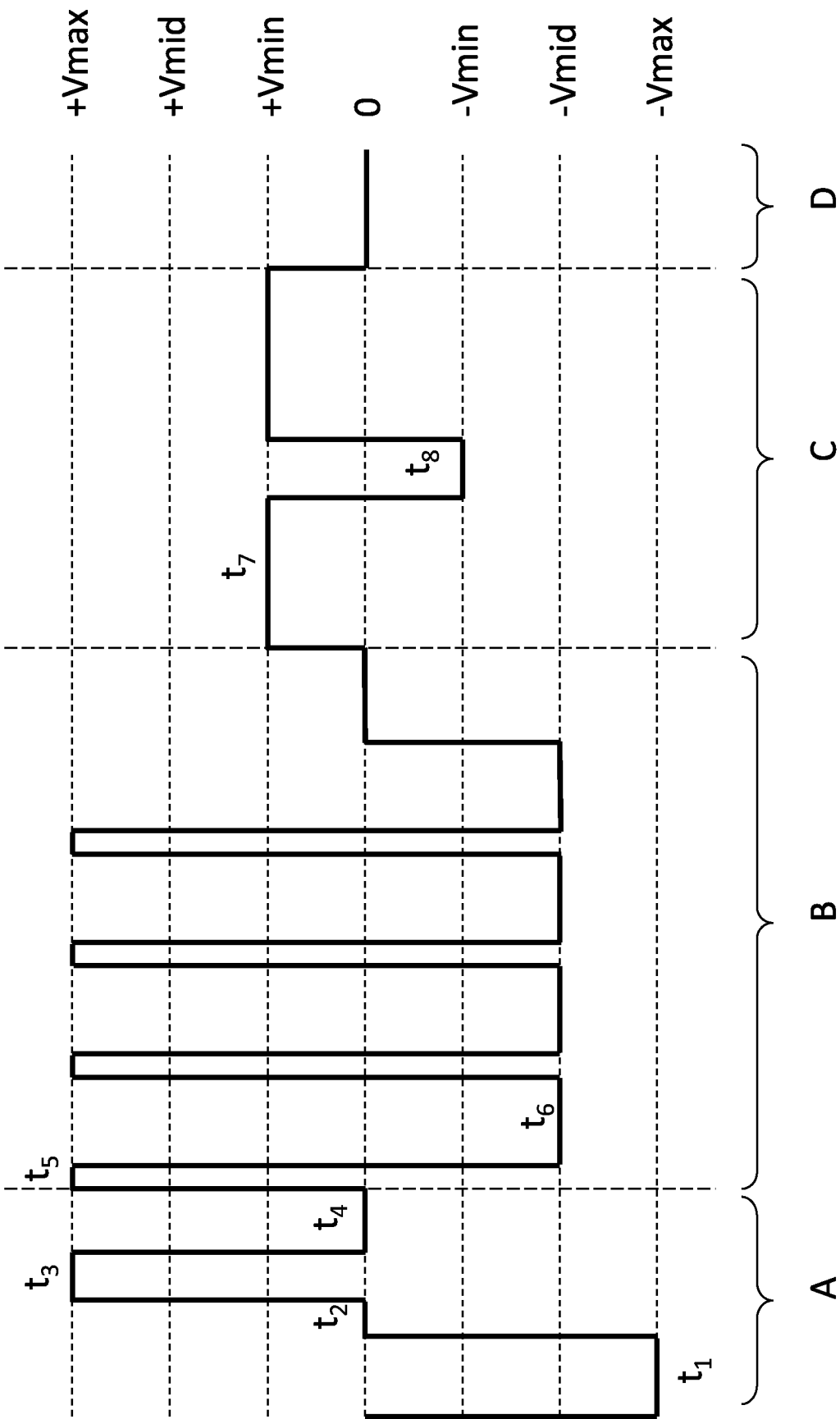
Fig. 8
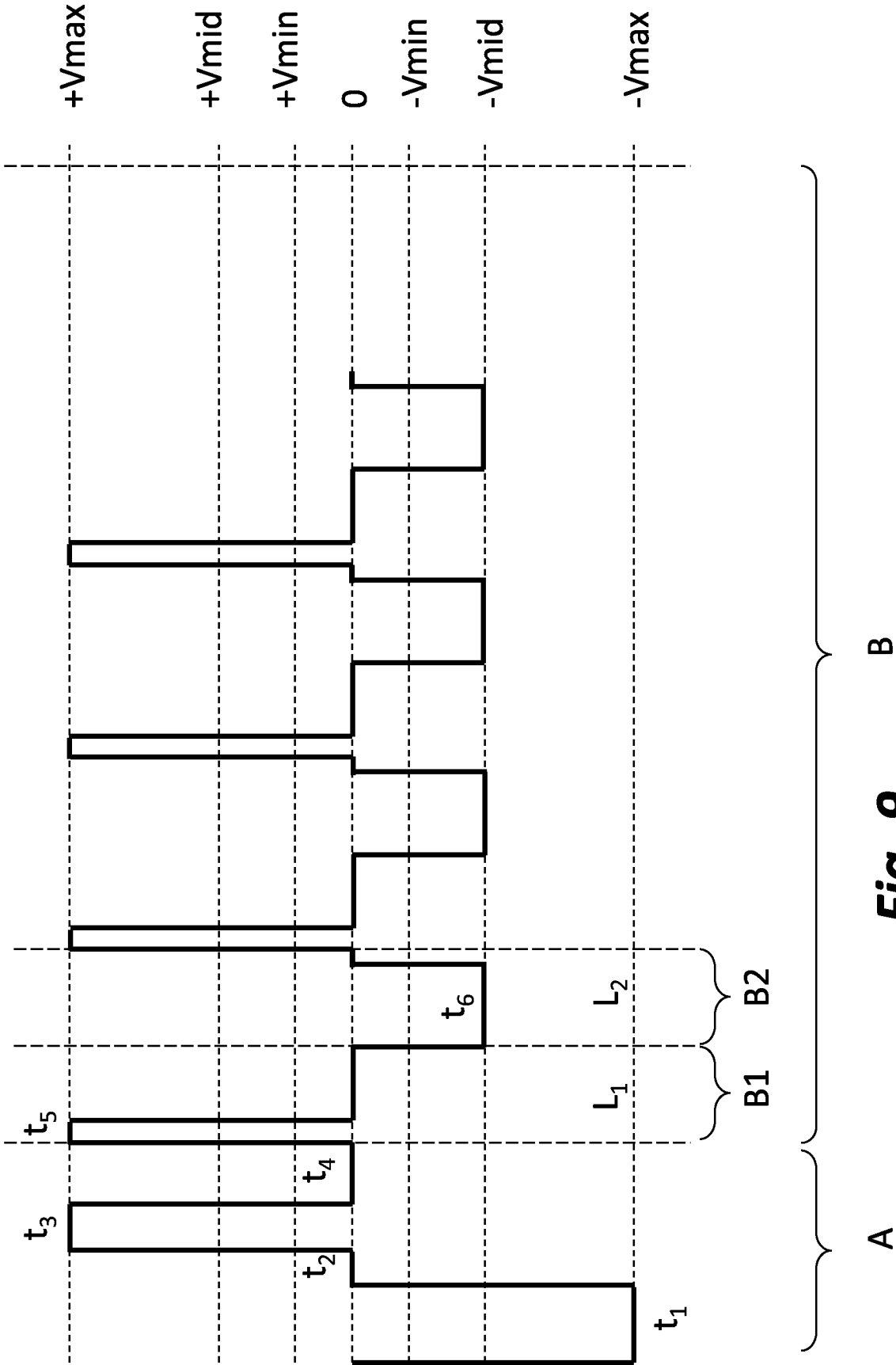
Fig. 9
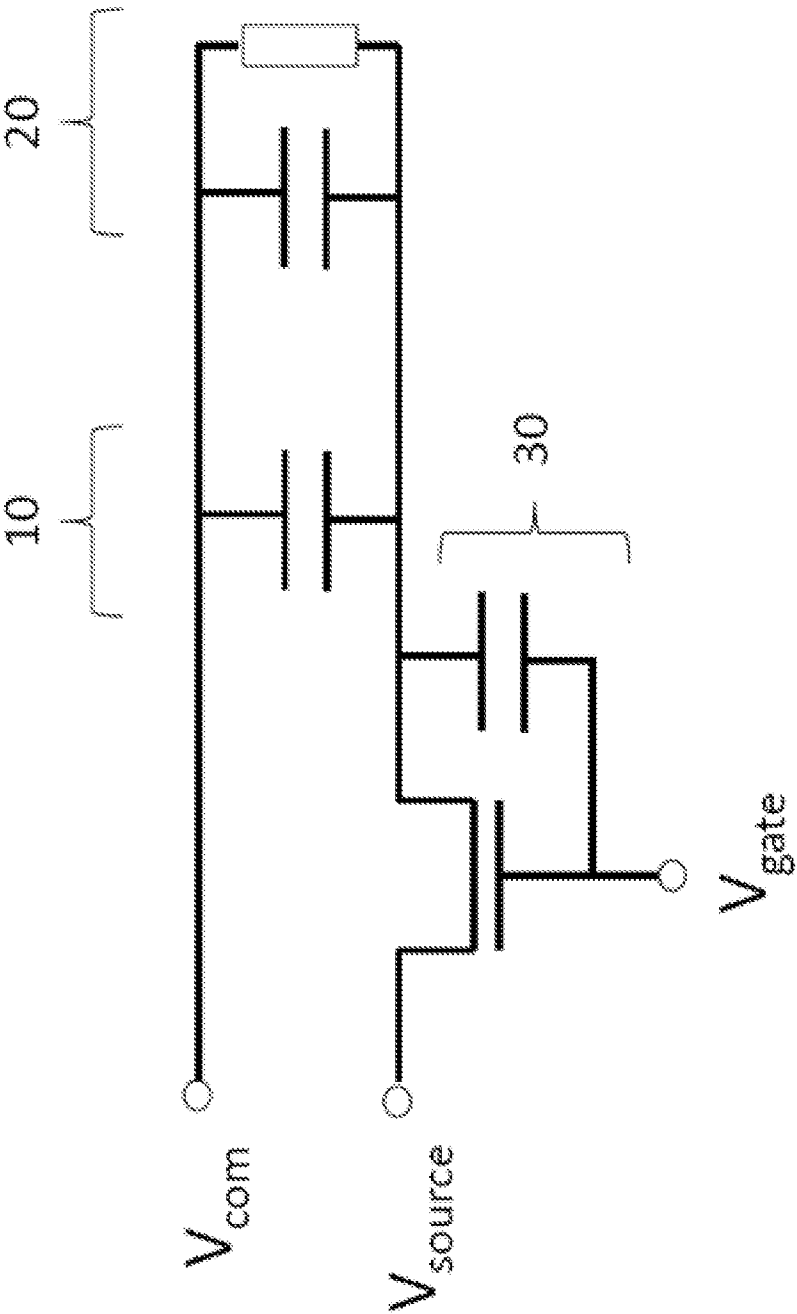
Fig. 10
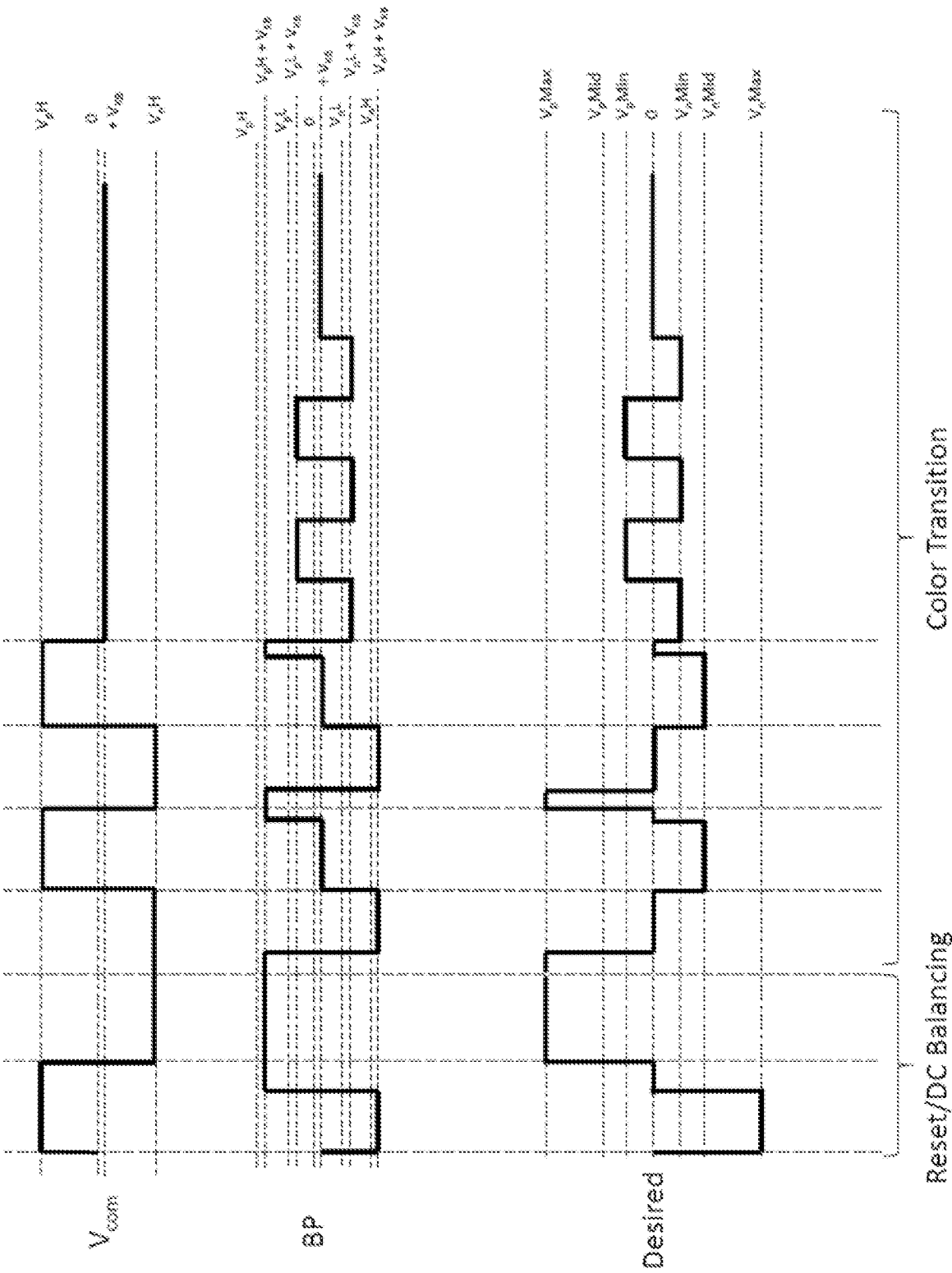
Fig. 11
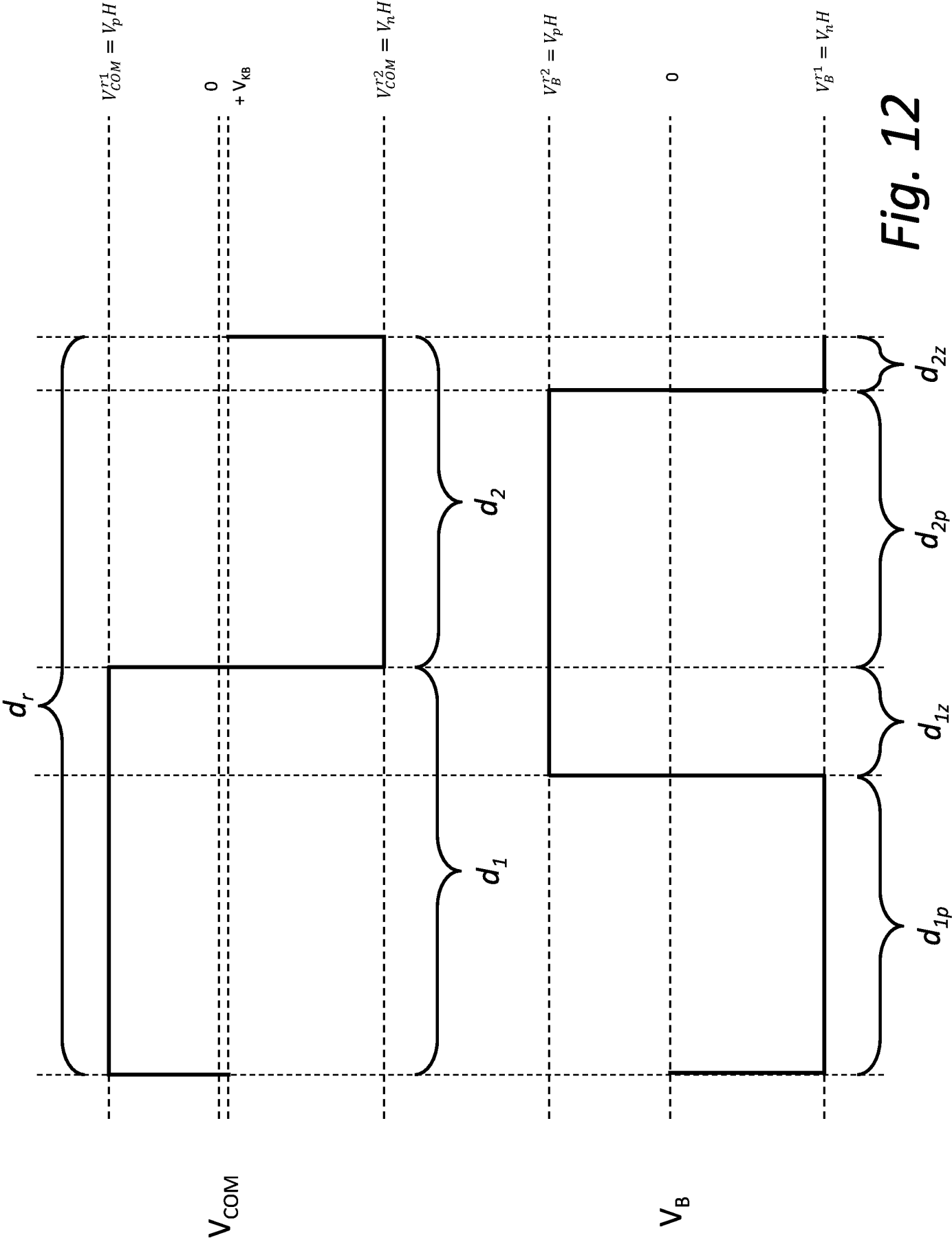
Fig. 12
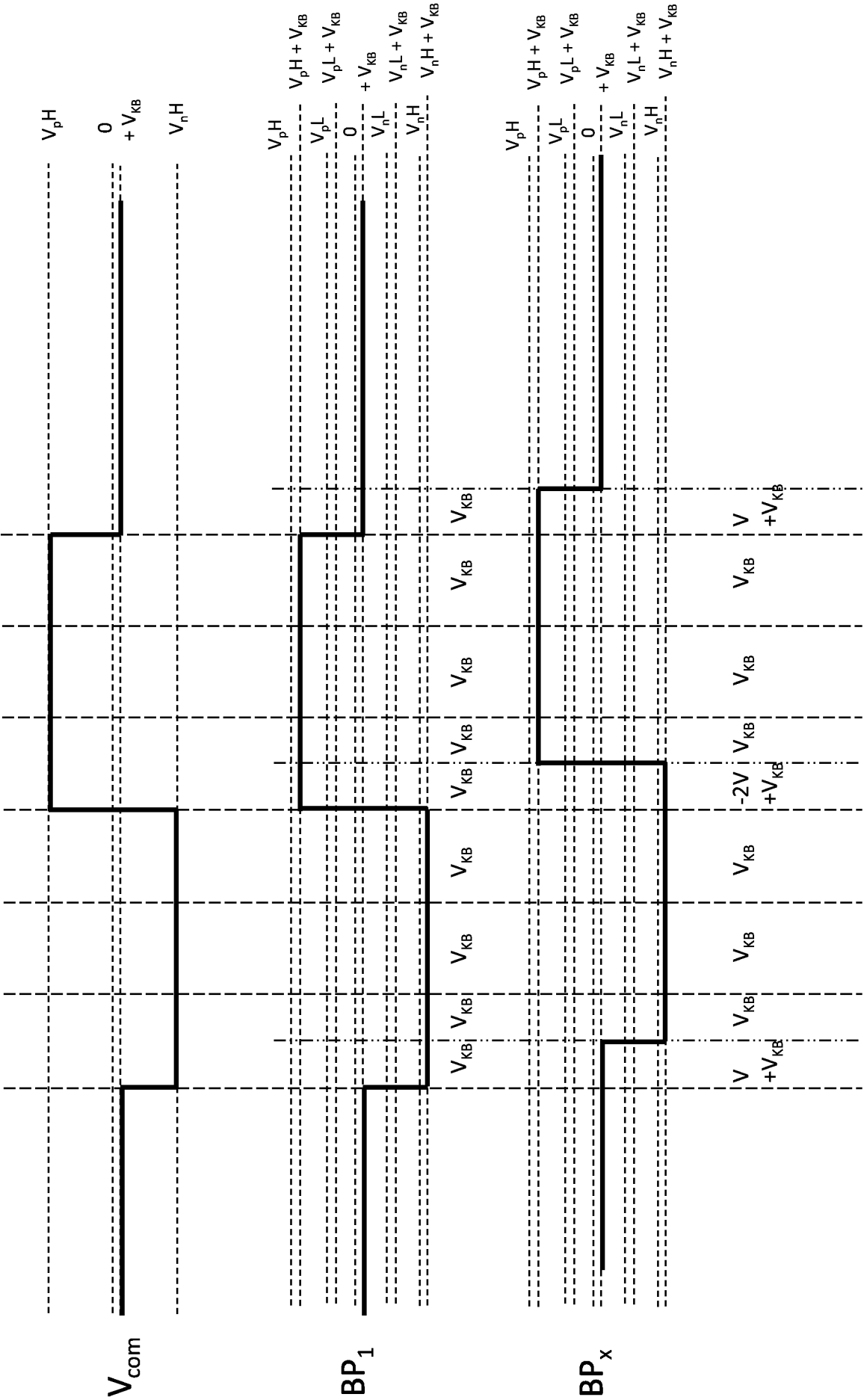
Fig. 13

A. CLASSIFICATION OF SUBJECT MATTER

G09G 3/34(2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
G09G 3/34; G09G 5/10; G09G 5/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Korean utility models and applications for utility models
Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
eKOMPASS(KIPO internal) & keywords: electro-optic display, electrophoretic, driving phase, polarity, amplitude, duration, impulse offset, zero

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2008-0204399 A1 (UK-CHUL CHOI et al.) 28 August 2008 See paragraphs [0079], [0082], [0091]-[0092], [0101]-[0127]; claim 1; and figures 2, 11.	1-17
A	US 6504524 B1 (HOLLY GATES et al.) 07 January 2003 See column 15, line 63 - column 16, line 27; and figures 3D-3F.	1-17
A	US 2009-0267970 A1 (JIALOCK WONG et al.) 29 October 2009 See paragraphs [0027]-[0041]; and figure 2.	1-17
A	US 2011-0292026 A1 (MARK THOMAS JOHNSON) 01 December 2011 See paragraphs [0058]-[0065]; and figure 3.	1-17
A	US 2012-0320017 A1 (ROBERT SPRAGUE et al.) 20 December 2012 See paragraphs [0067]-[0075]; and figures 5a-6.	1-17

☐ Further documents are listed in the continuation of Box C.   ☒ See patent family annex.

* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents,such combination being obvious to a person skilled in the art "&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
08 June 2017 (08.06.2017)	**09 June 2017 (09.06.2017)**

Name and mailing address of the ISA/KR	Authorized officer
International Application Division Korean Intellectual Property Office 189 Cheongsa-ro, Seo-gu, Daejeon, 35208, Republic of Korea	CHIN, Sang Bum
Facsimile No. +82-42-481-8578	Telephone No. +82-42-481-8398

Form PCT/ISA/210 (second sheet) (January 2015)

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2008-0204399 A1	28/08/2008	KR 10-2008-0079383 A	01/09/2008
US 6504524 B1	07/01/2003	AU 1996-69550 B2	26/10/2000
		AU 1999-31904 A1	11/10/1999
		AU 1999-34875 A1	01/11/1999
		AU 1999-35526 A1	01/11/1999
		AU 1999-37678 A1	16/11/1999
		AU 1999-39872 A1	29/11/1999
		AU 1999-47039 A1	10/01/2000
		AU 1999-50946 A1	01/02/2000
		AU 1999-50948 A1	01/02/2000
		AU 1999-52243 A1	14/02/2000
		AU 2002-230520 A1	11/06/2002
		AU 2002-366174 A1	10/06/2003
		AU 2003-239619 A1	31/12/2003
		AU 2004-285022 A1	12/05/2005
		CA 2300827 A1	04/03/1999
		CA 2300830 A1	04/03/1999
		CA 2300830 C	10/04/2007
		CA 2300849 A1	04/03/1999
		CA 2320788 A1	23/09/1999
		CA 2321131 A1	21/10/1999
		CA 2321131 C	08/04/2008
		CA 2323879 A1	21/10/1999
		CA 2323879 C	16/01/2007
		CA 2329173 A1	04/11/1999
		CA 2330950 A1	18/11/1999
		CA 2333358 A1	29/12/1999
		CA 2336101 A1	20/01/2000
		CA 2336596 A1	20/01/2000
		CA 2336744 A1	03/02/2000
		CA 2497391 A1	17/08/2006
		CA 2544350 A1	12/05/2005
		CA 2553137 A1	29/01/1998
		CA 2612822 A1	12/01/2006
		CN 100437714 C	26/11/2008
		CN 100504997 C	24/06/2009
		CN 100505005 C	24/06/2009
		CN 101027711 A	29/08/2007
		CN 101027711 C	05/11/2008
		CN 101261812 A	10/09/2008
		CN 101261812 B	08/12/2010
		CN 101373581 A	25/02/2009
		CN 101373581 B	16/07/2014
		CN 101387809 A	18/03/2009
		CN 101390148 A	18/03/2009
		CN 101390148 B	06/07/2011
		CN 101393369 A	25/03/2009
		CN 101393369 B	27/03/2013

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		CN 101430864 A	13/05/2009
		CN 101430864 B	07/03/2012
		CN 101601080 A	09/12/2009
		CN 101601080 B	23/10/2013
		CN 101676980 A	24/03/2010
		CN 101676980 B	04/06/2014
		CN 101800033 A	11/08/2010
		CN 101800033 B	29/05/2013
		CN 101800034 A	11/08/2010
		CN 101800034 B	06/03/2013
		CN 101826304 A	08/09/2010
		CN 101826304 B	14/03/2012
		CN 101859544 A	13/10/2010
		CN 101859544 B	04/07/2012
		CN 101882423 A	10/11/2010
		CN 101882423 B	12/02/2014
		CN 102027528 A	20/04/2011
		CN 102027528 B	27/08/2014
		CN 102074200 A	25/05/2011
		CN 102074200 B	28/11/2012
		CN 102106025 A	22/06/2011
		CN 102640043 A	15/08/2012
		CN 102640043 B	01/10/2014
		CN 102768822 A	07/11/2012
		CN 102768822 B	02/12/2015
		CN 102789758 A	21/11/2012
		CN 102789758 B	18/05/2016
		CN 102789764 A	21/11/2012
		CN 102789764 B	27/05/2015
		CN 102834857 A	19/12/2012
		CN 102834857 B	02/03/2016
		CN 102939561 A	20/02/2013
		CN 103474034 A	25/12/2013
		CN 104238227 A	24/12/2014
		CN 105654889 A	08/06/2016
		CN 1500229 A	26/05/2004
		CN 1500229 C	25/10/2006
		CN 1589462 A	02/03/2005
		CN 1589462 B	27/03/2013
		CN 1659618 A	24/08/2005
		CN 1659618 C	26/11/2008
		CN 1768366 A	03/05/2006
		CN 1768366 C	24/12/2008
		CN 1813279 A	02/08/2006
		CN 1813279 C	02/08/2006
		CN 1813281 A	02/08/2006
		CN 1813281 C	20/08/2008
		CN 1890594 A	03/01/2007
		CN 1890594 C	19/11/2008
		CN 1938746 A	28/03/2007

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		CN 1938746 C	28/03/2007
		EP 0839356 A2	20/03/2002
		EP 1010035 A1	21/06/2000
		EP 1010035 B1	27/10/2004
		EP 1010036 A1	21/06/2000
		EP 1010036 B1	23/06/2004
		EP 1010037 A1	21/06/2000
		EP 1010037 B1	07/05/2003
		EP 1064584 A1	03/01/2001
		EP 1064584 B1	19/05/2004
		EP 1070276 A1	24/01/2001
		EP 1070276 B1	01/06/2005
		EP 1075670 A1	14/02/2001
		EP 1075670 B1	17/12/2008
		EP 1078331 A2	28/02/2001
		EP 1093600 A1	25/04/2001
		EP 1093600 B1	15/09/2004
		EP 1095354 A1	02/05/2001
		EP 1095354 B1	27/11/2002
		EP 1099207 A1	16/05/2001
		EP 1099207 B1	27/03/2002
		EP 1105772 A1	13/06/2001
		EP 1105772 B1	23/06/2004
		EP 1145072 A2	17/10/2001
		EP 1145072 A3	02/05/2002
		EP 1145072 B1	07/05/2003
		EP 1231500 A2	14/08/2002
		EP 1231500 A3	09/07/2003
		EP 1231500 B1	07/03/2007
		EP 1335211 A2	13/08/2003
		EP 1335211 A3	26/11/2003
		EP 1335211 B1	03/06/2015
		EP 1340216 A2	03/09/2003
		EP 1370905 A1	17/12/2003
		EP 1370905 B1	05/01/2011
		EP 1390810 A1	25/02/2004
		EP 1390810 B1	26/04/2006
		EP 1393122 A1	03/03/2004
		EP 1446791 A2	18/08/2004
		EP 1446791 B1	09/09/2015
		EP 1482355 A2	01/12/2004
		EP 1482355 A3	20/04/2005
		EP 1482355 B1	27/12/2006
		EP 1507165 A1	16/02/2005
		EP 1512137 A2	09/03/2005
		EP 1557714 A2	27/07/2005
		EP 1557714 A3	28/09/2005
		EP 1557714 B1	06/11/2013
		EP 1614097 A1	11/01/2006
		EP 1623405 A2	08/02/2006

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		EP 1623405 B1	29/07/2015
		EP 1639574 A1	29/03/2006
		EP 1639574 B1	22/04/2015
		EP 1666964 A2	07/06/2006
		EP 1666964 A3	14/03/2007
		EP 1671172 A2	21/06/2006
		EP 1671172 A4	22/08/2007
		EP 1680406 A1	19/07/2006
		EP 1697786 A2	06/09/2006
		EP 1735652 A2	27/12/2006
		EP 1735652 B1	11/05/2016
		EP 1743316 A2	17/01/2007
		EP 1754995 A2	21/02/2007
		EP 1754995 A3	23/05/2007
		EP 1754995 B1	04/04/2012
		EP 1772768 A2	11/04/2007
		EP 1772768 A3	16/07/2008
		EP 1784813 A2	16/05/2007
		EP 1784813 B1	14/12/2016
		EP 2122405 A2	25/11/2009
		EP 2122405 B1	16/11/2016
		EP 2122405 B8	21/12/2016
		EP 2126885 A1	02/12/2009
		EP 2126885 B1	02/01/2013
		EP 2548077 A2	23/01/2013
		EP 2555182 A1	06/02/2013
		EP 2556499 A2	13/02/2013
		JP 03833266 B2	11/10/2006
		JP 03901197 B2	04/04/2007
		JP 04105107 B2	25/06/2008
		JP 04188091 B2	26/11/2008
		JP 04209436 B2	14/01/2009
		JP 04410135 B2	03/02/2010
		JP 04460149 B2	12/05/2010
		JP 04460150 B2	12/05/2010
		JP 04460151 B2	12/05/2010
		JP 04460507 B2	12/05/2010
		JP 04460508 B2	12/05/2010
		JP 04568429 B2	27/10/2010
		JP 04568477 B2	27/10/2010
		JP 04599349 B2	15/12/2010
		JP 04615860 B2	19/01/2011
		JP 04651193 B2	16/03/2011
		JP 04651383 B2	16/03/2011
		JP 04664501 B2	06/04/2011
		JP 04672727 B2	20/04/2011
		JP 04740943 B2	03/08/2011
		JP 04776532 B2	21/09/2011
		JP 04790622 B2	12/10/2011
		JP 04848280 B2	28/12/2011

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		JP 04852478 B2	11/01/2012
		JP 04852572 B2	11/01/2012
		JP 04889129 B2	07/03/2012
		JP 04938266 B2	23/05/2012
		JP 04940227 B2	30/05/2012
		JP 05005800 B2	22/08/2012
		JP 05008791 B2	22/08/2012
		JP 05047331 B2	10/10/2012
		JP 05117470 B2	16/01/2013
		JP 05250580 B2	31/07/2013
		JP 05421965 B2	19/02/2014
		JP 05421966 B2	19/02/2014
		JP 05542283 B2	09/07/2014
		JP 05560294 B2	23/07/2014
		JP 05580891 B2	27/08/2014
		JP 05615851 B2	29/10/2014
		JP 05616207 B2	29/10/2014
		JP 05618811 B2	05/11/2014
		JP 05632164 B2	26/11/2014
		JP 05632861 B2	26/11/2014
		JP 05641721 B2	17/12/2014
		JP 05706606 B2	22/04/2015
		JP 05718651 B2	13/05/2015
		JP 05734395 B2	17/06/2015
		JP 05739319 B2	24/06/2015
		JP 05758440 B2	05/08/2015
		JP 05785584 B2	30/09/2015
		JP 05805167 B2	04/11/2015
		JP 05869776 B2	24/02/2016
		JP 05873241 B2	01/03/2016
		JP 05904690 B2	20/04/2016
		JP 05904931 B2	20/04/2016
		JP 05904990 B2	20/04/2016
		JP 05905061 B2	20/04/2016
		JP 05908048 B2	26/04/2016
		JP 05928840 B2	01/06/2016
		JP 05931038 B2	08/06/2016
		JP 06075934 B2	08/02/2017
		JP 06076942 B2	08/02/2017
		JP 11-502950 A	09/03/1999
		JP 2001-500172 A	09/01/2001
		JP 2002-221918 A	09/08/2002
		JP 2002-504696 A	12/02/2002
		JP 2002-507765 A	12/03/2002
		JP 2002-511606 A	16/04/2002
		JP 2002-511607 A	16/04/2002
		JP 2002-513169 A	08/05/2002
		JP 2002-514793 A	21/05/2002
		JP 2002-520655 A	09/07/2002
		JP 2004-252465 A	09/09/2004

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		JP 2004-500583 A	08/01/2004
		JP 2004-500584 A	08/01/2004
		JP 2004-522179 A	22/07/2004
		JP 2004-524570 A	12/08/2004
		JP 2004-526199 A	26/08/2004
		JP 2004-526210 A	26/08/2004
		JP 2005-227795 A	25/08/2005
		JP 2005-509925 A	14/04/2005
		JP 2005-530201 A	06/10/2005
		JP 2006-003906 A	05/01/2006
		JP 2006-003921 A	05/01/2006
		JP 2006-011483 A	12/01/2006
		JP 2006-058901 A	02/03/2006
		JP 2006-189899 A	20/07/2006
		JP 2006-522372 A	28/09/2006
		JP 2006-525557 A	09/11/2006
		JP 2007-041624 A	15/02/2007
		JP 2007-249230 A	27/09/2007
		JP 2007-249231 A	27/09/2007
		JP 2007-293365 A	08/11/2007
		JP 2007-328372 A	20/12/2007
		JP 2007-508588 A	05/04/2007
		JP 2007-512242 A	17/05/2007
		JP 2007-512579 A	17/05/2007
		JP 2007-518493 A	12/07/2007
		JP 2007-529018 A	18/10/2007
		JP 2007-531009 A	01/11/2007
		JP 2007-531016 A	01/11/2007
		JP 2008-217042 A	18/09/2008
		JP 2008-233939 A	02/10/2008
		JP 2008-242495 A	09/10/2008
		JP 2008-509449 A	27/03/2008
		JP 2009-086687 A	23/04/2009
		JP 2009-104176 A	14/05/2009
		JP 2009-116365 A	28/05/2009
		JP 2009-116366 A	28/05/2009
		JP 2009-116367 A	28/05/2009
		JP 2009-139960 A	25/06/2009
		JP 2010-002933 A	07/01/2010
		JP 2010-044404 A	25/02/2010
		JP 2010-049272 A	04/03/2010
		JP 2010-117720 A	27/05/2010
		JP 2010-134468 A	17/06/2010
		JP 2010-204671 A	16/09/2010
		JP 2010-217916 A	30/09/2010
		JP 2010-224576 A	07/10/2010
		JP 2010-244066 A	28/10/2010
		JP 2010-517115 A	20/05/2010
		JP 2011-008271 A	13/01/2011
		JP 2011-008288 A	13/01/2011

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		JP 2011-076103 A	14/04/2011
		JP 2011-085944 A	28/04/2011
		JP 2011-100151 A	19/05/2011
		JP 2011-100155 A	19/05/2011
		JP 2011-102994 A	26/05/2011
		JP 2011-118434 A	16/06/2011
		JP 2011-128625 A	30/06/2011
		JP 2011-170389 A	01/09/2011
		JP 2011-180620 A	15/09/2011
		JP 2011-232760 A	17/11/2011
		JP 2011-242809 A	01/12/2011
		JP 2011-242810 A	01/12/2011
		JP 2011-520137 A	14/07/2011
		JP 2012-032838 A	16/02/2012
		JP 2012-068686 A	05/04/2012
		JP 2012-078860 A	19/04/2012
		JP 2012-078867 A	19/04/2012
		JP 2012-078874 A	19/04/2012
		JP 2012-098733 A	24/05/2012
		JP 2012-133380 A	12/07/2012
		JP 2012-137786 A	19/07/2012
		JP 2012-232999 A	29/11/2012
		JP 2012-238032 A	06/12/2012
		JP 2012-530283 A	29/11/2012
		JP 2013-047831 A	07/03/2013
		JP 2013-057975 A	28/03/2013
		JP 2013-140368 A	18/07/2013
		JP 2013-178580 A	09/09/2013
		JP 2013-218342 A	24/10/2013
		JP 2013-522676 A	13/06/2013
		JP 2013-531804 A	08/08/2013
		JP 2014-006558 A	16/01/2014
		JP 2014-059577 A	03/04/2014
		JP 2014-059582 A	03/04/2014
		JP 2014-063176 A	10/04/2014
		JP 2014-063185 A	10/04/2014
		JP 2014-074913 A	24/04/2014
		JP 2014-130371 A	10/07/2014
		JP 2014-142668 A	07/08/2014
		JP 2014-160283 A	04/09/2014
		JP 2014-160284 A	04/09/2014
		JP 2014-186357 A	02/10/2014
		JP 2014-197229 A	16/10/2014
		JP 2014-199463 A	23/10/2014
		JP 2014-199466 A	23/10/2014
		JP 2014-199467 A	23/10/2014
		JP 2014-211651 A	13/11/2014
		JP 2015-007793 A	15/01/2015
		JP 2015-018255 A	29/01/2015
		JP 2015-028649 A	12/02/2015

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
		JP 2015-028665 A	12/02/2015
		JP 2015-064588 A	09/04/2015
		JP 2015-099376 A	28/05/2015
US 2009-0267970 A1	29/10/2009	US 8462102 B2	11/06/2013
US 2011-0292026 A1	01/12/2011	AT 322063 T	15/04/2006
		AU 2003-260855 A1	04/05/2004
		CN 100517449 C	22/07/2009
		CN 1689066 A	26/10/2005
		DE 60304368 T2	07/12/2006
		EP 1554714 A1	20/07/2005
		EP 1554714 B1	29/03/2006
		JP 04947901 B2	06/06/2012
		JP 2006-503321 A	26/01/2006
		KR 10-2005-0061532 A	22/06/2005
		TW 200421246 A	16/10/2004
		US 2006-0050361 A1	09/03/2006
		US 7995029 B2	09/08/2011
		US 8350803 B2	08/01/2013
		WO 2004-036537 A1	29/04/2004
US 2012-0320017 A1	20/12/2012	US 2008-303780 A1	11/12/2008
		US 2016-335956 A1	17/11/2016
		US 9373289 B2	21/06/2016