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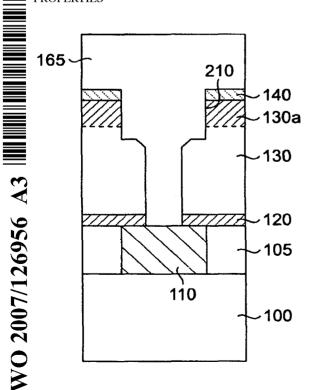
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(57) Abstract: A method is provided for fabricating a damascene interconnection The method begins by forming on a substrate (100) a porous dielectric layer (130) and imparting a porogen material into an upper portion of the porous dielectric layer to define a less porous dielectric sublayer (130a) within the dielectric layer A capping layer (140) is formed on the less porous dielectric sublayer and a resist pattern (145) is formed over the capping layer to define a first interconnect opening (150) The capping layer and the dielectric layer ar etched through the resist pattern to form the first interconnect opening The resist pattern is removed and an interconnection is formed by filling the first interconnect opening with conductive material (165) The interconnection is plana π zed to remove excess conductive material



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C. DOC	UMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where a	paronriate of the relevant passages	Relevant to claim No.
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E	US 7,166,531 B1 (Van Den Hoek et al) 23 January 2007 (23.01.2007), fig. 1C with related		1-15
1	text		
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