



(12) **United States Design Patent** (10) **Patent No.:** **US D869,310 S**
Nagasawa et al. (45) **Date of Patent:** **** Dec. 10, 2019**

(54) **SAMPLE ANALYZER**

(56) **References Cited**

(71) Applicant: **Otsuka Electronics Co., Ltd.**, Osaka (JP)

U.S. PATENT DOCUMENTS

(72) Inventors: **Hiroya Nagasawa**, Kyoto (JP); **Ikuo Wakayama**, Osaka (JP); **Kazuyoshi Kamekawa**, Kyoto (JP)

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(73) Assignee: **Otsuka Electronics Co., Ltd.**, Osaka (JP)

* cited by examiner

(**) Term: **15 Years**

Primary Examiner — Antoine Duval Davis

(21) Appl. No.: **29/641,363**

(74) *Attorney, Agent, or Firm* — Fitch, Even, Tabin & Flannery LLP

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(30) **Foreign Application Priority Data**

(57) **CLAIM**

Sep. 29, 2017 (JP) 2017-021531

We claim the ornamental design for a sample analyzer, as shown and described.

(51) **LOC (12) Cl.** **10-04**

(52) **U.S. Cl.**
USPC **D10/81; D24/216**

DESCRIPTION

(58) **Field of Classification Search**

USPC D10/81; D24/232, 233, 234, 216–230
CPC .. G01N 21/00; G01N 21/01; G01N 2201/022;
G01N 2201/0221; G01N 2201/0222;
G01N 2201/0224; G01N 2201/0225;
G01N 2201/0227; G01N 2201/0228;
G01N 2201/024; G01N 2201/0245; G01N
2021/0112–21/958; G01N
2021/0106–2021/9586; G01N
35/00–35/1097; G01N
2035/00019–2035/1093; G01N
2204/022–2204/0228

FIG. 1 is a front, top, and left side perspective view of a sample analyzer showing our new design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a top plan view thereof;
FIG. 4 is a left side elevational view thereof;
FIG. 5 is a right side elevational view thereof;
FIG. 6 a rear elevational view thereof; and,
FIG. 7 is a bottom plan view thereof.
The broken lines illustrate portions of the sample analyzer and form no part of the claimed design.

See application file for complete search history.

1 Claim, 7 Drawing Sheets

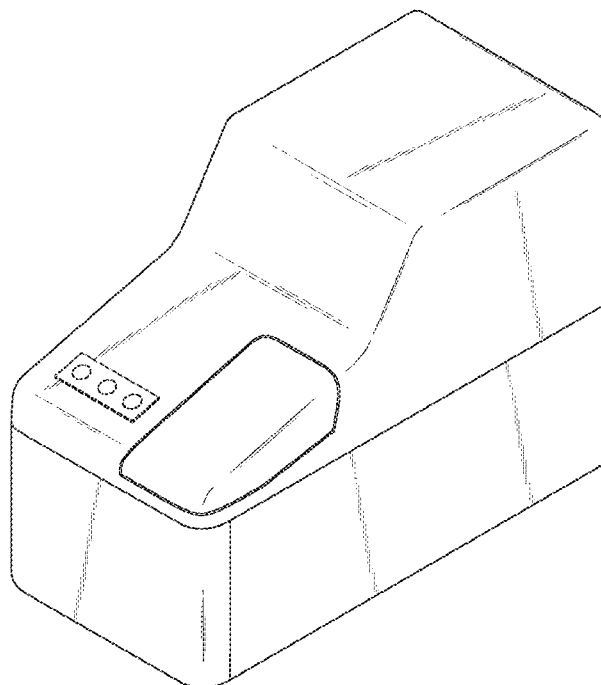


FIG. 1

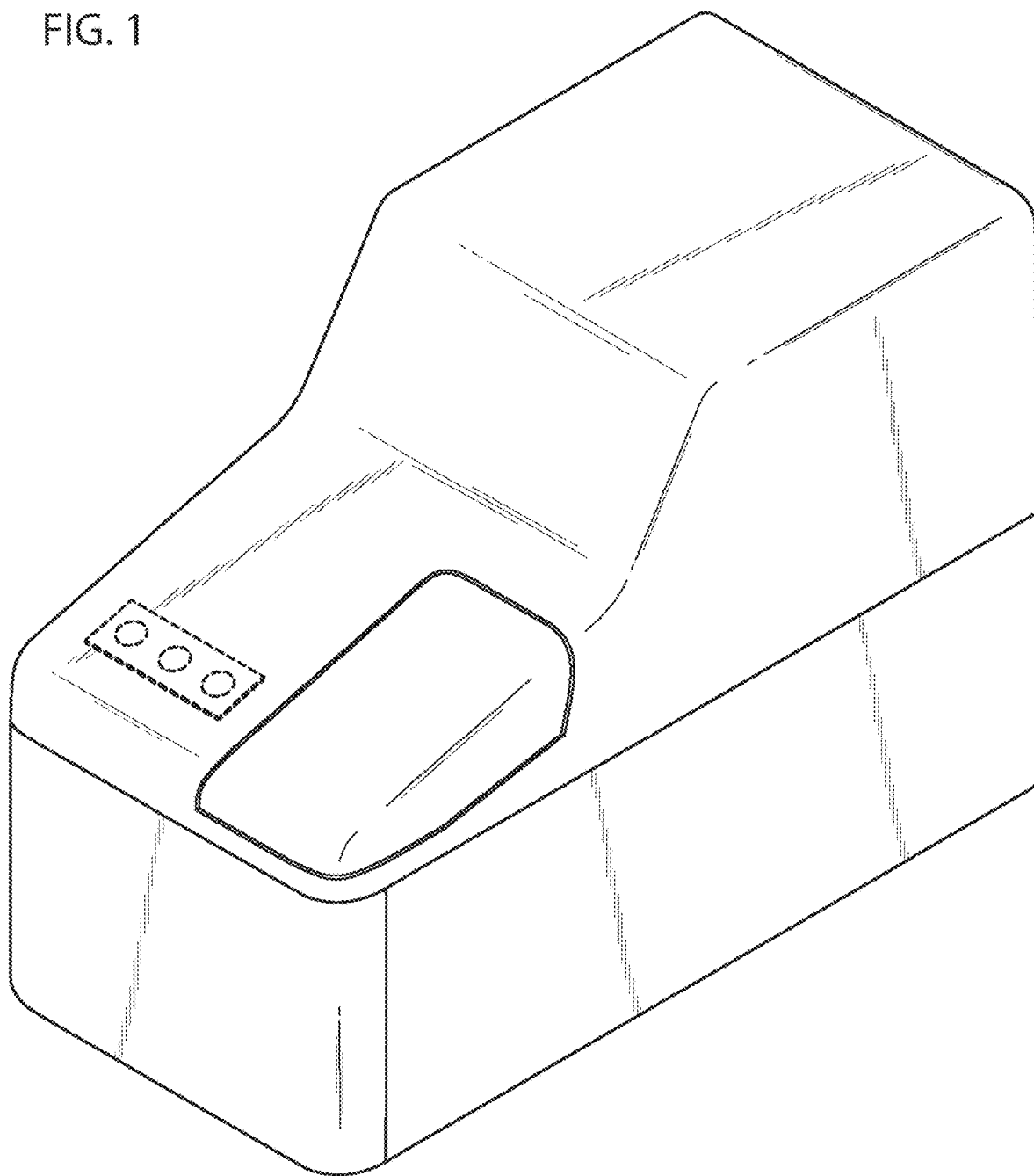


Fig. 2

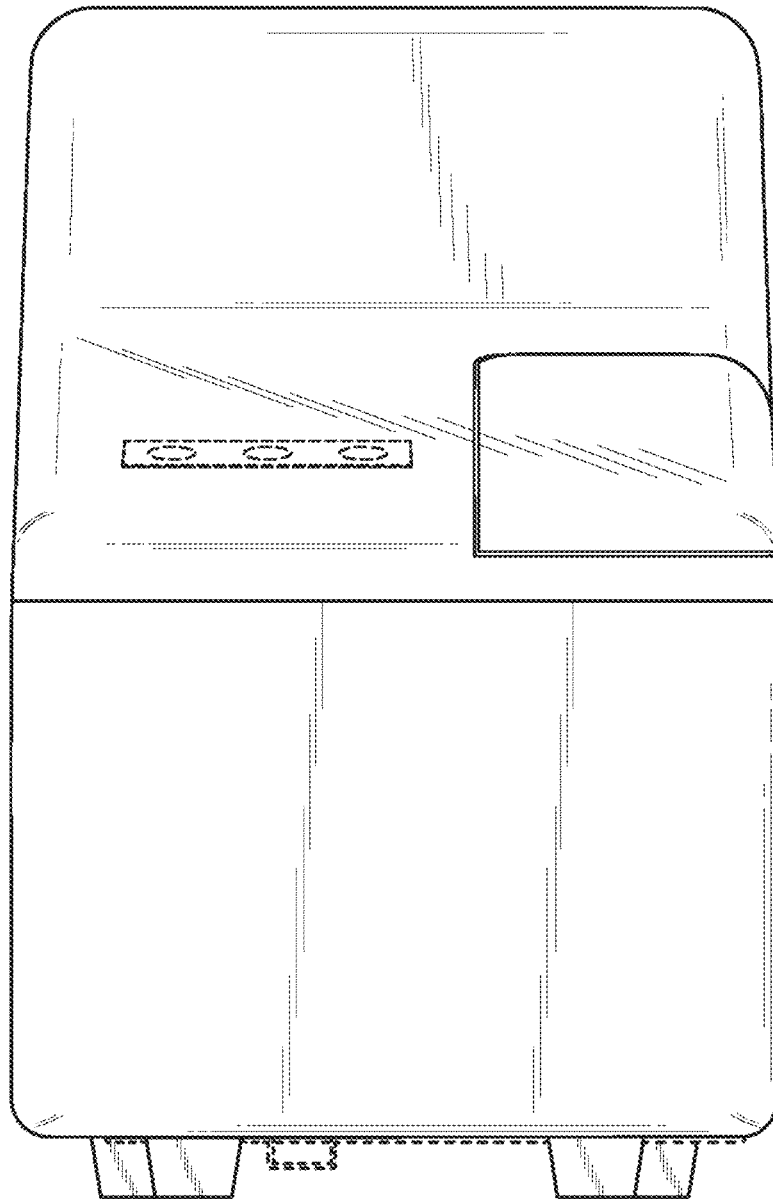
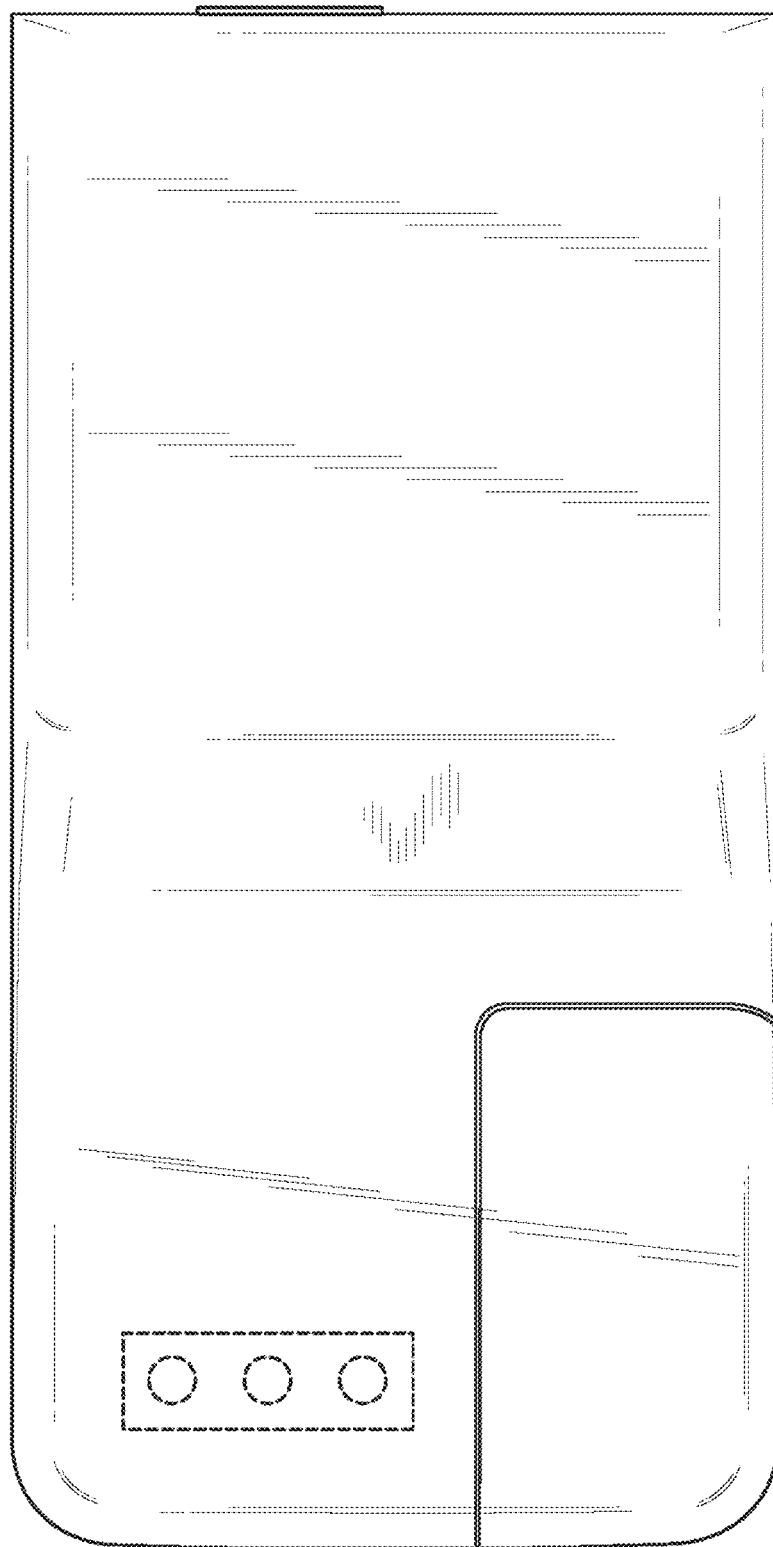


FIG. 3



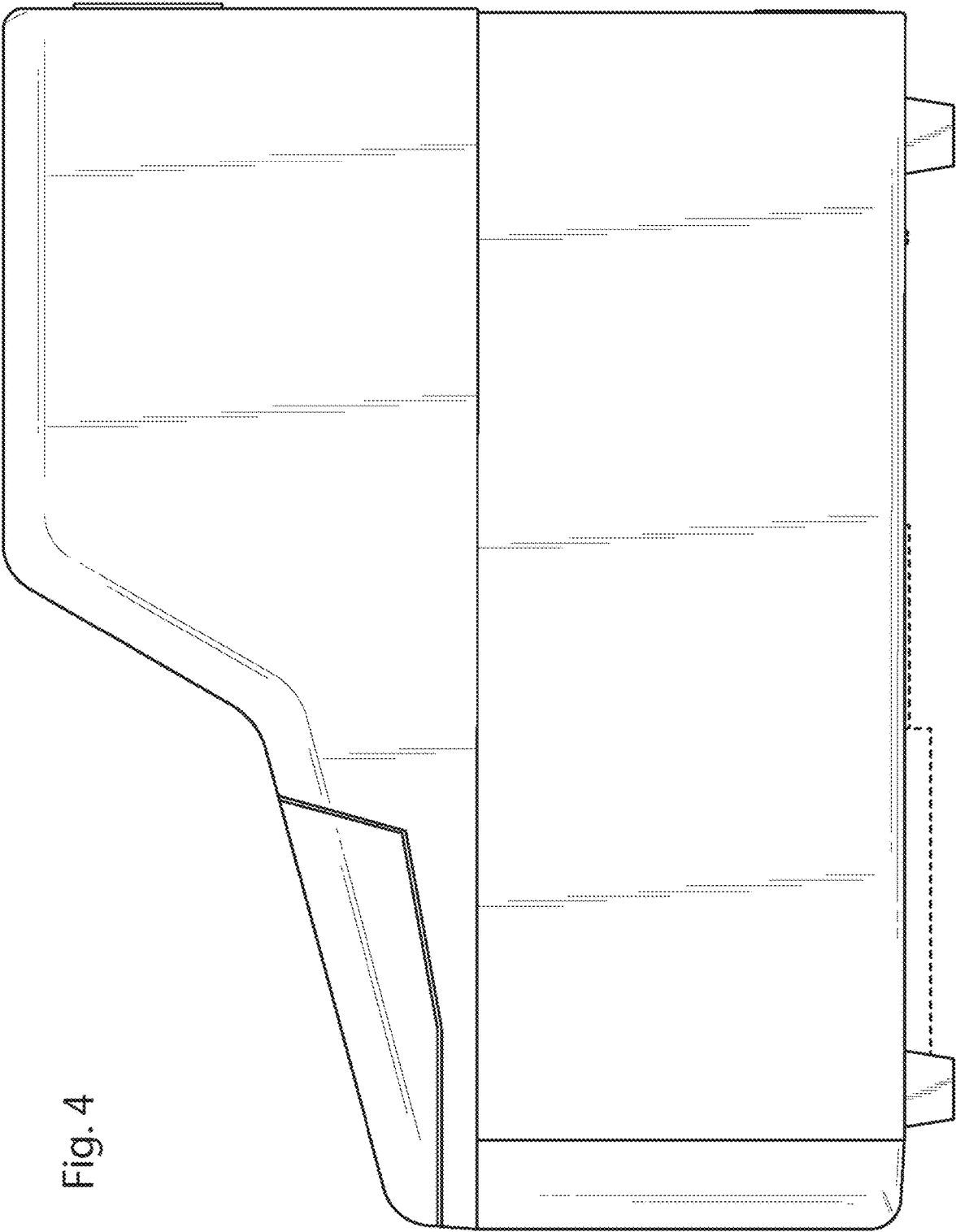


Fig. 4

Fig. 5

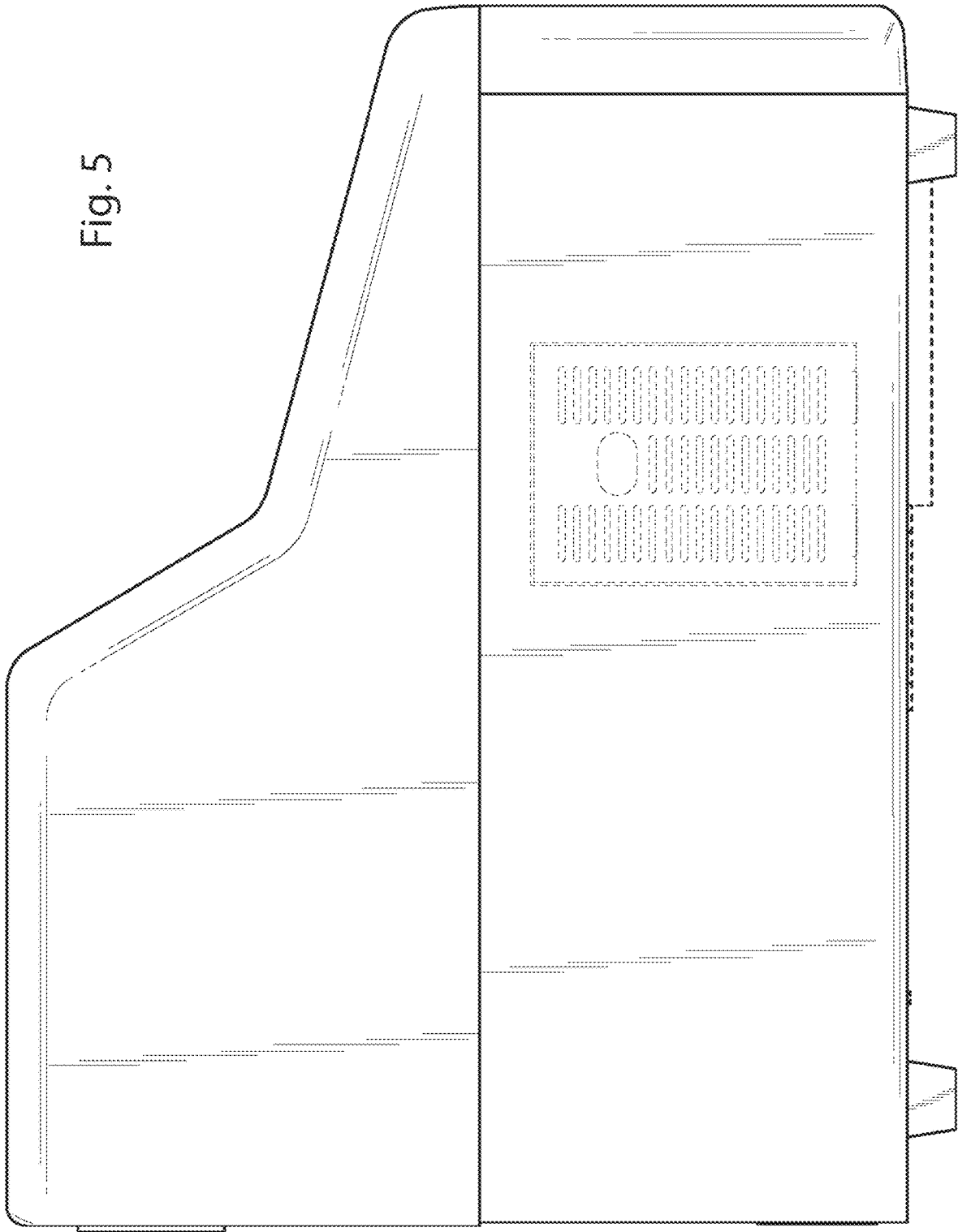


Fig. 6

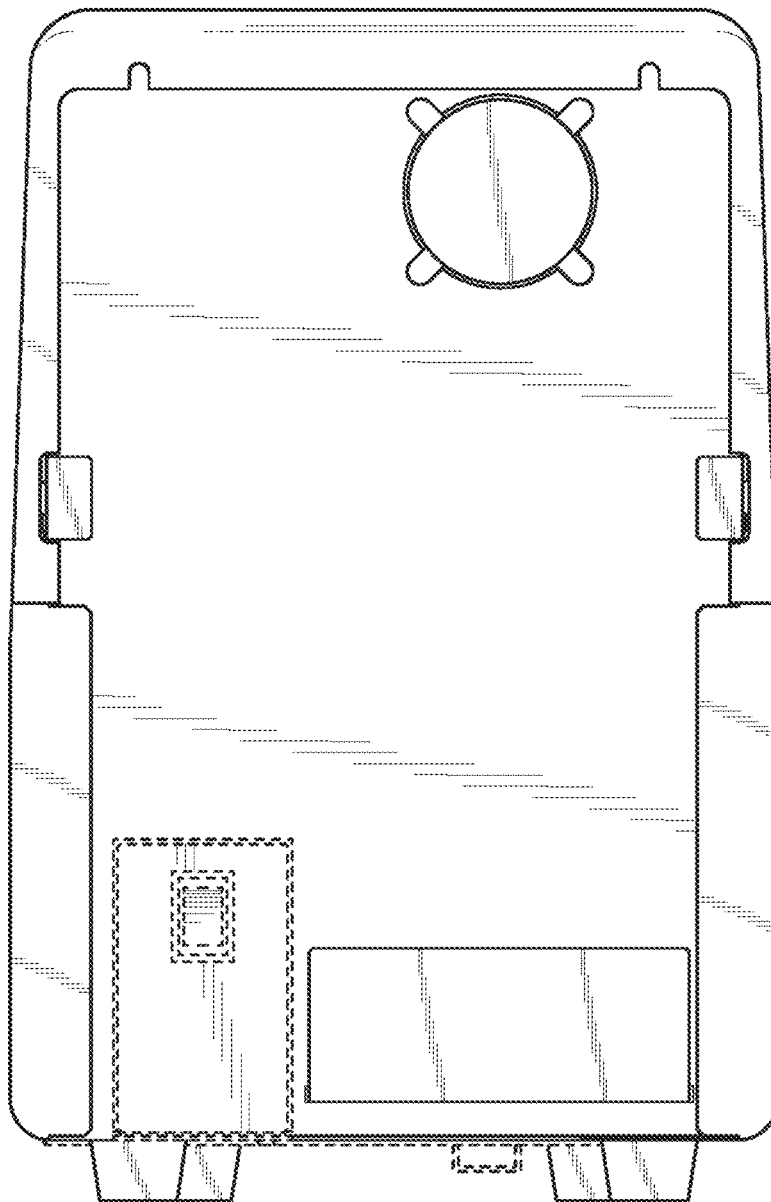


Fig. 7

