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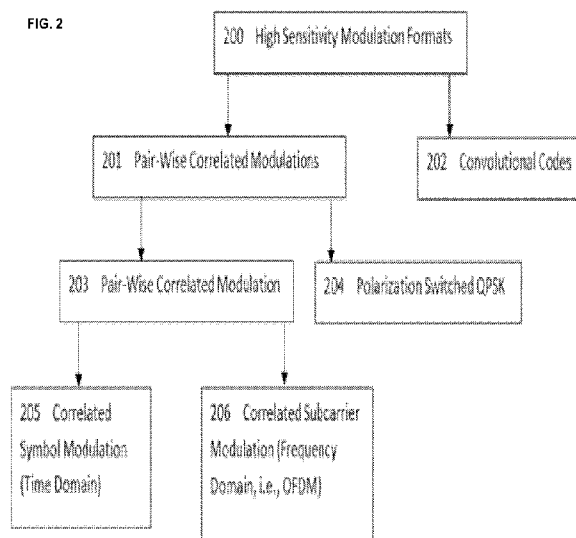
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(54) Title: PAIR-WISE SYMBOL CORRELATED HIGH RECEIVER SENSITIVITY MODULATION FORMAT

FIG. 2



(57) Abstract: The invention is directed to a novel computer implemented method for finding a modulation format that has better receiver sensitivity than modulation formats that are currently being used, with a correlated symbol modulation in which neighboring symbols are coded and decoded together to increase receiver sensitivity.

**PAIR-WISE SYMBOL CORRELATED
HIGH RECEIVER SENSITIVITY MODULATION FORMAT**

[0001] This application claims the benefit of U.S. Provisional Application Nos. 61/709,538, filed October 4, 2013 and 61/710,800, filed October 8, 2012, of which the contents are incorporated herein by reference.

BACKGROUND OF THE INVENTION

[0002] The present invention relates generally to optics and, more particularly, to a pair-wise symbol correlated high receiver sensitivity modulation format.

[0003] The present invention attempts to solve the problem of finding a modulation format that would have better receiver sensitivity than modulation formats that are currently being used. In current, ultra long-haul transmission systems, especially in legacy links, the modulation formats that are most frequently used are BPSK and QPSK. The current invention introduces a new modulation format still based on QPSK constellation however, with a better receiver sensitivity than both BPSK and QPSK. Having a better receiver sensitivity allows for extending the transmission distance at the same capacity. It also allows for reducing the total energy consumption.

[0004] In transmission systems, it is very critical to choose the right kind of spectral efficiency for a given distance to maximize the capacity. In general the maximum achievable distance drops dramatically as the capacity is increased by increasing spectral efficiency. The present invention has the spectral efficiency of 1.5 b/s per polarization, which is 50% higher than BPSK, while maintaining the same receiver sensitivity. At the same bit rate, the current modulation format requires a lower baud-rate, in this case it will deliver the same capacity at a better receiver sensitivity, and therefore at a longer distance.

[0005] Since for a given transmission distance there is an optimum spectral efficiency that maximize the capacity it is important to adjust the spectral efficiency depending on the required transmission distance. The easiest way to adjust spectral efficiency is to switch between different modulation formats and constellation sizes. Since transceiver architectures depend heavily on the kind of modulation formats, it is very costly to design and operate transceivers which can modulate and demodulate multiple constellations. Therefore it is very cost effective to design a configuration that can easily switch between different modulation formats. One advantage of the proposed modulation format is that it requires a simple modification of transceivers designed for standard constellations.

[0006] Others have attempted to solve the above problem by reducing capacity by using simplest of the constellations such as BPSK. While others have introduced modulation formats with increased receiver sensitivity such as polarization-switched QPSK. However, compared to polarization-switched QPSK, the new modulation format has the following advantages:

1. It can use the same DSP platform as the standard QPSK transceivers.
2. The two polarization tributaries are independent which makes the transceiver design simpler and more robust.
3. It is possible to use polarization bit-interleaving which provides better nonlinearity tolerance especially at legacy links with lower baud rates
4. It is possible to have this modulation format in single polarization as opposed to polarization-switching.
5. It is possible to extend the benefits of this new modulation format to higher constellations.
6. This method can be implemented in time domain between two symbols which may or may not be adjacent, or in frequency domain between subcarriers as in the case of OFDM where the correlated subcarriers may be adjacent in frequency or not.

[0007] Accordingly, there is a need for finding a modulation format that would have better receiver sensitivity than modulation formats that are currently being used.

BRIEF SUMMARY OF THE INVENTION

[0008] In an aspect of the present invention, a computer implemented method for finding a modulation format with improved receiver sensitivity includes providing on a transmitter side of a communication system a pair-wise symbol bits correlation on incoming data bits; applying a quadrature phase shift keying QPSK constellation mapping to the pair-wise symbol bits correlation; and applying optical modulation the QPSK constellation mapped information for a transmitted signal.

[0009] In a preferred embodiment of the invention, the computer implemented method further includes receiving the transmitted signal at a QPSK receiver; applying a quadrature phase shift keying QPSK constellation de-mapping responsive to the QPSK receiver; and applying a pair-wise symbol bits decoding responsive to the QPSK constellation de-mapping to obtain the received bits.

[0010] In a similar aspect of the present invention, a computer system configured with instructions for finding a modulation format with improved receiver sensitivity includes providing on a transmitter side of a communication system a pair-wise symbol bits correlation on incoming data bits; applying a quadrature phase shift keying QPSK constellation mapping to the pair-wise symbol bits correlation; and applying optical modulation the QPSK constellation mapped information for a transmitted signal.

[0011] These and other advantages of the invention will be apparent to those of ordinary skill in the art by reference to the following detailed description and the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0012] Fig. 1a is a block diagram of a modified QPSK transmitter and receiver, in accordance with the invention;

[0013] Fig. 1b is diagram showing how 3 incoming bits are mapped onto 2 pairs of bits that are correlated, and then mapped onto QPSK constellations based on the constellation mapping shown in Fig. 1c, in accordance with the invention,

[0014] Fig. 1d is a diagram showing how the correlated-symbol symbols are decoded at the receiver after the standard QPSK DSP just before slicing, in accordance with the invention.

[0015] Fig. 2 is a diagram showing key aspects of the invention; and

[0016] Fig. 3 is a diagram showing a generic computer configuration for carrying out the invention.

DETAILED DESCRIPTION

[0017] The invention is directed to a novel computer implemented method for finding a modulation format that has better receiver sensitivity than modulation formats that are currently being used, with a correlated symbol modulation in which neighboring symbols are coded and decoded together to increase receiver sensitivity.

[0018] Figure 1a shows the flow diagram of the modified QPSK transmitter and the receiver. The red blocks show the modification required to convert a QPSK transceiver into correlated symbol transceiver. The correlated symbol modulation carries 1.5 bits per symbol, in other words only 3 bits per 2 symbols as opposed to the QPSK modulation where two symbols carry a total of 4 bits. The number of bits per symbol is reduced from 4 to 3 by introducing a correlation between each pair of symbols as it is described below.

[0019] Figure 1b shows how the 3 incoming bits are mapped onto 2 pairs of bits that are correlated, and then mapped onto QPSK constellations based on the constellation mapping shown in Fig. 1c. The incoming bits are grouped in three bits. Each three bits is mapped onto two QPSK symbols. The first two bits (black) is mapped directly to the first symbol. The bits carried

by the second symbol are determined by the first two bits and the third bit (red). If the third bit is 0, the second symbol also carries the same data as the first symbol. If the third bit is 1 the second symbol carries the opposite bits in the second symbol. Note that only each pair of symbols are correlated and neighboring pairs are completely independent as opposed to the correlations imposed by convolutional codes.

[0020] Figure 1d shows how the correlated-symbol symbols are decoded at the receiver after the standard QPSK DSP just before slicing. The received symbols first go through the standard QPSK DSP, and the symbol constellations are obtained. The symbol pairs are determined based on the pairing at the transmitter. From each symbol pair, another pair of constellation is obtained by first adding them together, and then subtracting them. Since each pair contains either the same bits or opposite bits, either the summation is zero, or the subtraction is, and the other constellation is just twice the as the first symbol of the pair as shown in the Fig 1d. Depending on whether the summation is larger than the subtraction the third bit (red) is determined first. The larger constellation also determines the first two bits (black). Since the summation or subtraction depending on the value of the third bit combines the power of the two symbols, all bits have 3 dB higher sensitivity compared to symbol by symbol detection. Compared to symbol by symbol detection, a 25% of the capacity is sacrificed however a 3 dB sensitivity is gained. Note that, capacity can be recovered by increasing the baud rate by 33% , which will reduce the sensitivity by 1.3 dB, in which case a sensitivity gain of about 1.7 will remain.

[0021] The above description would also hold for the case of OFDM, where the pair-wise correlation can be introduced between different subcarriers.

[0022] Referring to Fig. 2 and the above discussion, the invention technique for high-sensitivity modulation formats 200 entails a pair-wise correlated modulation 201 and convolutional codes 202. The pair-wise correlated modulations 201 include pair-wise correlated modulation 203 and polarization-switched quadrature phase shift keying QPSK 204. The pair-wise correlated modulation 203 entails correlated symbol modulation in the time domain 205 or

correlated subcarrier modulation in the frequency domain, i.e., orthogonal frequency division multiplexing OFDM 206.

[0023] The invention may be implemented in hardware, firmware or software, or a combination of the three. Preferably the invention is implemented in a computer program executed on a programmable computer having a processor, a data storage system, volatile and non-volatile memory and/or storage elements, at least one input device and at least one output device.

[0024] By way of example, a block diagram of a computer to support the system is discussed in FIG. 3. The computer preferably includes a processor, random access memory (RAM), a program memory (preferably a writable read-only memory (ROM) such as a flash ROM) and an input/output (I/O) controller coupled by a CPU bus. The computer may optionally include a hard drive controller which is coupled to a hard disk and CPU bus. Hard disk may be used for storing application programs, such as the present invention, and data. Alternatively, application programs may be stored in RAM or ROM. I/O controller is coupled by means of an I/O bus to an I/O interface. I/O interface receives and transmits data in one of or combination of analog or digital form over one or a number of communication links such as a serial link, local area network, wireless link, optical link and parallel link. Optionally, a display, a keyboard and a pointing device (mouse) may also be connected to I/O bus. Alternatively, separate connections (separate buses) may be used for I/O interface, display, keyboard and pointing device. Programmable processing system may be preprogrammed or it may be programmed (and reprogrammed) by downloading a program from another source (e.g., a floppy disk, CD-ROM, or another computer).

[0025] Each computer program is tangibly stored in a machine-readable storage media or device (e.g., program memory or magnetic disk) readable by a general or special purpose programmable computer, for configuring and controlling operation of a computer when the storage media or device is read by the computer to perform the procedures described herein. The inventive system may also be considered to be embodied in a computer-readable storage medium, configured with a computer program, where the storage medium so configured causes a

computer to operate in a specific and predefined manner to perform the functions described herein.

[0026] From the foregoing it can also be appreciated that the inventive modulation format has higher sensitivity than the most commonly used modulation formats such as DPBPSK and DPQPSK and that the inventive modulation format has similar sensitivity to modulation formats such as dual polarity-QPSK (DP-QPSK), however, the inventive modulation format also has the following advantages compared to polarization-switched QPSK.

- I. It requires only a small modification in the standard QPSK transceiver design.
- II. It allows for single polarization operation.
- III. If polarization multiplexing is used, there is no strict path matching between the two polarization tributaries, which makes the transmitter and receiver design simpler.
- IV. The modulation format can be applied in time domain or in the case OFDM it can be applied in the frequency domain.
- V. The basic idea can be applied to higher modulation formats to gain sensitivity but also to provide flexibility in the receiver sensitivity and bit rate by small modification of the transceiver architecture.

[0027] The foregoing is to be understood as being in every respect illustrative and exemplary, but not restrictive, and the scope of the invention disclosed herein is not to be determined from the Detailed Description, but rather from the claims as interpreted according to the full breadth permitted by the patent laws. Additional Information is provided in the accompanying "Appendix to the Specification". It is to be understood that the embodiments shown and described herein are only illustrative of the principles of the present invention and that those skilled in the art may implement various modifications without departing from the scope and spirit of the invention. Those skilled in the art could implement various other feature combinations without departing from the scope and spirit of the invention.

CLAIMS:

1. A computer implemented method for finding a modulation format with improved receiver sensitivity, comprising the steps of:
 - providing on a transmitter side of a communication system a pair-wise symbol bits correlation on incoming data bits;
 - applying a quadrature phase shift keying QPSK constellation mapping to the pair-wise symbol bits correlation; and
 - applying optical modulation the QPSK constellation mapped information for a transmitted signal.
2. The computer implemented method for claim 1, further comprising:
 - receiving the transmitted signal at a QPSK receiver;
 - applying a quadrature phase shift keying QPSK constellation de-mapping responsive to the QPSK receiver; and
 - applying a pair-wise symbol bits decoding responsive to the QPSK constellation de-mapping to obtain the received bits.
3. The computer implemented method of claim 1, wherein a correlated symbol modulation carries 1.5 bits per symbol, in other words only 3 bits per 2 symbols as opposed to the QPSK modulation where two symbols carry a total of 4 bits. The number of bits per symbol is reduced from 4 to 3 by introducing a correlation between each pair of symbols.
4. The computer implemented method of claim 1, wherein 3 incoming bits are mapped onto 2 pairs of bits that are correlated, and then mapped onto QPSK constellations based on the constellation mapping, incoming bits are grouped in three bits with each three bits being mapped onto two QPSK symbols.

5. The computer implemented of claim 4, wherein first two bits are mapped directly to a first symbol and bits carried by a second symbol are determined by the first two bits and a third bit.
6. The computer implemented method of claim 5, wherein if a third bit is 0, the second symbol also carries the same data as the first symbol, if the third bit is 1 the second symbol carries the opposite bits in the second symbol, only each pair of symbols are correlated and neighboring pairs are completely independent as opposed to correlations imposed by convolutional codes.
7. The computer implemented method of claim 2, wherein the correlated-symbol symbols are decoded at the receiver after the standard QPSK DSP just before slicing. comprises the received symbols first going through a standard QPSK DSP for obtaining symbol constellations.
8. The computer implemented of claim 7, wherein the symbol pairs are determined based on a pairing at the transmitter and from each symbol pair, another pair of constellation being obtained by first adding them together, and then subtracting them.
9. The computer implemented method of claim 8, wherein since each pair of the constellation contains either the same bits or opposite bits, either the summation is zero, or the subtraction is, and the other constellation is just twice the as the first symbol of the pair.
10. The computer implemented method of claim 9, wherein depending on whether the summation is larger than the subtraction the third bit is determined first and the larger constellation determining the first two bits, and since the summation or subtraction depending on the value of the third bit combines the power of the two symbols, all bits have 3 dB higher sensitivity compared to symbol by symbol detection.

11. A computer system configured with instructions for finding a modulation format with improved receiver sensitivity, the computer system carrying out the following steps of:
 - providing on a transmitter side of a communication system a pair-wise symbol bits correlation on incoming data bits;
 - applying a quadrature phase shift keying QPSK constellation mapping to the pair-wise symbol bits correlation; and
 - applying optical modulation the QPSK constellation mapped information for a transmitted signal.
12. The computer system of claim 11, further comprising:
 - receiving the transmitted signal at a QPSK receiver;
 - applying a quadrature phase shift keying QPSK constellation de-mapping responsive to the QPSK receiver; and
 - applying a pair-wise symbol bits decoding responsive to the QPSK constellation de-mapping to obtain the received bits.
13. The computer system of claim 11, wherein a correlated symbol modulation carries 1.5 bits per symbol, in other words only 3 bits per 2 symbols as opposed to the QPSK modulation where two symbols carry a total of 4 bits. The number of bits per symbol is reduced from 4 to 3 by introducing a correlation between each pair of symbols.
14. The computer system of claim 11, wherein 3 incoming bits are mapped onto 2 pairs of bits that are correlated, and then mapped onto QPSK constellations based on the constellation mapping, incoming bits are grouped in three bits with each three bits being mapped onto two QPSK symbols.
15. The computer system of claim 14, wherein first two bits are mapped directly to a first symbol and bits carried by a second symbol are determined by the first two bits and a third bit.

16. The computer system of claim 15, wherein if a third bit is 0, the second symbol also carries the same data as the first symbol, if the third bit is 1 the second symbol carries the opposite bits in the second symbol, only each pair of symbols are correlated and neighboring pairs are completely independent as opposed to correlations imposed by convolutional codes.
17. The computer system of claim 12, wherein the correlated-symbol symbols are decoded at the receiver after the standard QPSK DSP just before slicing. comprises the received symbols first going through a standard QPSK DSP for obtaining symbol constellations.
18. The computer system of claim 17, wherein the symbol pairs are determined based on a pairing at the transmitter and from each symbol pair, another pair of constellation being obtained by first adding them together, and then subtracting them.
19. The computer system of claim 18, wherein since each pair of the constellation contains either the same bits or opposite bits, either the summation is zero, or the subtraction is, and the other constellation is just twice the as the first symbol of the pair.
20. The computer system of claim 19, wherein depending on whether the summation is larger than the subtraction the third bit is determined first and the larger constellation determining the first two bits, and since the summation or subtraction depending on the value of the third bit combines the power of the two symbols, all bits have 3 dB higher sensitivity compared to symbol by symbol detection.

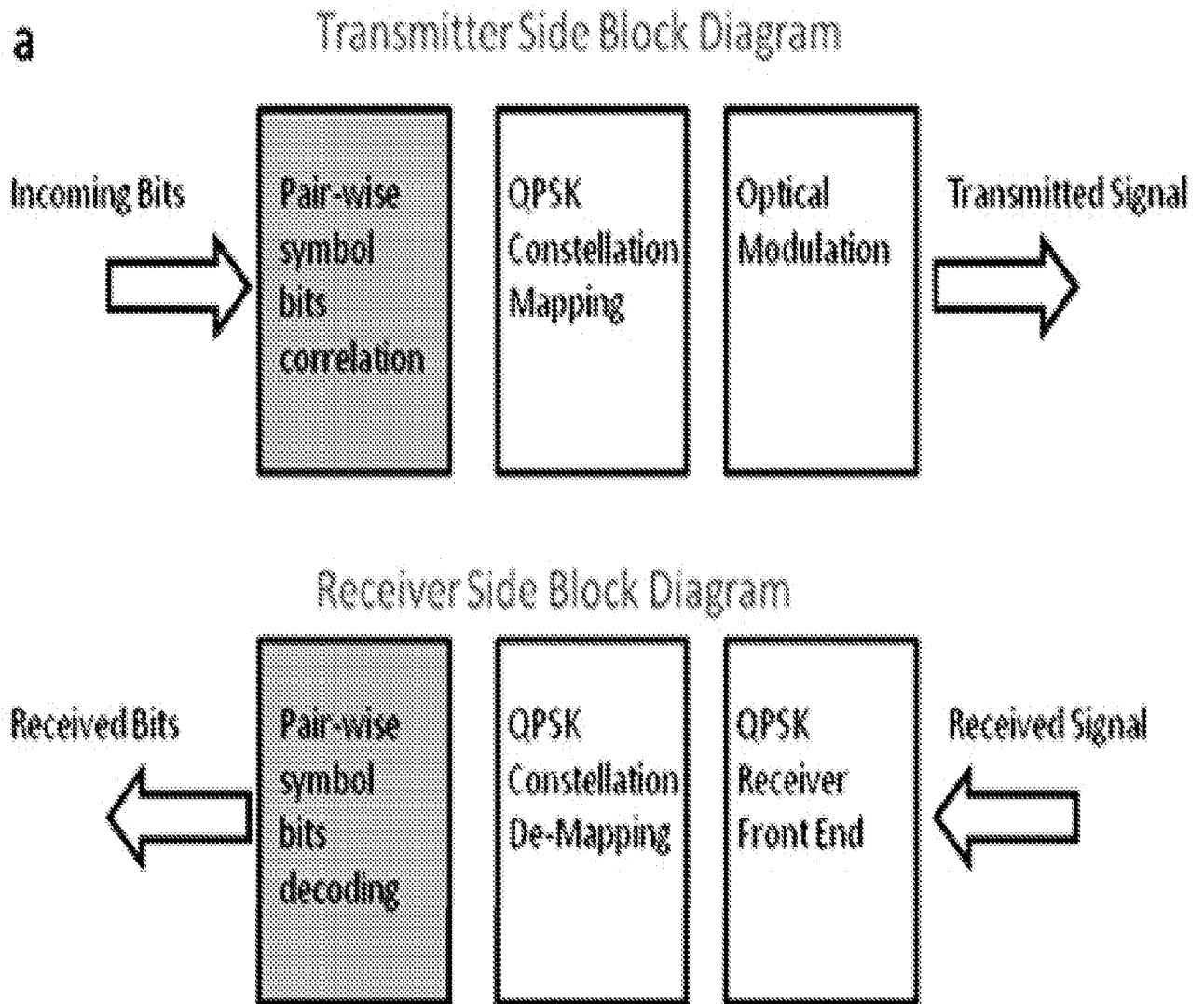


FIG. 1A

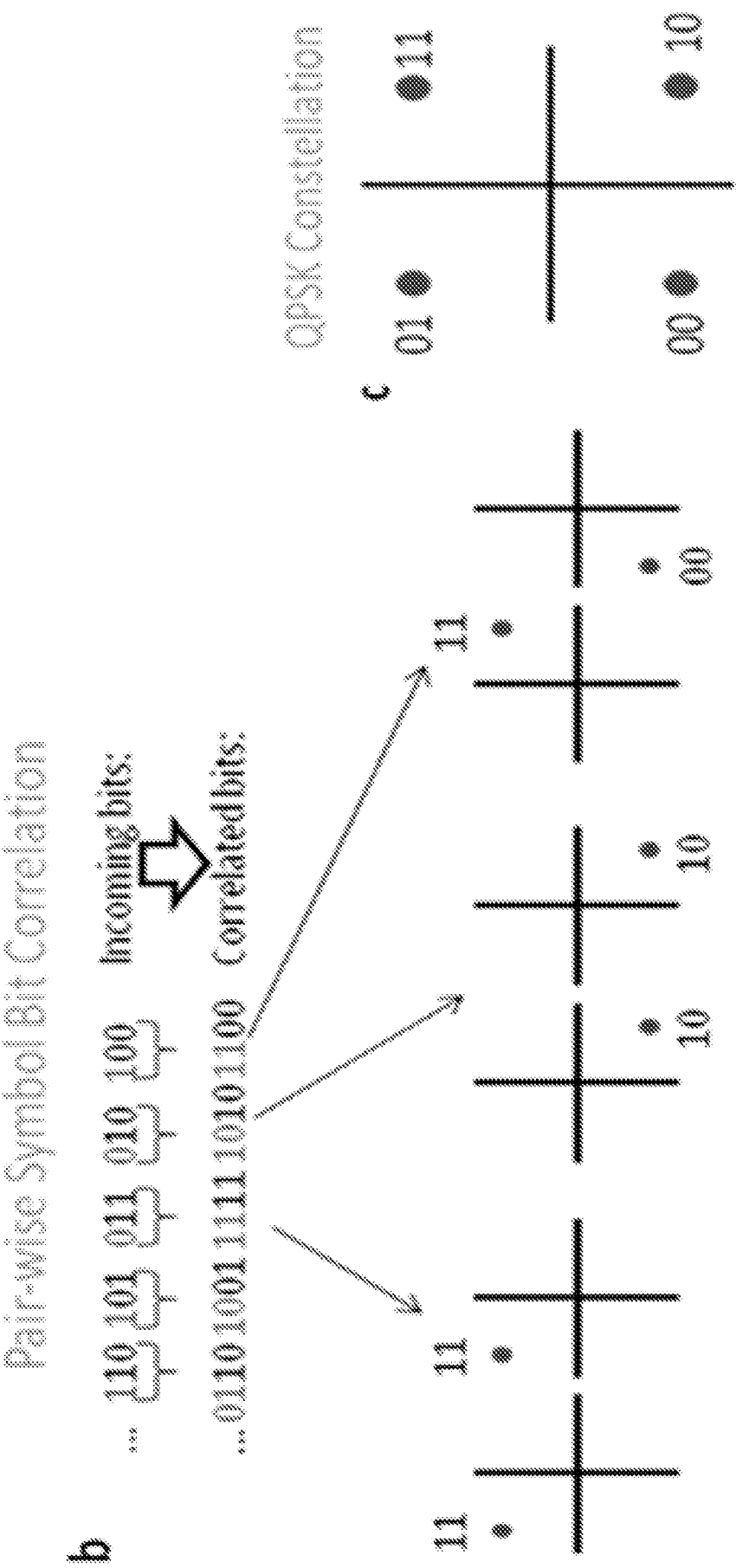


FIG. 1B, 1C

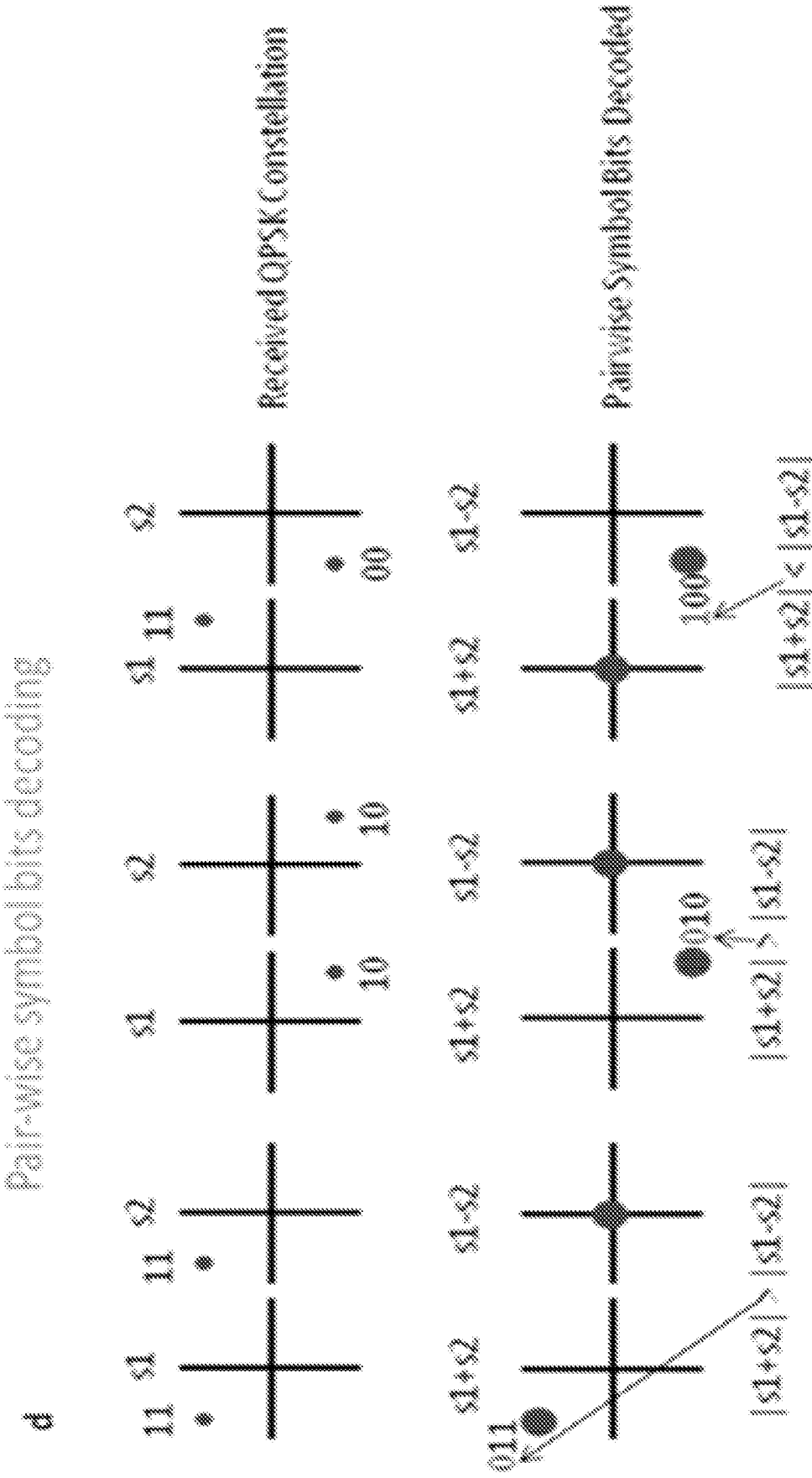


FIG. 1D

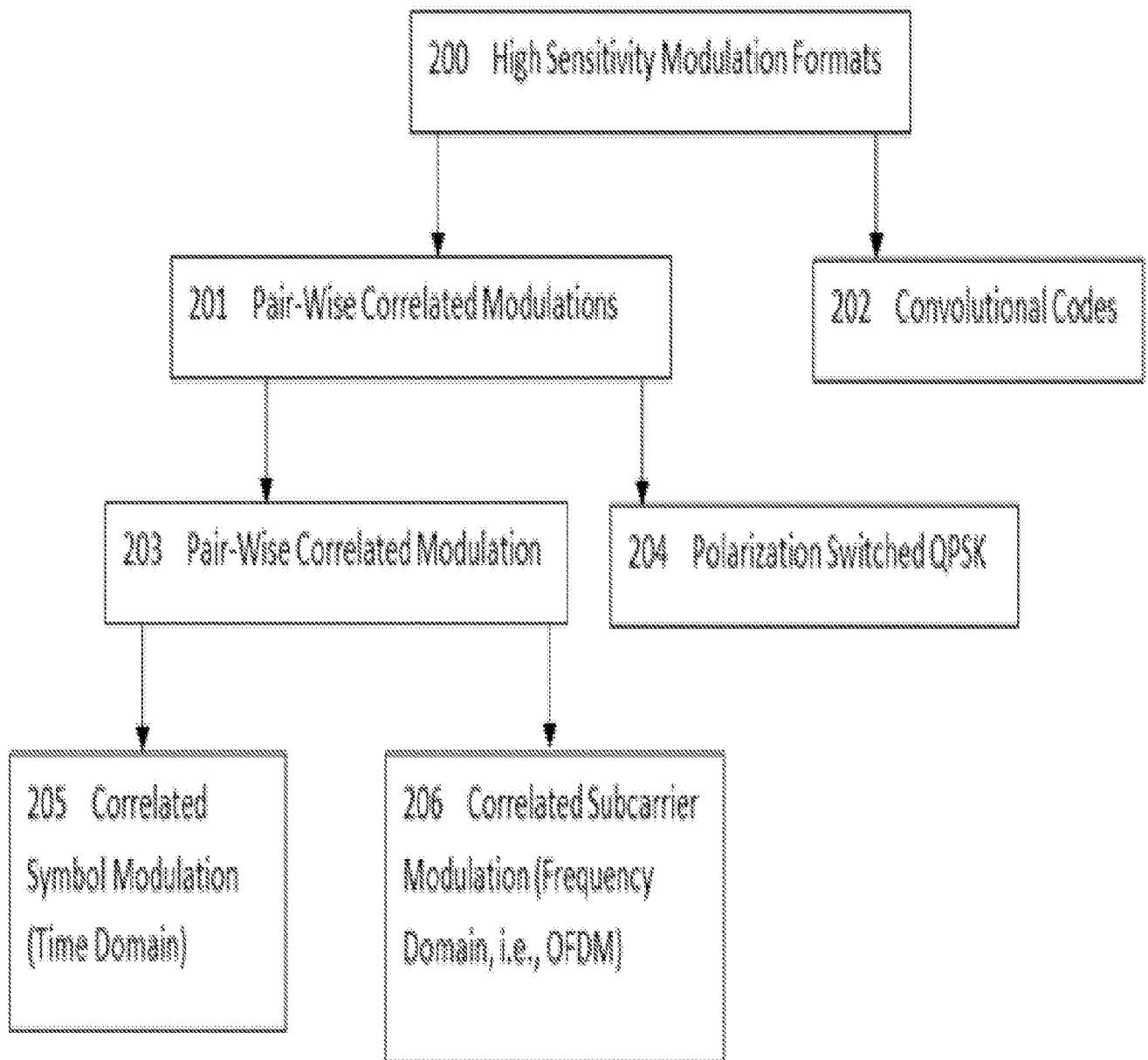


FIG. 2

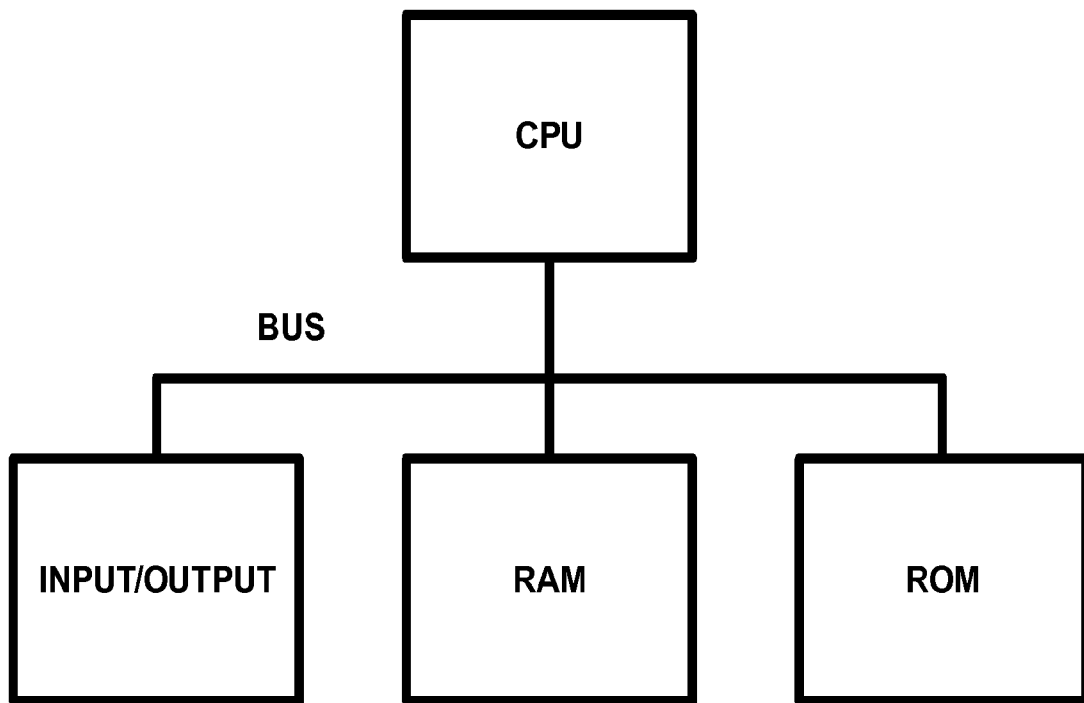


FIG. 3

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2013/063049**A. CLASSIFICATION OF SUBJECT MATTER****H04L 27/34(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04L 27/34; H04L 1/00; H04J 14/06; H04L 1/20; H04J 14/00; H04B 10/00; H04B 10/155; H04B 10/04; G06F 11/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: modulation format, receiver sensitivity, pair-wise, bit, correlation, QPSK, constellation, mapping, optical modulation, opposite, QPSK DSP

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 2506459 A1 (ALCATEL LUCENT) 3 October 2012 See paragraphs 6-10, 13, 18; and claims 1, 3, 4; and figures 1-4.	1-3, 11-13
A		4-10, 14-20
A	US 2012-0093510 A1 (HONGBIN ZHANG et al.) 19 April 2012 See paragraphs 7, 15-21; claims 1-3; and figures 1, 2.	1-20
A	EP 2264928 A2 (NORTEL NETWORKS LIMITED) 22 December 2010 See paragraphs 90-92; claims 1, 2; and figures 3, 8.	1-20
A	US 2006-0242475 A1 (SUMEET SANDHU et al.) 26 October 2006 See paragraphs 32-35; and figures 4A-4C.	1-20
A	WO 2012-031334 A1 (AUSANDA COMMUNICATIONS PTY LTD) 15 March 2012 See page 7, lines 12-18; and figure 3.	1-20



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:

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"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2013/063049

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