



(22) Date de dépôt/Filing Date: 2006/10/12
(41) Mise à la disp. pub./Open to Public Insp.: 2007/06/28
(45) Date de délivrance/Issue Date: 2016/05/17
(62) Demande originale/Original Application: 2 629 998
(30) Priorités/Priorities: 2005/12/22 (US60/752,410);
2006/07/24 (KR10-2006-0069389)

(51) Cl.Int./Int.Cl. *H04H 60/11* (2009.01),
H04H 20/72 (2009.01), *H04N 19/89* (2014.01)

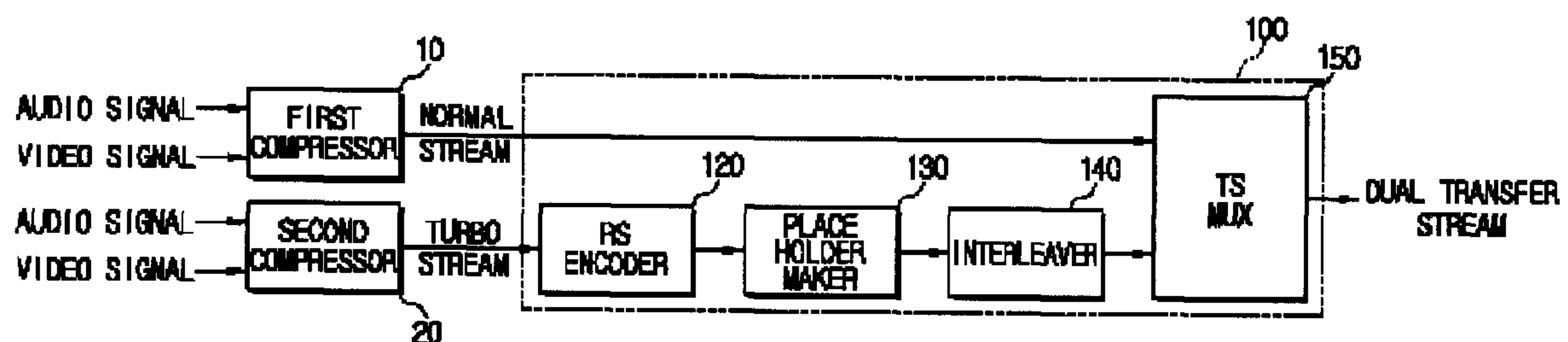
(72) Inventeurs/Inventors:
SONG, DONG-IL, KR;
JEONG, HAE-JOO, KR;
YU, JUNG-PIL, KR

(73) Propriétaire/Owner:
SAMSUNG ELECTRONICS CO., LTD., KR

(74) Agent: FETHERSTONHAUGH & CO.

(54) Titre : EMETTEUR DE RADIODIFFUSION NUMERIQUE, PROCEDE DE TRAITEMENT DE FLUX TURBO ASSOCIE
ET SYSTEME DE RADIODIFFUSION NUMERIQUE PRESENTANT LEDIT EMETTEUR

(54) Title: DIGITAL BROADCASTING TRANSMITTER, TURBO STREAM PROCESSING METHOD THEREOF, AND
DIGITAL BROADCASTING SYSTEM HAVING THE SAME



(57) Abrégé/Abstract:

A digital broadcast transmitter and a stream processing method of a digital broadcast transmitter are provided. The transmitter includes a first compressor for compressing an audio signal and a video signal in a first compression format and constructing a normal data stream, a second compressor for compressing an audio signal and a video signal in a second compression format and constructing an additional data stream, and a transport stream (TS) constructor for multiplexing the normal data stream and the additional data stream and constructing a transport stream.

30235-86D

ABSTRACT

A digital broadcast transmitter and a stream processing method of a digital broadcast transmitter are provided. The transmitter includes a first compressor for compressing an audio signal and a video signal in a first compression format and constructing a normal data stream, a second compressor for compressing an audio signal and a video signal in a second compression format and constructing an additional data stream, and a transport stream (TS) constructor for multiplexing the normal data stream and the additional data stream and constructing a transport stream.

30235-86D

Description

DIGITAL BROADCASTING TRANSMITTER, TURBO STREAM PROCESSING METHOD THEREOF, AND DIGITAL BROADCASTING SYSTEM HAVING THE SAME

This is a divisional of Canadian National Phase Patent Application Serial No. 2,629,998 filed October 12, 2006.

Technical Field

- [1] An aspect of the present invention relates to a digital broadcasting transmitter, a turbo stream processing method thereof, and a digital broadcasting system having the same. More particularly, an aspect of the present invention relates to a digital broadcasting transmitter which processes a normal stream and a turbo stream compressed in different formats from each other, a turbo stream processing method thereof, and a digital broadcasting system having the same.

Background Art

- [2] There are different digital broadcasting standards such as a U.S.-oriented advanced television system committee (ATSC) format and a European-oriented digital video broadcasting-handheld (DVB-H).
- [3] The U.S.-oriented transmission format is based on the National Television System Committee (NTSC) frequency band, and has the advantage of easily implementing a transmitter and receiver. The U.S.-oriented transmission format is a single carrier wave amplitude modulation vestigial side band (VSB) format, that is, this format can transmit high-quality video, audio and auxiliary data in the single 6MHz bandwidth.
- [4] In the U.S.-oriented transmission format, an image signal is compressed in a moving picture experts group-2 (MPEG-2), a sound and voice signal is compressed in digital audio compression (AC-3), and the VSB technology is used to transmit such signals.
- [5] The reason why the image signal and the sound and voice signals are compressed in the MPEG-2 and AC-3, respectively is to reduce the bit rate of the image, voice and digital auxiliary data stream.
- [6] In the U.S.-oriented transmission format, the MPEG-2 used to compress an image signal has been developed for instances when a channel bandwidth or a storage capacity of a storing medium are limited, and when efficient transmission structure is needed. The MPEG-2 is a compression format which is interoperably designed with the asynchronous transfer mode (ATM) transmission structure.

Disclosure of Invention

Technical Problem

- [7] Conventionally, only normal streams are used, but recently a dual transport stream

30235-86D

adding a turbo stream with enhanced coding to the normal stream is used.

- [8] In this case, the compression performance and image quality of the turbo stream which is compressed in the conventional MPEG-2, is considerably decreased. Accordingly, the load on a transmission system and operating expenses of a broadcasting station are increased.

Technical Solution

- [9] An aspect of embodiments of the present invention is to solve at least the above and/or other problems and/or disadvantages and to provide the advantages described below and/or other advantages. Accordingly, an aspect of embodiments of the present invention is to provide a digital broadcasting transmitter to enhance the compression performance and image quality by compressing a normal stream and a turbo stream of a dual transport stream in different formats, a turbo stream processing method thereof, and a digital broadcasting system having the same.

Advantageous Effects

- [10] As can be appreciated from the above description of a digital broadcasting transmitter, a turbo stream processing method thereof, and a digital broadcasting system having the same according to an embodiment of the present invention, the normal stream is processed in MPEG-2 format and the turbo stream is compressed in the H.264 format, making it more efficient to use the turbo stream in the present A-VSB system.
- [11] Therefore, the following benefits can be attained from the above system and method, broadcasting of similar image quality can be implemented at a lower transmission rate, broadcasting of a better image quality is possible at the same rate as the transmission rate of the MPEG-2 format, and broadcasting of the similar image quality can be implemented in various channels. Accordingly, operating expenses of a broadcasting station can be reduced and profits maximized.

30235-86D

Summary of the Invention

[11a] According to an aspect of the present invention, there is provided a digital broadcast transmitter, comprising: an RS encoder to RS-encode an additional data compressed by a first compression format; a place holder maker to provide a parity insertion region for inserting a parity to the RS-encoded additional data; and a TS multiplexer to multiplex a normal data compressed by a second compression format and the RS-encoded additional data, wherein the parity is inserted to the parity insertion region after the normal data and the RS-encoded additional data is multiplexed by the TS multiplexer.

[11b] According to another aspect of the present invention, there is provided a stream processing method of a digital broadcast transmitter, the method comprising: RS-encoding an additional data compressed by a first compression format; adding a parity insertion region for inserting a parity to the RS-encoded additional data; and multiplexing a normal data compressed by a second compression format and the RS-encoded additional data, wherein the parity is inserted to the parity insertion region after the normal data and the RS-encoded additional data is multiplexed by the TS multiplexer.

Brief Description of the Drawings

[12] These and/or other aspects and advantages of the invention will become apparent and more readily appreciated from the following description of the embodiments, taken in conjunction with the accompanying drawings of which:

[13] FIG. 1 is a block diagram describing a digital broadcasting transmitter according to an embodiment of the present invention;

[14] FIG. 2 is a block diagram describing the TS constructor of FIG. 1;

[15] FIGS. 3 through 6 are views describing an H.264 packet constructed in the TS constructor of FIG. 2;

[16] FIG. 7 is a flow chart describing a turbo stream processing method of a digital broadcasting transmitter according to an embodiment of the present invention; and

[17] FIG. 8 is a block diagram of a digital broadcasting receiver according to an embodiment of the present invention.

[18]

Best Mode for Carrying Out the Invention

[19] Reference will now be made in detail to the present embodiments of the present invention, examples of which are illustrated in the accompanying drawings, wherein like reference numerals refer to the like elements throughout. The embodiments are described below in order to explain the present invention by referring to the figures.

[20] FIG. 1 is a block diagram describing a digital broadcasting transmitter according to an embodiment of the present invention.

[21] Referring to FIG. 1, the digital broadcasting transmitter 1000 according to an embodiment of the present invention includes a transport stream (TS) constructor 100 and a TS processor 200.

[22] The TS constructor 100 compresses a turbo stream in the H.264 format, and multiplexes a normal stream and the turbo stream to construct a dual transport stream. The TS constructor 100 will be described in detail in FIG. 2.

[23] The TS processor 200 robustly processes and transmits the dual transport stream transmitted from the TS constructor 100 to the receiver. The TS processor 200 includes a randomizer 201, a Reed-Solomon (RS) encoder 203, a data interleaver 205, a turbo processor 207, a trellis encoder 209, a multiplexer (MUX) 211, a pilot inserter 213, a modulator 215, and a radio frequency (RF) converter 217.

[24] The randomizer 201 randomizes a dual transfer stream to make the best use of an allocated channel space.

[25] The RS encoder 203 RS-encodes the dual transfer stream randomized by the randomizer 201. The RS encoder 203 may be a concatenated coder which adds a parity to the transfer stream to correct errors caused by channel features upon transmission.

[26] The data interleaver 205 interleaves the dual transfer stream RS-encoded by the RS encoder 203. Data-interleaving does not alter data, but changes the location of frame in the frame of data.

[27] In this embodiment, the data interleaver 205 is located between the RS encoder 203 and the turbo processor 207, but is not necessarily limited to this location. For example, the data interleaver 205 can be located between the turbo processor 207 and the trellis encoder 209.

[28] The turbo processor 207 robustly processes the dual transfer stream interleaved by the data interleaver 205. More specifically, the turbo processor 207 separates the dual transfer stream into the normal stream and the turbo stream, passes the normal stream, turbo-codes the turbo stream, and multiplexes and outputs the normal stream and the

turbo-coded turbo stream.

- [29] The trellis encoder 209 trellis-encodes the dual transfer stream robustly processed in the turbo processor 207. The trellis encoder 209 converts the dual transfer stream into a symbol, and performs symbol-mapping through trellis-encoding of certain rates.
- [30] The MUX 211 multiplexes the dual transfer stream by adding a segment sync and a field sync to the dual transfer stream trellis-encoded by the trellis encoder 209.
- [31] The pilot inserter 213 adds a pilot signal to the dual transfer stream including the field sync and the segment sync added by the MUX 211. The pilot signal occurs at a zero frequency point of a spectrum where a little bit of DC deviation is supplied to the 8-VSB baseband right before modulation and a little bit of residual carrier waves are modulated, and functions to synchronize an RF phase locked loop (PLL) circuit of the receiver regardless of the transfer signal.
- [32] The modulator 215 performs pulse-shaping for the transfer stream added with the pilot signal by the pilot inserter 213, loads the transfer stream into an intermediate frequency carrier wave, and performs VSB modulation to modulate the amplitude.
- [33] The RF converter 217 RF-converts and amplifies the transfer stream VSB-modulated by the modulator 215, and transmits the transfer stream through a channel allocated to a certain band.
- [34] FIG. 2 is a block diagram describing the first and the second compressors, and the TS constructor.
- [35] Referring to FIG. 2, there are first and second compressors 10 and 20 provided at a front end of the TS constructor 100, and the TS constructor 100 includes an RS encoder 120, a place holder maker 130, an interleaver 140, and a TS MUX 150.
- [36] The first compressor 10 forms a normal stream by compressing audio signals and video signals according to a first compression format, and the normal stream is inputted to the TS constructor 100. The first compression format may preferably be in an MPEG-2 (Moving Picture Experts Group-2) format.
- [37] The second compressor 20 forms a turbo stream by compressing the audio signals and video signals according to a second compression format, and the turbo stream is inputted to the TS constructor 100. The second compression format may preferably be in an H.264 format.
- [38] The H.264 format is known as the advanced video coding, is a standard for coded expression of visual information, and emphasizes efficiency and reliability. Additionally, the H.264 format compresses approximately twice (about 50%) more efficient than the MPEG-2 format, and is approximately 1.5 times (about 35%) more efficient than the MPEG-4 format. Upon real-time compression such as broadcasting, the performance difference somewhat decreases, but is 30~40% more efficient than the MPEG-2 format.

- [39] For example, to implement the SD-level image quality, data has to be transmitted at the bit rate (the transmission rate of a digital signal) of 4Mbps (4 million bits per second) in the MPEG-2 format, but 2Mbps, which is half of the above bit rate, is enough in the H.264 format.
- [40] Good compression efficiency means less damage in the image quality in spite of the high compression rate. If the compression rate increases, the capacity decreases. Accordingly, if the compression performance is good higher resolution can be implemented when transmitted through a digital signal of less capacity.
- [41] In addition, if higher resolution can be implemented with less capacity, a frequency corresponding to the transmission route is less used. Therefore, in limited frequency resources, since the H.264 format can transmit using less capacity in the same frequency band, extra room is created in the frequency. Accordingly, if the H.264 format is used, more channel services are possible.
- [42] The RS encoder 120 adds and encodes the parity to the turbo stream compressed in the H.264 format by the second compressor 20.
- [43] The place holder maker 130 generates a region to insert a parity to the turbo stream, which will be added in the turbo processor 207 of the TS processor 200. For example, 1 byte of 8 bits, which is the construction unit of a turbo stream, is formed as 1 byte of 4 bits, so that 2 bytes are generated.
- [44] The interleaver 140 interleaves the turbo stream with the region for inserting the parity. The interleaver 140 can be omitted if necessary, or can be replaced with a different element. However, if the RS encoder 120 is included in the TS constructor 100, the interleaver 140 may be included in the TS constructor 100.
- [45] The TS MUX 150 constructs a dual transfer stream by multiplexing the normal stream input in the TS constructor 100, and the turbo stream interleaved in the interleaver 140, and outputs the dual transfer stream.
- [46] FIGS. 3 through 6 are views describing an H.264 packet constructed in the TS constructor of FIG. 2.
- [47] In general, a packet applied to the digital broadcasting consists of 1 byte of sync, 3 bytes of header, and 184 bytes of payload. The header of the packet includes a packet identifier (PID). A normal stream and a robust stream are separated according to a type of data included in the payload.
- [48] FIG. 3 exemplifies a turbo stream input to the TS constructor 100, including the turbo data in the payload part. That is, the turbo data, compressed in H.264 format by the second compressor 20, is input to the RS encoder 120, the place holder maker 130, the interleaver 140 and the TS MUX 150 of the TS constructor 100.
- [49] FIG. 4 exemplifies a normal stream input to the TS constructor 100, and the normal data is included in the payload part, and also includes an adaptation field to insert the

turbo data considering connection to the turbo stream. The adaptation field includes 2 bytes of an AF header and an N byte of null data.

- [50] The turbo stream in FIG. 3 and the normal stream in FIG. 4 are multiplexed in the TS MUX 150 to form a dual transfer stream as in FIG. 5.
- [51] FIG. 6 shows a different connection form of a turbo stream and a normal stream, a packet entirely includes a turbo stream or a normal stream. The TS MUX 140 arranges a turbo stream and a normal stream in the ratio of 1:3. The embodiment of arranging a turbo stream and a normal stream in the ratio of 1:3 is exemplified, but it is not limited to this embodiment.
- [52] The dual transfer streams in FIGS. 3 through 6 are similar to a dual transfer stream compressed in the conventional MPEG-2 format, but different in that the turbo stream compressed by H.264 format has a higher efficiency than the normal stream.
- [53] FIG. 7 is a flow chart describing a turbo stream processing method of a digital broadcasting transmitter according to an embodiment of the present invention.
- [54] Referring to FIGS. 1-4, the turbo stream processing method of the digital broadcasting transmitter according to an embodiment of the present invention will be described.
- [55] The first compressor 10 forms a normal stream by compressing audio and video signals according to a first compression format (S300), and the second compressor 20 constructs a turbo stream by compressing audio and video signal according to a second compression format (S310).
- [56] The turbo stream compressed by the second compressor 20 is RS-encoded by being added with a parity by the RS encoder 120 (S310), and the place holder maker 130 generates a region to be added with a parity in the TS processor 200 (S320).
- [57] The turbo stream with a region to be added with a parity is interleaved by the interleaver 140 (S330), and is input to the TS MUX 150. The TS MUX 150 multiplexes the normal stream and the turbo stream to form a dual transfer stream (S340).
- [58] Next, the dual transfer stream is input to the TS processor 200, passes randomization, RS encoding, interleaving, turbo coding, trellis encoding, multiplexing, pilot insertion, VSB modulation, and RF conversion, and is transmitted through a channel.
- [59] FIG. 8 is a block diagram of a digital broadcasting receiver according to an embodiment of the present invention.
- [60] Referring to FIG. 8, the digital broadcasting receiver 2000 includes a demodulator 410, an equalizer 420, a first processor 430 and a second processor 440.
- [61] The demodulator 410 detects synchronization of a synchronous signal appended to a baseband signal of the dual TS received from the digital broadcasting transmitter 1000, and accordingly performs demodulation of the dual TS.

- [62] The equalizer 420 equalizes the demodulated dual TS, and compensates for a multi-path channel distortion. The dual TS, when equalized by the equalizer 420, is then provided to the first and second processors 430 and 440.
- [63] The first processor 430 processes the normal stream of the dual TS, and thus recovers the normal stream. The first processor 430 includes a viterbi decoder 431, a first deinterleaver 432, an RS decoder 433 and a first derandomizer 434.
- [64] The viterbi decoder 431 performs error correction of the normal stream of the equalized dual TS, decodes the error-corrected symbol, and outputs the decoded normal stream packet.
- [65] The first deinterleaver 432 deinterleaves the decoded normal stream packet to rearrange the distributed packets.
- [66] The RS decoder 433 RS-decodes the deinterleaved normal stream packet to correct errors.
- [67] The first derandomizer 434 derandomizes the RS-decoded normal stream packet to recover the normal stream data.
- [68] Meanwhile, the second processor 440 processes the turbo stream of the dual TS to recover the turbo stream data. With reference to FIG. 8, the second processor 440 includes a turbo decoder 441, a second deinterleaver 442, a parity eliminator 443, a second derandomizer 444, a turbo de-MUX 445, and an eraser decoder 446.
- [69] The turbo decoder 441 performs turbo decoding of the turbo stream of the equalized dual TS. The turbo decoder 441 inserts the turbo stream back into the dual TS to reconstruct the dual TS when the turbo decoding is completed.
- [70] The second deinterleaver 442 deinterleaves the reconstructed dual TS to rearrange the packets.
- [71] The parity eliminator 443 removes parities from the deinterleaved dual TS.
- [72] The second derandomizer 444 derandomizes the parity-removed dual TS.
- [73] The turbo de-MUX 445 demultiplexes the derandomized dual TS to recover turbo stream data.
- [74] The eraser decoder 446 performs eraser decoding with the recovered turbo stream data.
- [75] The transmitter 1000 performs eraser encoding to remove noise, and then inserts the turbo stream to the normal stream to form a dual TS. Therefore, as the eraser decoder 446 of the receiver 2000 performs eraser decoding on the eraser-encoded turbo stream of the transmitter 1000, noise of the turbo stream can be removed and reception improved.
- [76] FIG. 8 only exemplifies a digital broadcasting receiver 2000 according to an embodiment of the present invention, and therefore is not limited to this example only. Therefore, many alternatives, modifications, and variations may be adequately made.

WO 2007/073033

PCT/KR2006/004110

For example, the second processor 440 may include the turbo decoder 441 only, while the first processor 430 is made to process the normal stream and the turbo stream, respectively

Industrial Applicability

[77] The present invention relates to a digital broadcasting transmitter.

30235-86D

CLAIMS:

1. A digital broadcast transmitter, comprising:

an RS encoder to RS-encode an additional data compressed by a first compression format;
- 5 a place holder maker to provide a parity insertion region for inserting a parity to the RS-encoded additional data; and

a TS multiplexer to multiplex a normal data compressed by a second compression format and the RS-encoded additional data,

wherein the parity is inserted to the parity insertion region after the normal data
10 and the RS-encoded additional data is multiplexed by the TS multiplexer.
2. The digital broadcast transmitter as claimed in claim 1, wherein the first compression format is at least one of a Moving Picture Experts Group-2 (MPEG-2) format, an H.264 format, and an MPEG-4 format.
3. The digital broadcast transmitter as claimed in claim 1, wherein the
15 second compression format is at least one of a Moving Picture Experts Group-2 (MPEG-2) format, an H.264 format, and an MPEG-4 format.
4. The digital broadcast transmitter as claimed in claim 1, further comprises:

an interleaver to interleave the additional data to which the parity insertion area is added.
- 20 5. A stream processing method of a digital broadcast transmitter, the method comprising:

RS-encoding an additional data compressed by a first compression format;

30235-86D

adding a parity insertion region for inserting a parity to the RS-encoded additional data; and

multiplexing a normal data compressed by a second compression format and the RS-encoded additional data,

5 wherein the parity is inserted to the parity insertion region after the normal data and the RS-encoded additional data is multiplexed by the TS multiplexer.

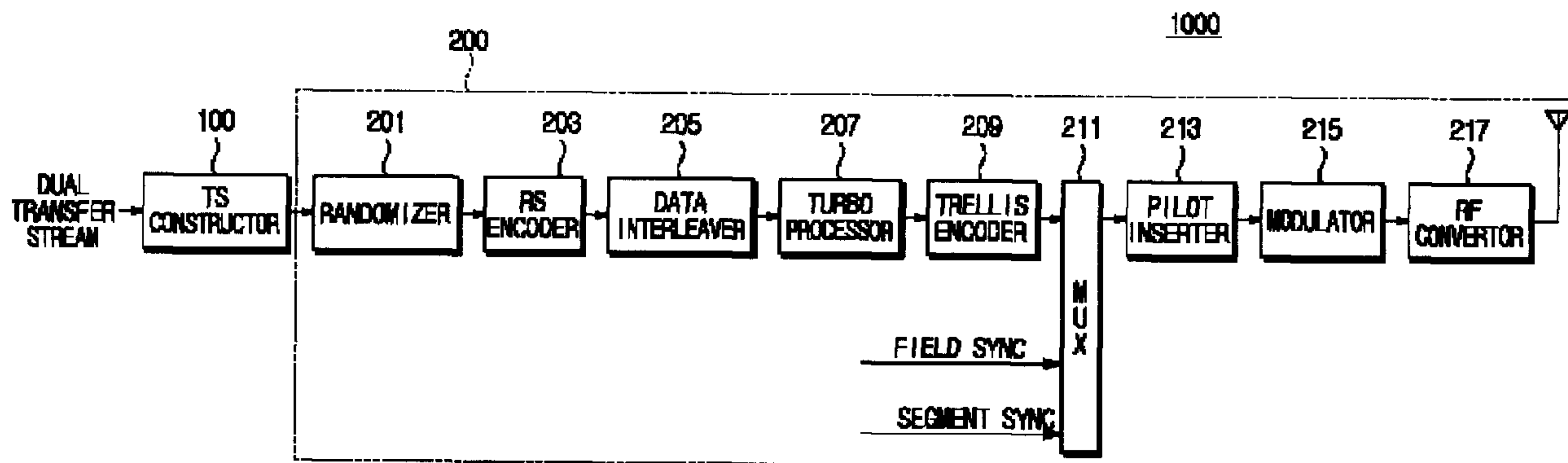
6. The method as claimed in claim 5, wherein the first compression format is at least one of a Moving Picture Experts Group-2 (MPEG-2) format, an H.264 format, and an MPEG-4 format.

10 7. The method as claimed in claim 5, wherein the second compression format is at least one of a Moving Picture Experts Group-2 (MPEG-2) format, an H.264 format, and an MPEG-4 format.

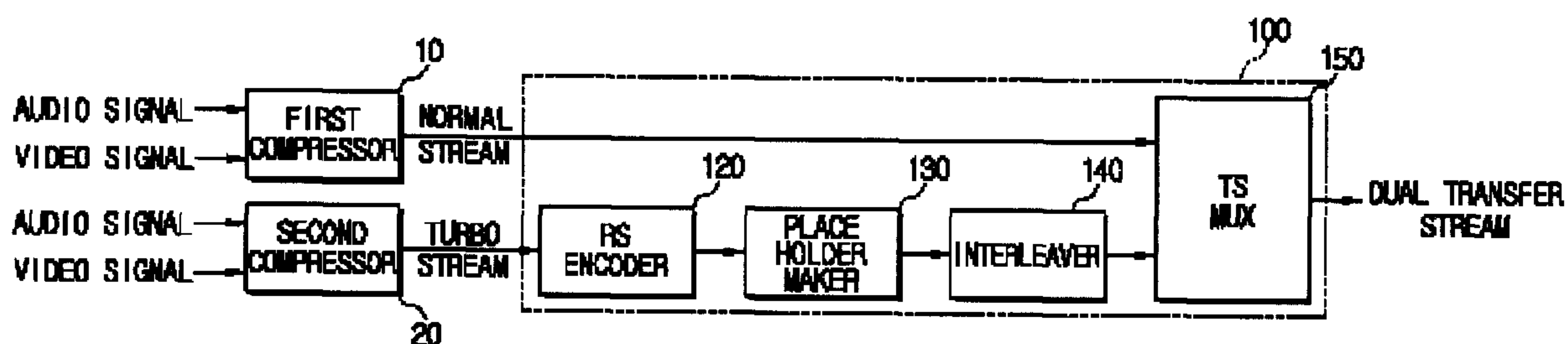
8. The method as claimed in claim 5, further comprising:

interleaving the additional data to which the parity insertion region is added.

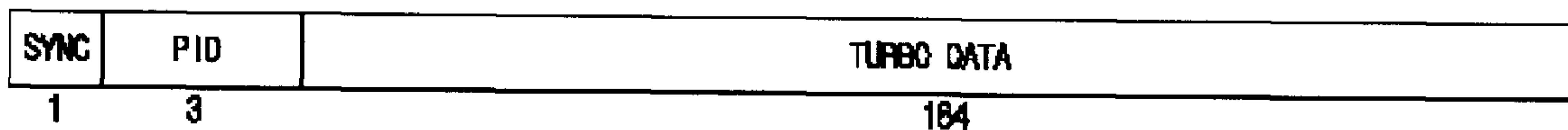
[Fig. 1]



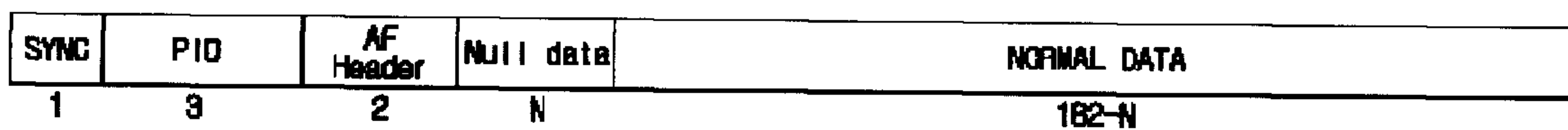
[Fig. 2]



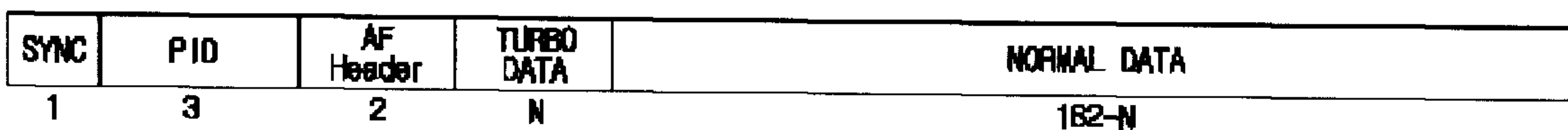
[Fig. 3]



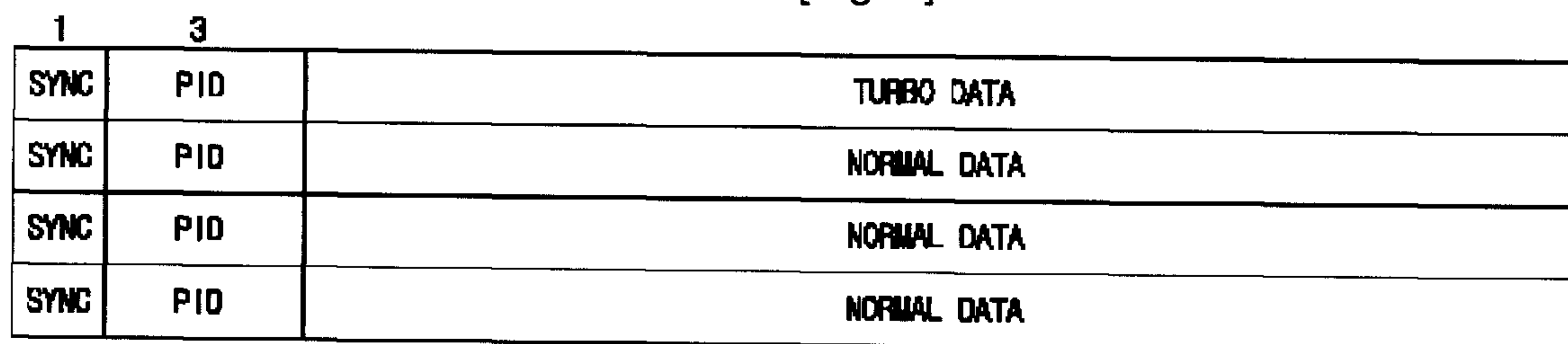
[Fig. 4]



[Fig. 5]

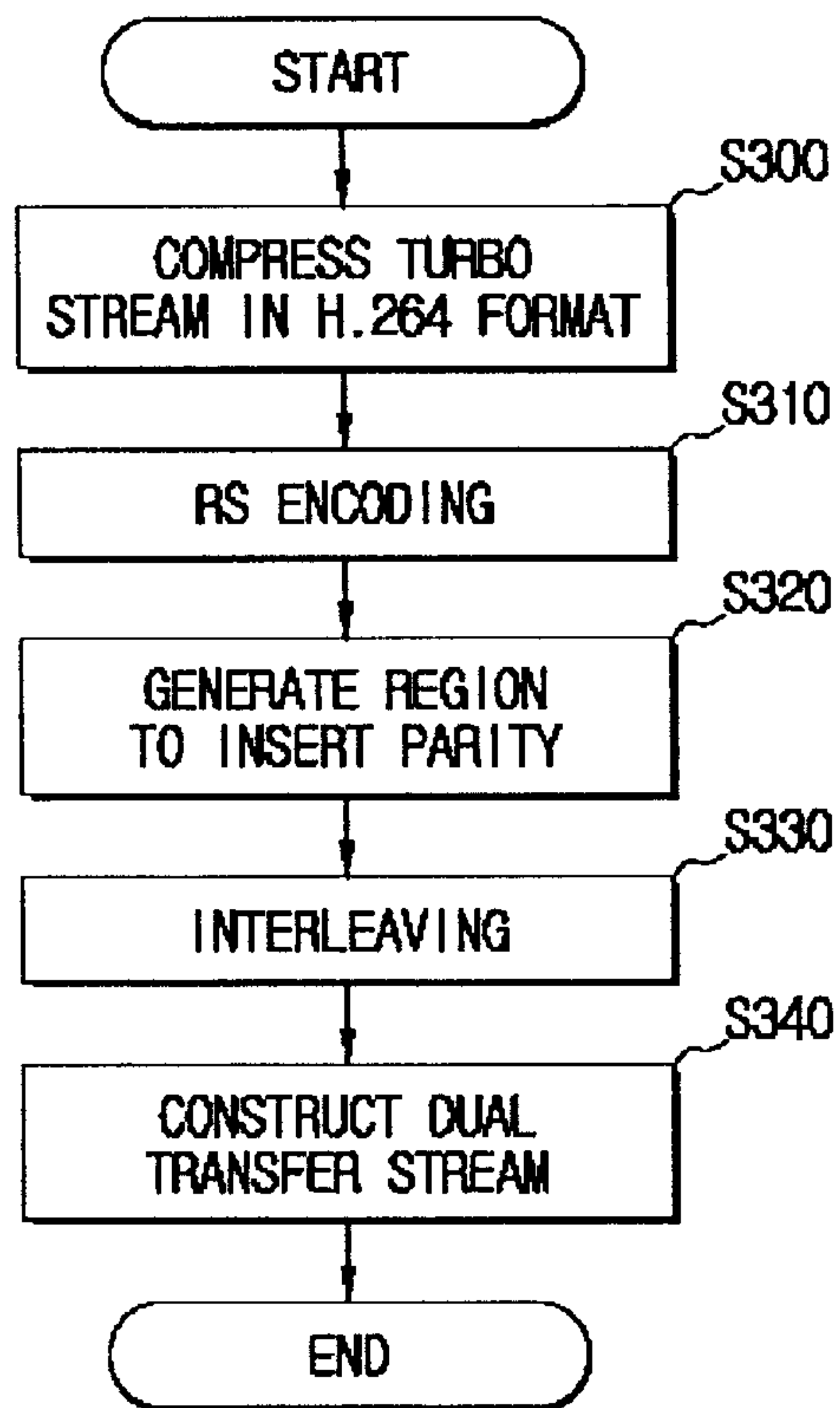


[Fig. 6]



•
•
•

[Fig. 7]



[Fig. 8]

2000

