(19) World Intellectual Property Organization International Bureau





(43) International Publication Date 14 August 2003 (14.08.2003)

PCT

(10) International Publication Number WO 03/067274 A3

(51) International Patent Classification7: G01R 31/3185

(21) International Application Number: PCT/US03/01709

(22) International Filing Date: 21 January 2003 (21.01.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:

10/061,844 1 February 2002 (01.02.2002) US

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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declaration under Rule 4.17:

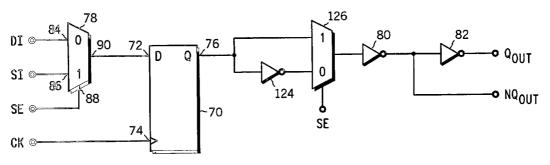
as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

Published:

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments
- (88) Date of publication of the international search report: 16 October 2003

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: METHOD AND DEVICE FOR DETECTING FAULTS ON INTEGRATED CIRCUITS



(57) **Abstract:** A scan-cell for use in a scan device of the type which is utilized to test integrated circuits comprises a first multiplexer (78); a switching device (70), and a second multiplexer (126). The first multiplexer provides a data signal on the output thereof when a control signal is in a first state and provides a test signal at the output thereof when the control signal is in a second state. The switching device is coupled to the output of the first multiplexer and captures the output. The second multiplexer has an input coupled to the output of the switching device and transmits the output when the control signal is in the first state. The second multiplexer transmits an inverted form of the output when the control signal is in the second state.



INTERNATIONAL SEARCH REPORT

Internal nal Application No PCT/US 03/01709

A. CLASSII IPC 7	a. classification of subject matter IPC 7 G01R31/3185							
According to	International Patent Classification (IPC) or to both national classifica	ation and IPC						
B. FIELDS	SEARCHED							
Minimum do IPC 7	cumentation searched (classification system followed by classification $G01R$	on symbols)						
Documentat	ion searched other than minimum documentation to the extent that s	uch documents are included in the fields se	earched					
Electronic da	ata base consulted during the international search (name of data bas	se and, where practical, search terms used)					
EPO-Internal, WPI Data, INSPEC								
C. DOCUMENTS CONSIDERED TO BE RELEVANT								
Category °	Citation of document, with indication, where appropriate, of the rela	Relevant to claim No.						
Х	US 5 923 676 A (NADEAU-DOSTIE BENOIT ET 1-20 AL) 13 July 1999 (1999-07-13) abstract; figures 2-5 column 5, line 19 -column 8, line 27		1-20					
A	US 5 617 428 A (ANDOH YASUHIRO) 1 April 1997 (1997-04-01) abstract; figures 5,8		1-20					
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Furti	ner documents are listed in the continuation of box C.	χ Patent family members are listed	in annex.					
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Date of the	actual completion of the international search	Date of mailing of the international sea	arch report					
	August 2003	13/08/2003						
Name and r	nailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL ~ 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fav. (+31-70) 340-3016	Authorized officer Böhm-Pélissier. A						

INTERNATIONAL SEARCH REPORT

Information on patent family members

Intermonal Application No
PCT/US 03/01709

Patent document cited in search report		Publication date	Patent family member(s)		Publication date
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US 5617428	A	01-04-1997	JP JP	2737695 B2 8313597 A	08-04-1998 29-11-1996

Form PCT/ISA/210 (patent family annex) (July 1992)