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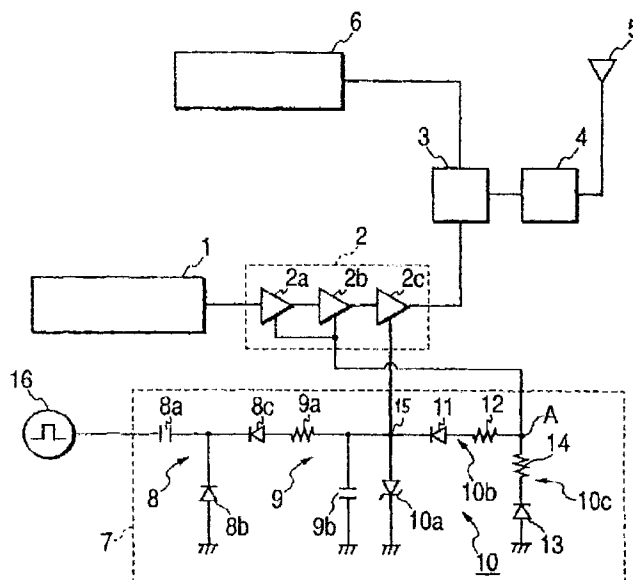
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**(54) Bias voltage stabilizing circuit**

(57) Disclosed herein is a bias voltage stabilizing circuit which comprises a dc voltage source, a first voltage drop circuit (10b) having a first diode (11), and a second voltage drop circuit (10c) having a second diode (13). The first voltage drop circuit and the second voltage drop circuit are electrically series-connected to each other and electrically parallel-connected to the dc voltage source. Resistors (12,14) are electrically con-

nected to at least one of the first voltage drop circuit and the second voltage drop circuit in series with either the first diode or the second diode. A closed loop is formed by the dc voltage source, the first diode, the second diode and the resistors. The voltage at a point where the first voltage drop circuit and the second voltage drop circuit are electrically connected to each other, is used as a bias voltage.

**FIG. 1**



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# EUROPEAN SEARCH REPORT

Application Number  
EP 98 30 6464

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
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			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H02M G05F
The present search report has been drawn up for all claims			
Place of search <b>THE HAGUE</b>		Date of completion of the search <b>12 March 1999</b>	Examiner <b>Schobert, D</b>
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
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