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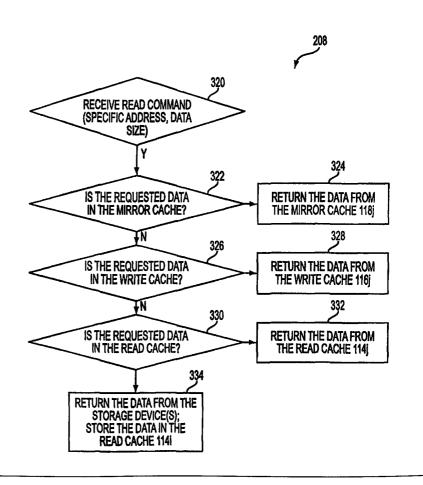
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(54) Title: FULL CACHE COHERENCY ACROSS MULTIPLE RAID CONTROLLERS

(57) Abstract

A method for providing cache coherency in a RAID system (100) in which multiple RAID controllers (104) provide read/write access to shared storage devices (108) for multiple host computers (102). Each controller includes read (114), write (116) and write mirror (118) caches and the controllers and the shared storage devices are coupled to one another via common backend buses (110). Whenever a controller receives a write command (302) from a host the controller writes the data to the shared devices, its write cache and the write mirror caches of the other controllers. Whenever a controller receives a read command (320) from a host the controller attempts to return the requested data from its write mirror cache, write cache and read cache and the storage devices, in that order.



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FULL CACHE COHERENCY ACROSS MULTIPLE RAID CONTROLLERS

The present invention relates generally to methods for caching disk reads and writes in a RAID system and, particularly, to methods for maintaining the coherency of multiple caches used for caching disk reads and disk writes in a RAID system.

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BACKGROUND OF THE INVENTION

FIG. 1 shows a block diagram of a typical multiple-controller RAID system 100 (RAID is an acronym for "Redundant Array of Independent Disks"). Each host computer 102 is connected to a respective RAID controller 104 through either a Fibre Channel or SCSI bus 106 via a host bus adapter (HBA). Each RAID controller 104 coordinates reading and writing requests from a respective host 102 directed to a shared set of storage devices 108 to which the RAID controllers 104 are connected via a backend Fibre Channel or SCSI disk bus 110. The controllers 104 use the same storage devices 108 so that each host computer 102 can access the same data. FIG. 1 shows only two controllers; however, the illustrated architecture is extendable to systems of N controllers (where N is an integer greater than 2). The controllers 104 have cache memories 112 in which they temporarily store the data most recently read and written by the host 102. The operation of these cache memories 112 is now described with reference to FIG. 2.

FIG. 2 shows a block diagram of the caches 112, which include a read cache 114, a write cache 116 and a write mirror cache 118. A controller 104i (where "i" represents any integer) places write data 103 (FIG. 1) from the host 102 into its write cache 116i and data 105 (FIG. 1) read from the controller 104 by the host 102 into its read cache 114i. Each write mirror cache 118i duplicates the contents 107j of another controller's write cache 116j. The write mirror cache 118i is written to by a controller 104j around the time it initiates a write operation. The write mirror caches 118 allow a duplicate copy of the write data 107 to be stored in a second controller so that a failure of either controller 104 will not result in the loss of data.

WO 00/43888

2

Data 107 for the write mirror caches 118 is transferred between the controllers through the backend SCSI or Fibre Channel disk buses (busses) 110. The data in a mirrored cache 118 is used only if a controller 104 involved in a write fails, in which case the mirrored data is transferred to the disks 108 for storage.

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The problem with this method is that the caches may not be synchronized, which can cause the hosts to receive inconsistent data following read operations. For example, if the host controller 104-1 performs a write to a disk device 108 and the second host system 102-1 attempts to read the same data, a copy of which is already in the read cache 114-2 of the second controller 104-2, the second host would receive state data as the read caches are not updated across controllers. Further, copying all read data across the controllers would severely compromise performance. This problem will become increasingly important as clustering environments increase in popularity.

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SUMMARY OF THE INVENTION

In summary, the present invention is a method to insure cache coherency across multiple RAID controllers. This invention synchronizes both the write and read caches, whereas prior art implementations only synchronize the write cache data.

In particular, the present invention synchronizes the read caches without needing to copy the read cache data between controllers. As a result, the present invention provides full cache coherency without using excessive bandwidth for inter-controller data transfers.

One embodiment of the present invention for use with multiple RAID controllers having associated read, write and mirror caches, where the controllers are connected via one or more backend channels to each other and a set of storage devices, includes the following steps:

3

- 1. In response to a command to write data to a specific address, a first controller initiates the write operation and copies the particular data to the mirror caches of one or more other controllers through the one or more backend channels.
- 5 2. The first controller then confirms that the write operation is complete.

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- 3. In response to a command to read data at the specific address, a second controller that is one of the other controllers:
 - a. checks for the data in its mirror cache and, if the data is there, returns that data to the host computer;
 - b. checks for the data in its write cache, and, if the data is there, returns that data to the host;
 - c. checks for the data in its read cache and, if the data is there, returns that data to the host; and
 - d. if the data is not in any of the caches, gets the data from the storage devices, places the data into its read cache and returns that data to the host.

In one embodiment, the backend channels can be any combination of SCSI and/or Fibre Channel buses. In another embodiment, the storage devices are disks. In none of the embodiments is it required that a controller has a one-to-one relationship with its associated read, write and write mirror caches or with a host for which it executes read and write commands.

The present invention also encompasses RAID systems incorporating RAID controllers
programmed to implement the preceding method, as well as computer programs and
computer program products recorded on tangible recording media which incorporate
the inventive method.

WO 00/43888

BRIEF DESCRIPTION OF THE DRAWINGS

Additional objects and features of the invention will be more readily apparent from the following detailed description and appended claims when taken in conjunction with the drawings, in which:

- FIG. 1 shows a block diagram of a typical multiple-controller RAID system 100 (RAID is an acronym for "Redundant Array of Independent Disks");
- FIG. 2 shows a block diagram of the caches 112, which include a read cache 114, a write cache 116 and a write mirror cache 118;
 - FIG. 3 shows a block diagram of a RAID controller in which the present invention is implemented;

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- FIG. 4 shows a flow diagram of a RAID controller write method implemented in accordance with the present invention;
- FIG. 5 shows a flow diagram of a RAID controller read method implemented in accordance with the present invention; and
 - FIG. 6 shows a sequence diagram illustrating actions performed and messages exchanged by sending and receiving RAID controllers in one embodiment of a cache mirroring system.

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DESCRIPTION OF THE PREFERRED EMBODIMENT

FIG. 3 shows a block diagram of a computer system 100 in which the present invention is implemented. The computer system 100 includes many of the same components illustrated and described with reference to FIG. 1. In particular, the computer system

5

100 includes at least two host computers 102i, 102j, each coupled to a RAID (Redundant Arrays of Independent Disks) controller 104i, 104j. The RAID controllers 104 provide the hosts 102 with read/write access to the shared storage devices 108, which are coupled to the controllers 104 via one or more backend disk buses (referred to herein after as the backend bus) 110. In different embodiments the backend bus 110 can be a Fibre Channel bus, a SCSI bus, or another type of high-speed bus (e.g., Ethernet, IPI, HIPPI, Fire-Wire or IDE). In different embodiments the shared storage devices 108 are magnetic disk drives, magnetic tape drives, optical disks, or another type of non-volatile storage (e.g., RAMDISK drives).

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Each RAID controller 104 includes a processor 200 and a memory 202. The memory 202 can be any combination of a fast memory, such as a semiconductor random access memory (RAM), a fast non-volatile memory, such as a read only memory (ROM) or an erasable read only memory EPROM, and a slow magnetic memory, such as a hard disk. The memory 202 includes a read cache 114, a write cache 116 and a write mirror cache 118 (referred to hereinafter as a "mirror cache"). The memory 202 also includes controller routines 204, which are programs that are executed by the processor 200 and determine, among other things, the operation of the controller 104 in response to read and write commands 126, 128 issued by the host computer 102. In one embodiment a read command 126 designates the address 132 and size 134 of data 140 to be read from the storage devices 108 and a write command 128 includes data 138 to be written to a particular address 136 of the storage devices 108.

The controller routines 204 include, but are not limited to, read and write programs 208, 206. In one embodiment the controller routines 204 are firmware, meaning that they are stored in non-volatile memory and therefore are available for execution as soon as the controller 104 is powered-up. The controller routines 204 can also be implemented as conventional software that is stored on a hard disk (not shown), or other non-volatile memory, and is loaded into RAM or other fast memory for execution by the processor 200 under control of an operating system (not shown). The read and write programs

6

208, 206, which embody principles of the present invention, are now described with reference to FIGS. 4 and 5, respectively.

FIG. 4 shows a flow chart of selected steps performed by a controller 104i under control of the write program 206. In the conventional manner, in response to a host write command 128 (step 302) the controller 104i writes the designated write data 138 to the shared storage devices 108 and to its write cache 116i (304). In accordance with the present invention, the controller 104i also copies the designated data 138 to the mirror caches 118j of each of the other controllers 104j (306). The controller 104i accomplishes this "mirror copy" operation by broadcasting the write data 138 over the backend bus 110 using a special mirror cache write command. Finally, the controller 104i confirms the write operation's completion (308). If it cannot confirm completion, the controller 104i re-attempts the write operation using the write data 138 previously stored in the write cache 116i. One embodiment of the mirror copy operation 306 is now described with reference to FIG. 6.

FIG. 6 shows a sequence diagram of one embodiment of the mirror copy operation 306 that can be performed by a sending controller 104i and one or more receiving controllers 104j. This diagram shows the sequence of actions performed and messages exchanged by the controllers 104 in which the special mirror cache write command is implemented using the "Vendor-Unique" command format defined by the SCSI protocol specification. In this embodiment, the sending controller 104i bundles the data 402 to be cached with cache meta-data 404 (information about the address 404a and size 404b of the cache data) and imbeds this data into the data-phase of a Vendor-Unique command 406 (6.1). Vendor-Unique commands are known in the art and is generally a command that allows the unique characteristics of the controller (for example, those characteristics not defined in the SCSI or Fibre Channel specifications and therefore possibly not available via standard SCSI or Fibre Channel commands or protocols) to be determined and set, as well as allowing other special operations to the controlled device. (Additional background information about the Vendor-Unique command may be found in the SCSI-III protocol specification, which is incorporated

7

herein by reference: SCSI-3 Primary Commands (SPC) {Date: 1997/03/28, Rev. 11a, Status: Published, Project 0995-D} X3.301:1997). The sender 104i then initiates a transfer of the cache and meta data 402, 404 to the receiving controller 104j using the Vendor-Unique command 406 (6.2). The receiver 104j, which is configured to recognize the Vendor-Unique command 406 and to have a-priori knowledge of the structure of the data 402, 404, receives the data (6.3) and transfers that data into the correct position in the receiver's write mirror cache 118 (6.4). The receiver 104j then acknowledges completion of the command 406 through the usual SCSI mechanisms (6.5). Sending the command 406 to any device other than an appropriately configured controller 104 will result in an error condition for that device. In this methodology, the command 406 is sent to a single receiver as the SCSI protocol does not define a "broadcast" method. In the situation of multiple receivers, the command is sent independently to all receivers. This can occur simultaneously since multiple back-end (disk-side) channels are connected to the controllers, allowing a command to be sent to each receiver on a different back end channel.

As a result of this write method, following execution of a write command 128 a copy of the new write data 138 is resident in the mirror caches 118j of all controllers 104j that did not perform the write command 128. The mirrored data can be used by subsequent read operations initiated by the controllers 104j, ensuring that a read command 126 issued for the new data 138 returns the newest version 140 of that data, which is not the case with the prior art methods. The read method of the present invention, which makes this possible, is now described with reference to FIG. 5.

FIG. 5 shows a flow chart of selected steps performed by a controller 104j under control of the read program 208. In accordance with the present invention, a controller 104j carries out a host read command 126 in such a way as to ensure that it returns the current version of the requested read data to the host 102j. In particular, in response to the read command 126 (320), the controller 104j first looks in its mirror cache 118j for the designated read data 140 (i.e., the data at address 132 of size 134) (322). If the read data 140 is in the mirror cache 118j (322-Y), the controller returns that data to the host

102j (324). If the read data 140 is not in the mirror cache (322-N), the controller 104j checks its write cache 116i (326). If the read data 140 is in its write cache 116j (326-Y), the controller 104j returns that data to the host 102j (328). If the read data 140 is not in the mirror cache (322-N), the controller checks its read cache 114i (330). If the read data 140 is in its read cache 114j (330-Y), the controller 104j returns that data to the host 102j (332). If the read data 140 is not in the mirror cache (330-N), the controller returns the designated read data from the storage devices 108 (334) and stores the same data in its read cache 116i for subsequent use (334). (Note: generally, the controller 104 writes any data returned to the host to its read cache 114).

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By checking the write mirror cache first in response to a read command, this embodiment ensures that a controller 104 returns to a host 102 the current version of the requested read data, even if previous versions of the requested data are already resident in the controller's write and/or read caches. Similarly, by requiring the controller 104 to return the requested data preferentially from its write cache 116 instead of its read cache 114 in the event the requested data is not in the mirror cache 118, this embodiment ensures that the controller 104 returns the most recent version of data it has updated. Finally, by providing for the controller 104 to supply the requested data from its read cache 114 when the other two options fail, the described embodiment ensures that data already read by the controller 104 is returned to the host 102 with minimum delay.

Thus, the present invention maintains cache coherency in a RAID system including multiple hosts and RAID controllers. In one embodiment, shown in FIG. 3, this advantage is provided without requiring additional, high bandwidth data transfers between controllers 104. This is possible because the controllers 104 not involved in a write operation simply receive the write data 138 as it is being written to the shared storage devices 108 via the backend bus.

In summary, in one embodiment for use in a RAID system having multiple RAID controllers and a set of storage devices, the host read and write processing includes:

- (1) in response to a write command 128 to write first data 138 to the storage devices 108, a first controller 104i writes the first data 138 to the storage devices 108 and copies the first data to mirror caches 118j associated with one or more other controllers 104j; and
- (2) in response to a read command 126 to read second data 134 from the storage devices 108, a second controller 104 checks for the second data 134 in an associated one of the mirror caches and, if the data 134 is in the associated mirror cache, returns the second data to the host computer 102 that issued the read command 126.
- In another alternate embodiment the first controller 104i copies the first data 138 to the associated mirror caches 118j by broadcasting the first data 138 to the associated mirror caches over a backend bus 110 to which the controllers 104 and the storage devices 108 are coupled. In a related embodiment the broadcasting step is implemented so that it adds no more than minimal overhead to the step of writing the first data 138 to the storage devices 108. Yet another related embodiment provides this minimal overhead by performing the broadcasting and writing steps simultaneously.

In embodiments where the RAID controllers 104 have associated read and write caches 114, 116, the host read and write processing includes the following steps in addition to the two outlined above:

- (3) in response to the write command 128 to write first data 138 to the storage devices 108, the first controller 104i also writes the first data to its associated write cache 116i;
- (4) in response to the read command 126 to read second data 134 from the storage devices 108, the second controller 104:
 - (a) checks for the second data 134 in the associated write cache 116, and, if the data is there, returns the second data 134 to the host 102;
 - (b) checks for the second data 134 in the read cache 114 and, if the data is there, returns the second data 134 to the host 102; and

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(c) if the second data 134 is not in the associated caches 116, 118, retrieves the second data from the storage devices 108 and returns the second data 134 to the host computer 102 that issued the read command.

We now review aspects of the invention by way of highlighting selected particular embodiments of the invention. In a first aspect the invention provides a synchronization method for use in an RAID system having multiple RAID controllers and a set of storage devices, the method including the steps: in response to a write command to write first data to the storage devices, a first one of the controllers writes the first data to the storage devices and copies the first data to mirror caches associated with one or more other controllers; and in response to a read command to read second data from the storage devices, a second controller checks for the data in an associated one of the mirror caches and, if the data is in the associated mirror cache, returns the second data to a host computer that issued the read command.

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In a second aspect, this synchronization method further provides that the RAID controllers have associated read and write caches, the method further comprises in response to the write command to write first data to the storage devices, the first controller also writes the first data to its associated write cache; in response to the read command to read second data from the storage devices, the second controller: checks for the second data in the associated write cache, and, if the data is there, returns the second data to the host; checks for the second data in the read cache and, if the data is there, returns the second data to the host; and if the second data is not in the associated caches, retrieves the second data from the storage devices and returns the second data to the host computer that issued the read command.

In a third aspect, the synchronization method provides that the copying of the first data to the associated mirror caches comprises broadcasting the first data to the associated mirror caches over a backend bus to which the controllers and the storage devices are coupled. In a fourth aspect, the synchronization method additionally provides that the broadcasting of the first data adds no more than minimal overhead to the writing of the

WO 00/43888

first data to the storage devices. In a fifth aspect, the broadcasting and the writing of the first data are performed simultaneously.

In a sixth aspect, the backend bus comprises any combination of: one or more Fibre Channel buses; or one or more SCSI buses. In a seventh aspect, the storage devices comprise magnetic disks.

In an eighth embodiment, the synchronization method provides that the copying of the first data to the associated mirror caches comprises transmitting, using a SCSI Vendor Unique command, the first data to the associated mirror caches over a backend bus to which the controllers and the storage devices are coupled.

In a ninth aspect, the invention provides a cache system for use in a RAID system including a plurality of RAID controllers providing access to a set of storage devices, comprising: a plurality of mirror caches accessible to the controllers; a first RAID being controller configured, when it receives a write command to write data to a specific address, to copy the data to the mirror caches of one or more different RAID controllers in addition to writing the data to the storage devices, any of the RAID controllers being configured, after receiving a read command to read the data at the specific address, to attempt first to retrieve the data from an associated one of the mirror caches.

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In a tenth aspect, the cache system is further defined such that the plurality of controllers and the set of storage devices are connected via a backend bus. In an eleventh aspect, the cache system is further defined such that the backend bus comprises any combination of: one or more Fibre Channel buses; or one or more SCSI buses.

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In a twelveth aspect the cache system further comprises: a plurality of write caches accessible to the controllers; a plurality of read caches accessible to the controllers; such that: in response to a read command to read data at the specific address, the second controller: checks for the data in its mirror cache and, if the data is there, returns the data to a host computer that issued the read command; checks for the data in its write cache, and, if the data is there, returns the data to the host; checks for the data in its read

12

cache and, if the data is there, returns the data to the host; and if the data is not in any of the caches, retrieves the data from the storage devices and returns the data to the host.

In a thirteenth aspect, the cache system is further defined to such that the first RAID controller is configured to copy the first data to the mirror caches using a SCSI Vendor Unique command transmitted over a SCSI bus to which the controllers and the storage devices are coupled. In a fourteenth aspect, the cache system is further defined such that the SCSI bus comprises a backend bus.

- In a fifteenth aspect, the invention further provides a synchronization method for use in a data storage system having at least first and second controllers and at least one storage device, the method including: in response to a write command to write first data to the storage device, the first controller writes the first data to the storage device and copies the first data to a mirror cache associated with the second controller; and in response to a read command to read second data from the storage device, the second controller checks for the data in an associated one of the mirror caches and, if the data is in the associated mirror cache, returns the second data to a host computer that issued the read command.
- In a sixteenth aspect, the synchronization method further provides that the controllers have associated read and write caches, the method further comprising: in response to the write command to write first data to the storage device, the first controller also writes the first data to its associated write cache; in response to the read command to read second data from the storage device, the second controller: checks for the second data in the associated write cache, and, if the data is there, returns the second data to the host; checks for the second data in the read cache and, if the data is there, returns the second data to the host; and if the second data is not in the associated caches, retrieves the second data from the storage device and returns the second data to the host computer that issued the read command.

13

In a seventeenth aspect, the synchronization method provides that the copying of the first data to the associated mirror caches comprises broadcasting the first data to the associated mirror caches over a bus to which the controllers and the storage devices are coupled. In an eighteenth aspect, this synchronization method additionally provides that the broadcasting of the first data adds substantially no overhead to the writing of the first data to the storage devices. In a nineteenth aspect, the synchronization method further provides that the broadcasting and the writing of the first data are performed substantially simultaneously. In a twentieth aspect, the synchronization method utilizes a bus comprises any combination of: one or more Fibre Channel buses; or one or more SCSI buses.

In a twenty-first aspect, the synchronization method is utilized in conjunction with magnetic disc drive storage devices wherein the storage devices comprise magnetic disk drives.

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In a twenty-second embodiment, the synchronization method is further defined such that the copying of the first data to the associated mirror caches comprises transmitting, using a SCSI Vendor Unique command, the first data to the associated mirror caches over a bus to which the controllers and the storage devices are coupled. In a twenty-third aspect, this synchronization method is further defined such that the bus is a backend bus coupling the controllers and the storage devices.

In a twenty-fourth aspect, the invention also provides a computer program as well as a computer program product for use in conjunction with a computer system, the computer program product comprising a computer readable storage medium and a computer program mechanism embedded therein, the computer program mechanism, comprising: a program module that directs at least one of a plurality of controllers connected to a host computer, and one or more disk storage devices grouped into a data storage system, to function in a specified manner, the program module including instructions for: a first one of the controllers writes the first data to the storage devices and copies the first data to mirror caches associated with one or more other controllers

14

in response to a write command to write first data to the storage devices; and a second controller checks for the data in an associated one of the mirror caches and, if the data is in the associated mirror cache, returns the second data to a host computer that issued the read command in response to a read command to read second data from the storage devices.

In a twenty-fifth aspect, the computer program and computer program product are further defined such that the computer program mechanism and computer mechanism further include a program module including instructions: the first controller also writes the first data to its associated write cache in response to the write command to write first data to the storage devices; the second controller, in response to the read command to read second data from the storage devices: checks for the second data in the associated write cache, and, if the data is there, returns the second data to the host; checks for the second data in the read cache and, if the data is there, returns the second data to the host; and if the second data is not in the associated caches, retrieves the second data from the storage devices and returns the second data to the host computer that issued the read command.

While the present invention has been described with reference to a few specific embodiments, the description is illustrative of the invention and is not to be construed as limiting the invention. Various modifications may occur to those skilled in the art without departing from the true spirit and scope of the invention as defined by the appended claims.

25 For example, in none of the embodiments is it required that the controllers 104 have a one-to-one relationship with a set of associated read, write and write mirror caches 114, 116, 118 or with a host 102 for which it executes read and write commands. Additionally, it is not required that each controller 104 has a full complement of associated read, write and write mirror caches 114, 116, 118. Instead, all that is required by the present invention is that each controller 104 have an associated write mirror cache or other quickly accessed memory location into which other controllers

copy host write data for subsequent, speedy retrieval by the former controller 104 in response to a host read command.

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WHAT IS CLAIMED IS:

- 1. A synchronization method for use in an RAID system having multiple RAID controllers and a set of storage devices, the method including:
- in response to a write command to write first data to the storage devices, a first one of the controllers writes the first data to the storage devices and copies the first data to mirror caches associated with one or more other controllers; and

in response to a read command to read second data from the storage devices, a second controller checks for the data in an associated one of the mirror caches and, if the data is in the associated mirror cache, returns the second data to a host computer that issued the read command.

- 2. The synchronization method of claim 1, wherein the RAID controllers have associated read and write caches, the method further comprising:
- in response to the write command to write first data to the storage devices, the first controller also writes the first data to its associated write cache;

in response to the read command to read second data from the storage devices, the second controller:

checks for the second data in the associated write cache, and, if the data is there, returns the second data to the host;

checks for the second data in the read cache and, if the data is there, returns the second data to the host; and

if the second data is not in the associated caches, retrieves the second data from the storage devices and returns the second data to the host computer that issued the read command.

3. The synchronization method of claim 1, wherein the copying of the first data to the associated mirror caches comprises broadcasting the first data to the associated mirror caches over a backend bus to which the controllers and the storage devices are coupled.

- 4. The synchronization method of claim 3, wherein the broadcasting of the first data adds no more than minimal overhead to the writing of the first data to the storage devices.
- 5 5. The synchronization method of claim 4, wherein the broadcasting and the writing of the first data are performed simultaneously.
 - 6. The synchronization method of claim 3, wherein the backend bus comprises any combination of:
- one or more Fibre Channel buses; or one or more SCSI buses.
 - 7. The synchronization method of claim 1, wherein the storage devices comprise magnetic disks.

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8. The synchronization method of claim 1, wherein the copying of the first data to the associated mirror caches comprises transmitting, using a SCSI Vendor Unique command, the first data to the associated mirror caches over a backend bus to which the controllers and the storage devices are coupled.

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- 9. A cache system for use in a RAID system including a plurality of RAID controllers providing access to a set of storage devices, comprising:
 - a plurality of mirror caches accessible to the controllers;
- a first RAID being controller configured, when it receives a write command to
 write data to a specific address, to copy the data to the mirror caches of one or more
 different RAID controllers in addition to writing the data to the storage devices,

any of the RAID controllers being configured, after receiving a read command to read the data at the specific address, to attempt first to retrieve the data from an associated one of the mirror caches.

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- 10. The cache system of claim 9, wherein the plurality of controllers and the set of storage devices are connected via a backend bus.
- 11. The cache system of claim 10, wherein the backend bus comprises any 5 combination of:

one or more Fibre Channel buses; or one or more SCSI buses.

- 12. The cache system of claim 9, further comprising:
- a plurality of write caches accessible to the controllers;
 - a plurality of read caches accessible to the controllers; such that:

in response to a read command to read data at the specific address, the second controller:

checks for the data in its mirror cache and, if the data is there, returns the
data to a host computer that issued the read command;

checks for the data in its write cache, and, if the data is there, returns the data to the host;

checks for the data in its read cache and, if the data is there, returns the data to the host; and

20 if the data is not in any of the caches, retrieves the data from the storage devices and returns the data to the host.

- 13. The cache system of claim 9, wherein the first RAID controller is configured to copy the first data to the mirror caches using a SCSI Vendor Unique command
 25 transmitted over a SCSI bus to which the controllers and the storage devices are coupled.
 - 14. The cache system of claim 13, wherein the SCSI bus comprises a backend bus.

15. A synchronization method for use in a data storage system having at least first and second controllers and at least one storage device, the method including:

in response to a write command to write first data to the storage device, the first controller writes the first data to the storage device and copies the first data to a mirror cache associated with the second controller; and

in response to a read command to read second data from the storage device, the second controller checks for the data in an associated one of the mirror caches and, if the data is in the associated mirror cache, returns the second data to a host computer that issued the read command.

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16. The synchronization method of claim 15, wherein the controllers have associated read and write caches, the method further comprising:

in response to the write command to write first data to the storage device, the first controller also writes the first data to its associated write cache;

in response to the read command to read second data from the storage device, the second controller:

checks for the second data in the associated write cache, and, if the data is there, returns the second data to the host;

checks for the second data in the read cache and, if the data is there, returns the second data to the host; and

if the second data is not in the associated caches, retrieves the second data from the storage device and returns the second data to the host computer that issued the read command.

- 25 17. The synchronization method of claim 15, wherein the copying of the first data to the associated mirror caches comprises broadcasting the first data to the associated mirror caches over a bus to which the controllers and the storage devices are coupled.
- 18. The synchronization method of claim 17, wherein the broadcasting of the first data adds substantially no overhead to the writing of the first data to the storage devices.

PCT/US00/01921

- 19. The synchronization method of claim 18, wherein the broadcasting and the writing of the first data are performed substantially simultaneously.
- 20. The synchronization method of claim 18, wherein the bus comprises any combination of: one or more Fibre Channel buses; or one or more SCSI buses.
 - 21. The synchronization method of claim 15, wherein the storage devices comprise magnetic disk drives.
- 10 22. The synchronization method of claim 15, wherein the copying of the first data to the associated mirror caches comprises transmitting, using a SCSI Vendor Unique command, the first data to the associated mirror caches over a bus to which the controllers and the storage devices are coupled.
- 15 23. The synchronization method of claim 22, wherein the bus is a backend bus coupling the controllers and the storage devices.
- A computer program for use in conjunction with a computer system, the computer program product comprising a computer readable storage medium and a
 computer program mechanism embedded therein, the computer program mechanism, comprising:

a program module that directs at least one of a plurality of controllers connected to a host computer, and one or more disk storage devices grouped into a data storage system, to function in a specified manner, the program module including instructions for:

a first one of the controllers writes the first data to the storage devices and copies the first data to mirror caches associated with one or more other controllers in response to a write command to write first data to the storage devices; and

a second controller checks for the data in an associated one of the mirror caches and, if the data is in the associated mirror cache, returns the second data to a host

computer that issued the read command in response to a read command to read second data from the storage devices.

25. A computer program as in claim 24, the computer program mechanism and computer mechanism further including a program module including instructions

the first controller also writes the first data to its associated write cache in response to the write command to write first data to the storage devices;

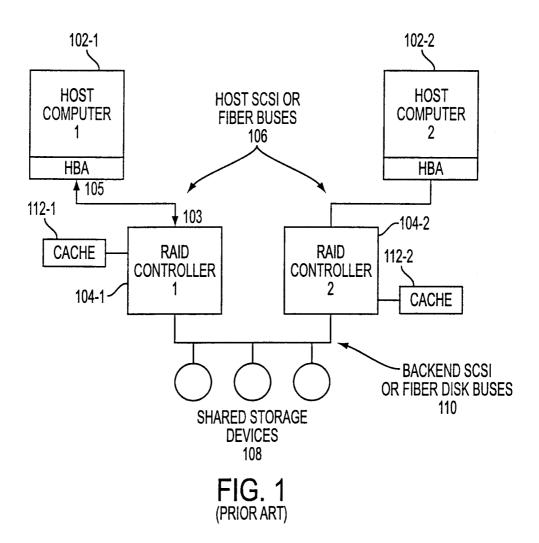
the second controller, in response to the read command to read second data from the storage devices:

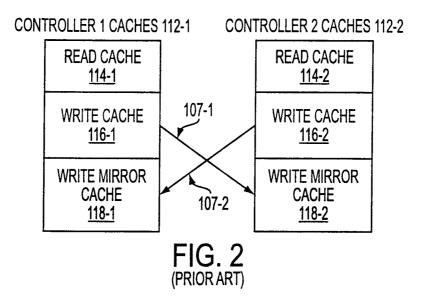
10 checks for the second data in the associated write cache, and, if the data is there, returns the second data to the host;

checks for the second data in the read cache and, if the data is there, returns the second data to the host; and

if the second data is not in the associated caches, retrieves the second data from the storage devices and returns the second data to the host computer that issued the read command.

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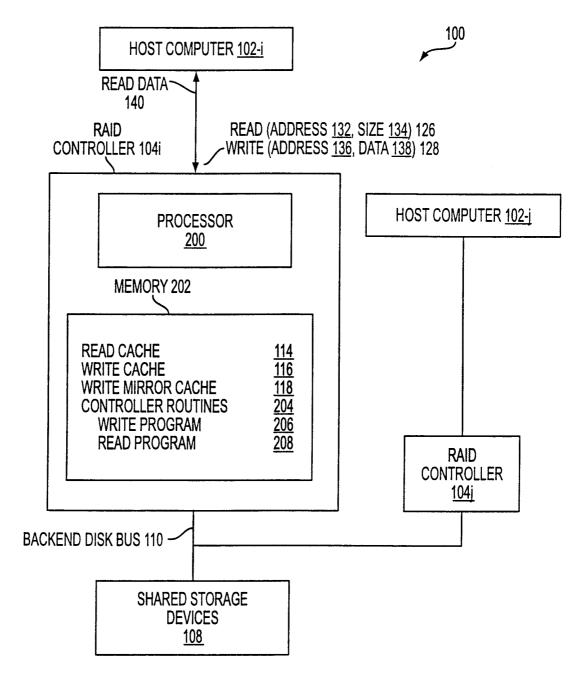
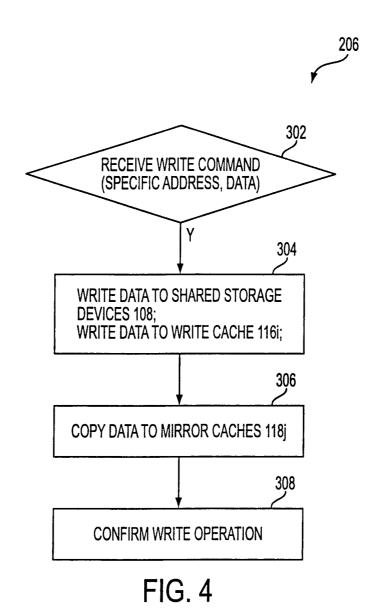


FIG. 3



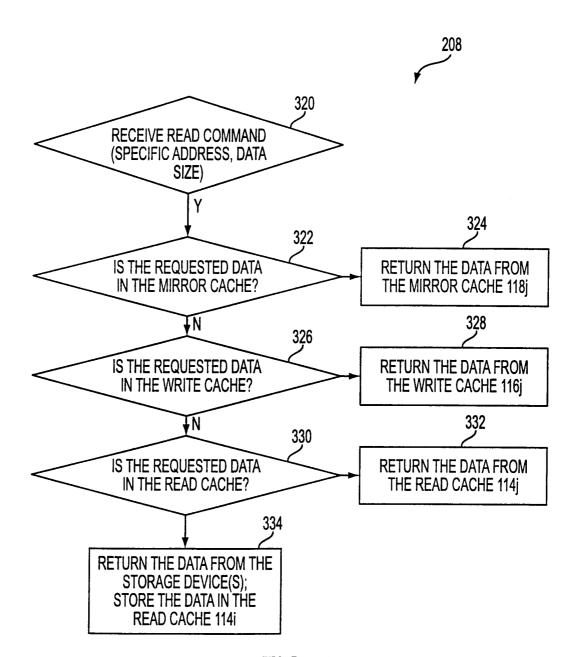
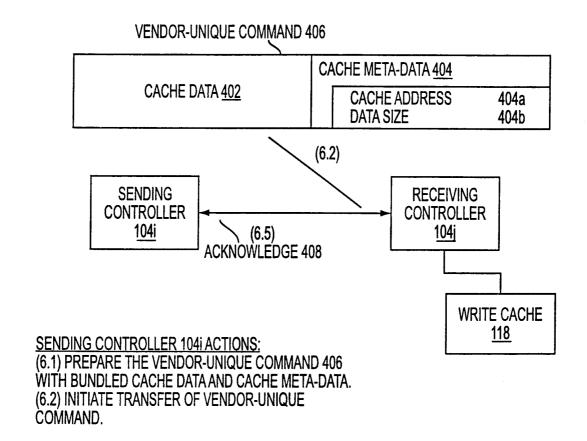


FIG. 5



RECEIVING CONTROLLER 104j ACTIONS:

- (6.3) RECEIVE THE VENDOR-UNIQUE COMMAND.
- (6.4) TRANSFER CACHE DATA AND CACHE META-DATA TO CORRECT POSITION IN WRITE MIRROR CACHE 118.
- (6.5) RETURN ACKNOWLEDGE 408

FIG. 6

INTERNATIONAL SEARCH REPORT

International application No. PCT/US00/01921

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A. CLASSIFICATION OF SUBJECT MATTER IPC(7) :G06F 12/08 US CL : 711/113; 714/6 According to International Patent Classification (IPC) or to both national classification and IPC									
B. FIELDS SEARCHED									
Minimum documentation searched (classification system followed by classification symbols)									
U.S. : 711/113, 114, 120, 124, 141, 142, 143; 714/5, 6, 7									
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched									
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)									
http://iel.ihs.com/ search terms: RAID, read cache, write cache, mirror									
C. DOCUMENTS CONSIDERED TO BE RELEVANT									
Category*	Citation of document, with indication, where	appropriate, of the relevant passages	Relevant to claim No.						
Y,P	US 5,974,506 A (SICOLA et al) 26 (col. 4 line 20.	October 1999, col. 3 line 58 to	1-25						
Y	US 5,761,705 A (DEKONING et al) (col. 6 line 21.	1-25							
Y	US 5,636,355 A (RAMAKRISHNAN lines 40-43.	1-25							
A	VARMA, A et al., Destage Algorithm Volatile Caches, 1995 ACM.	ns for Disk Arrays with Non-	1-25						
Purth	er documents are listed in the continuation of Box (C. See patent family annex.							
"A" doc	coial categories of cited documents:	*T* later document published after the inte date and not in conflict with the appli the principle or theory underlying the	cation but cited to understand						
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