



US00D342236S

# United States Patent [19]

[11] Patent Number: **Des. 342,236**

Albano et al.

[45] Date of Patent: **\*\* Dec. 14, 1993**

[54] **COMBINED HOUSING FOR A COMPUTER DISPLAY, A PORTABLE PROCESSING UNIT, AND AN INPUT AND OUTPUT INTERFACE MODULE**

*Assistant Examiner*—Freda S. Nunn  
*Attorney, Agent, or Firm*—Blakely Sokoloff Taylor & Zafman

[75] Inventors: **Dan Albano**, Carlisle; **Dave Desilets**, Hopkinton, both of Mass.; **William Izzicupo**, Windham, N.H.; **Steve Peart**, Los Gatos, Calif.

### [57] CLAIM

The ornamental design for a combined housing for a computer display, a portable processing unit, and an input and output interface module, as shown and described.

[73] Assignee: **Sun Microsystems, Inc.**, Mountain View, Calif.

### DESCRIPTION

[\*\*] Term: **14 Years**

FIG. 1 is an exploded top rear and side perspective view of the combined housing for a computer display, a portable processing unit, and an input and output interface module showing our new design;

[21] Appl. No.: **745,097**

FIG. 2 is a side view thereof shown in an operative position, the opposite side being identical in appearance; FIG. 3 is a side view thereof with the portable processing unit and the input and output interface module being unattached;

[22] Filed: **Aug. 13, 1991**

[52] U.S. Cl. .... **D14/100**

[58] Field of Search ..... **D14/100, 101, 106, 113, D14/124-127; 340/700, 706; 364/708, 709.04; 361/390-395**

FIG. 4 is an enlarged bottom front and opposite side perspective view of the portable processing unit shown separately for clarity of illustration only;

### [56] References Cited

#### U.S. PATENT DOCUMENTS

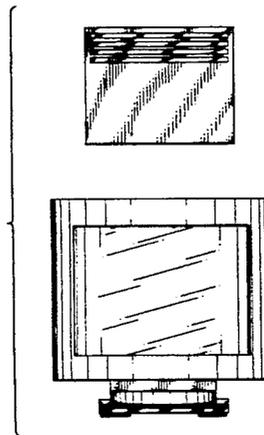
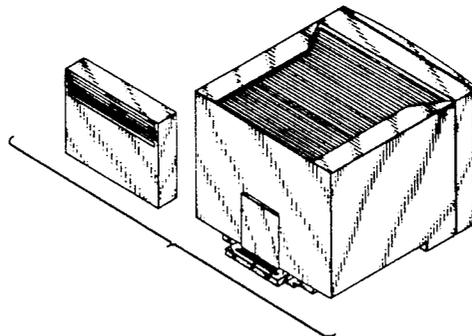
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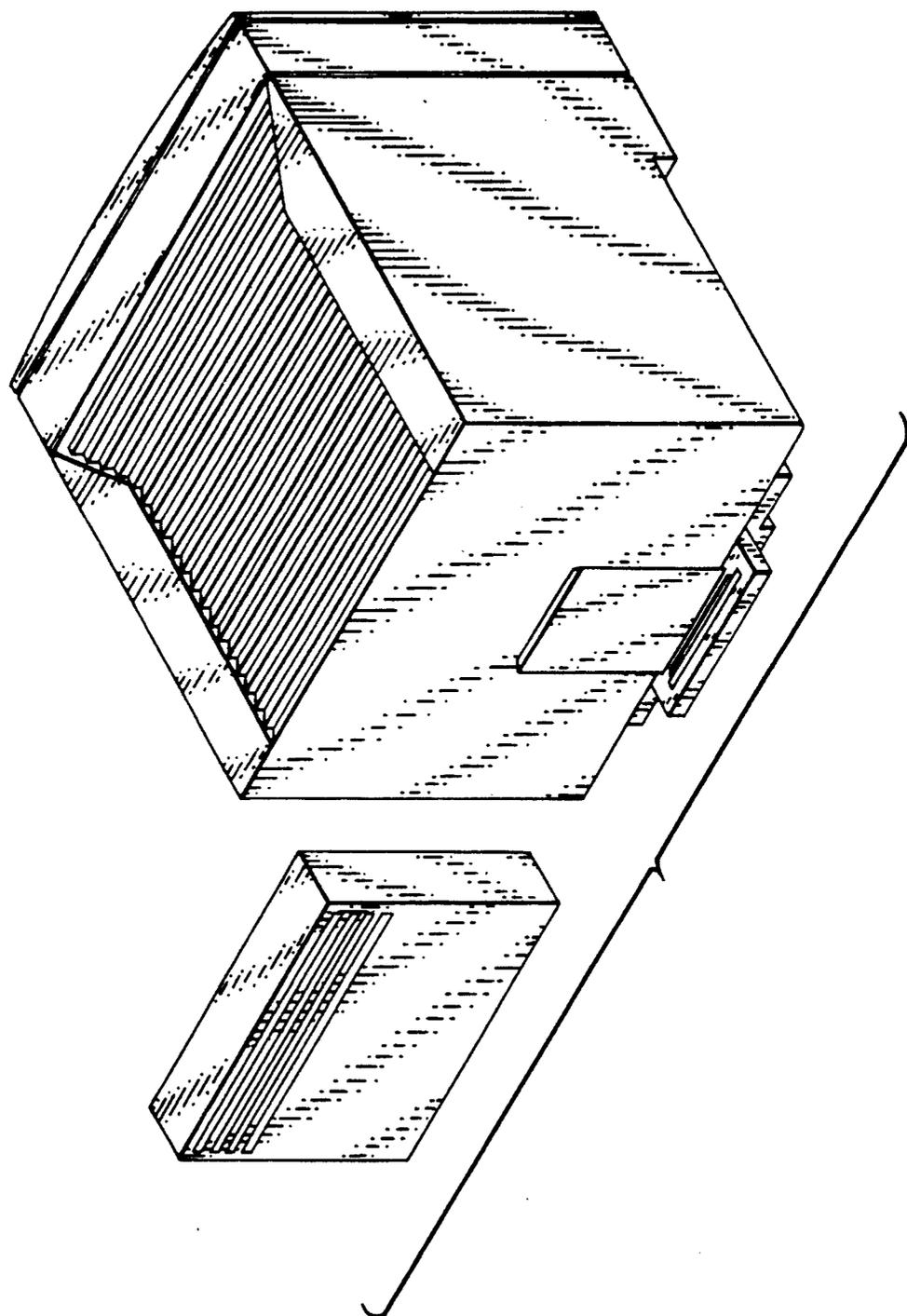
FIG. 5 is an enlarged top plan view of the input and output interface module shown separately for clarity of illustration only;

FIG. 6 is an exploded front view of the computer display and the portable processing unit shown separately for clarity of illustration only; and,

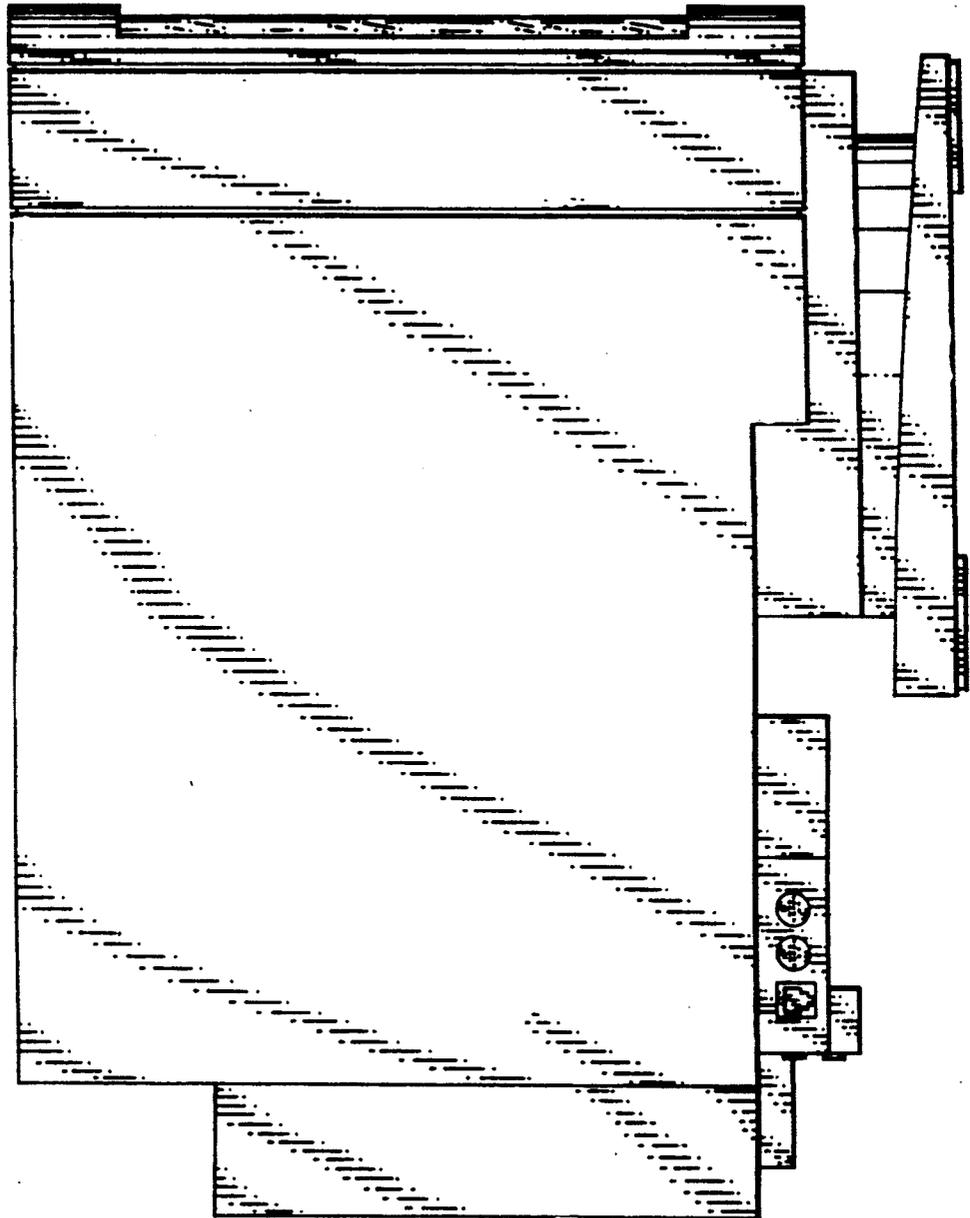
FIG. 7 is an enlarged bottom front and side view of the input and output interface module shown separately for clarity of illustration only.

*Primary Examiner*—Wallace R. Burke

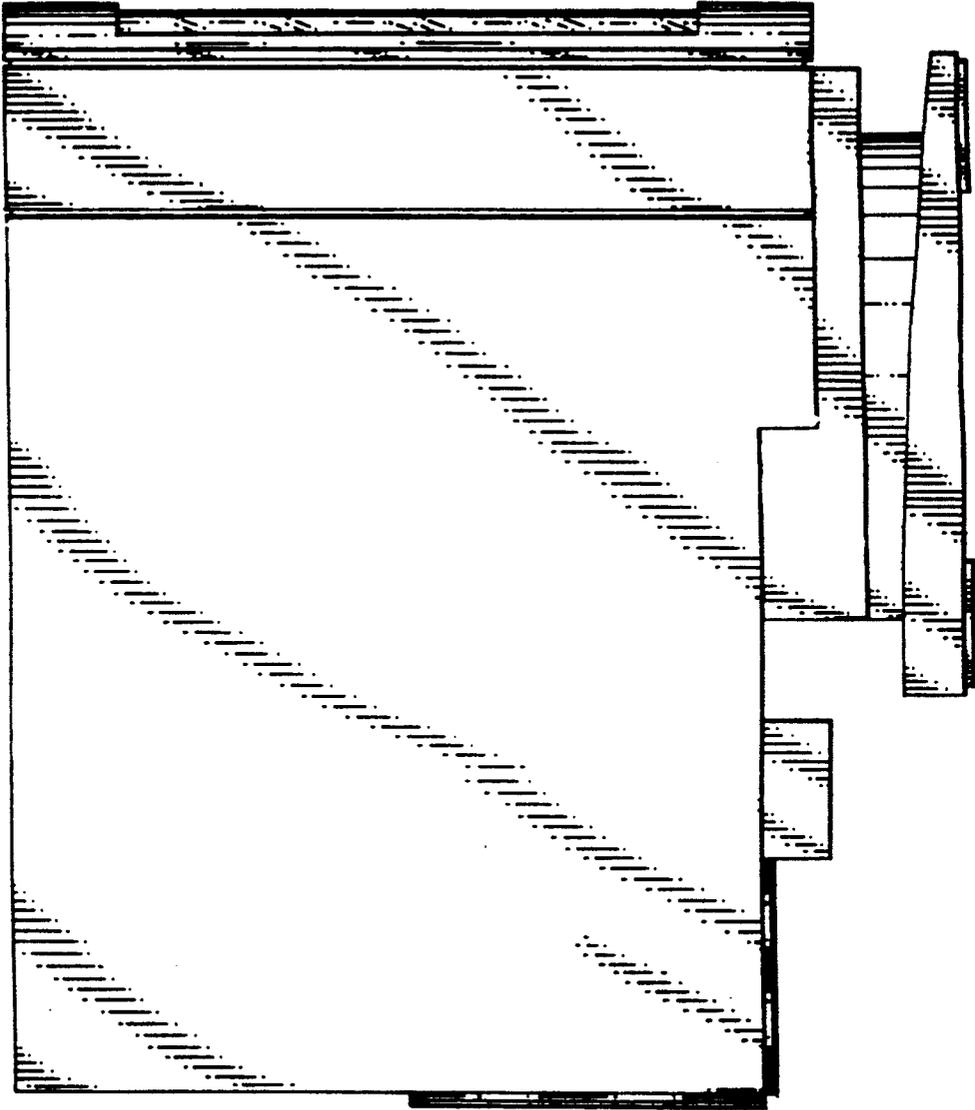




**FIG. 1**

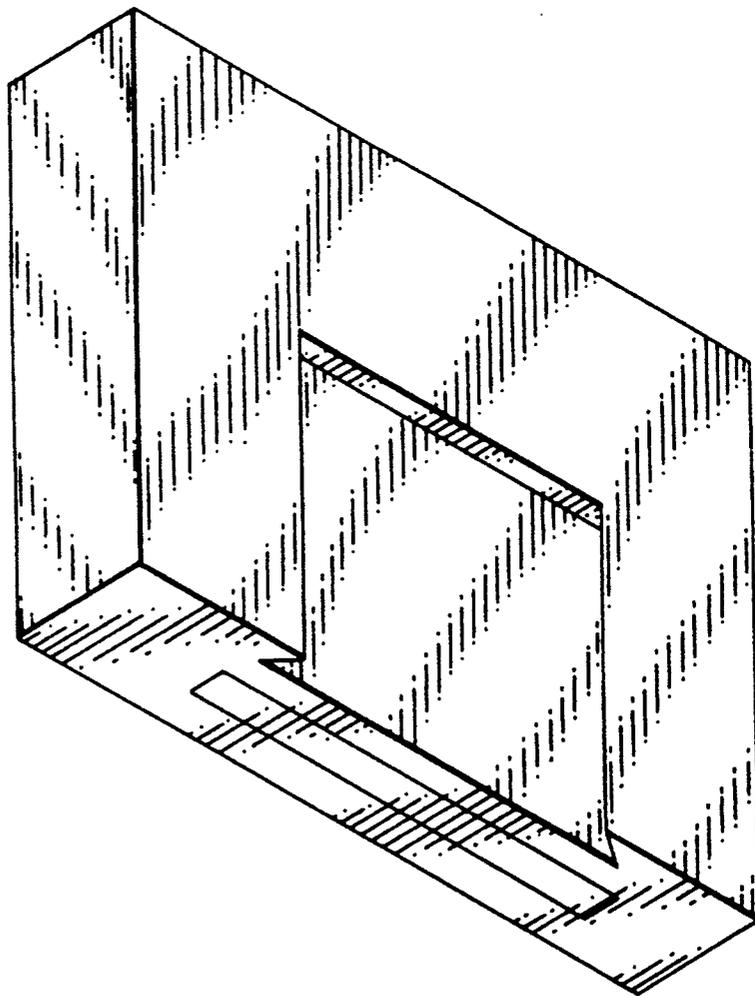


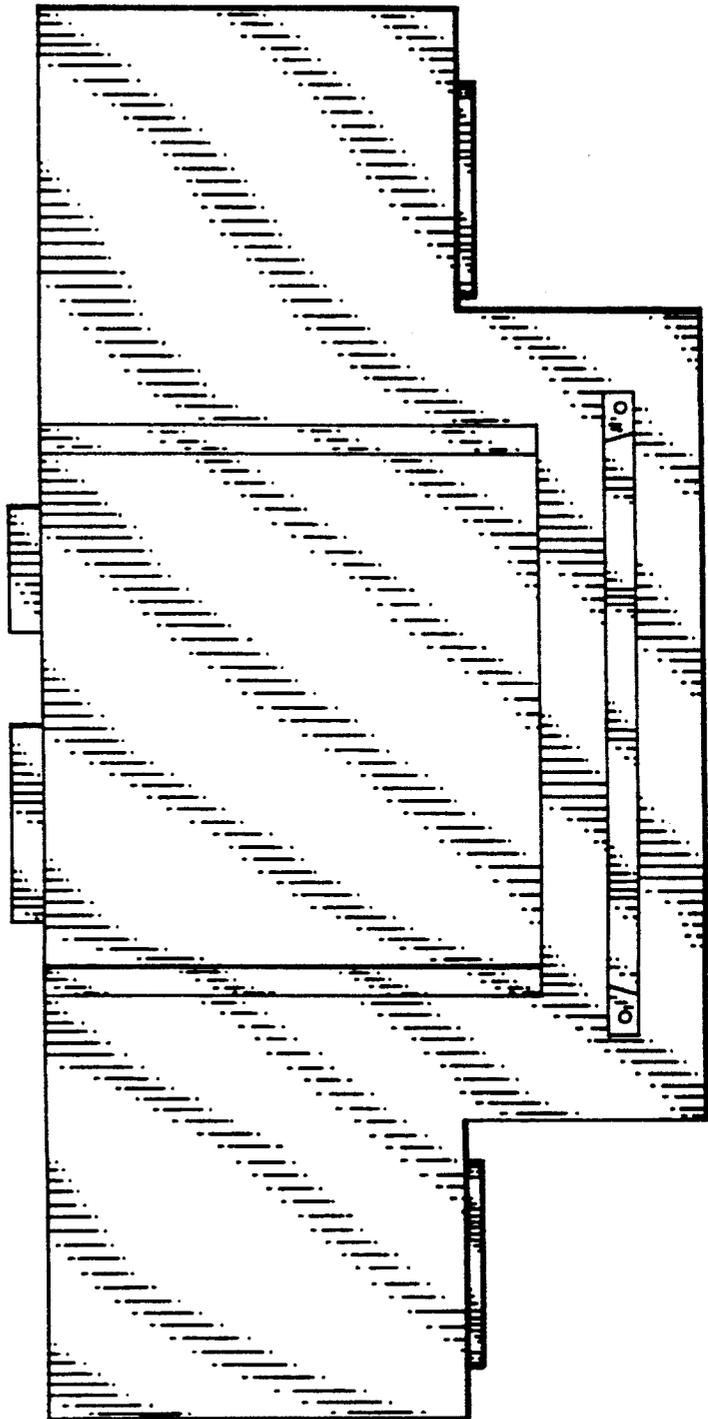
**FIG 2**



**FIG. 3**

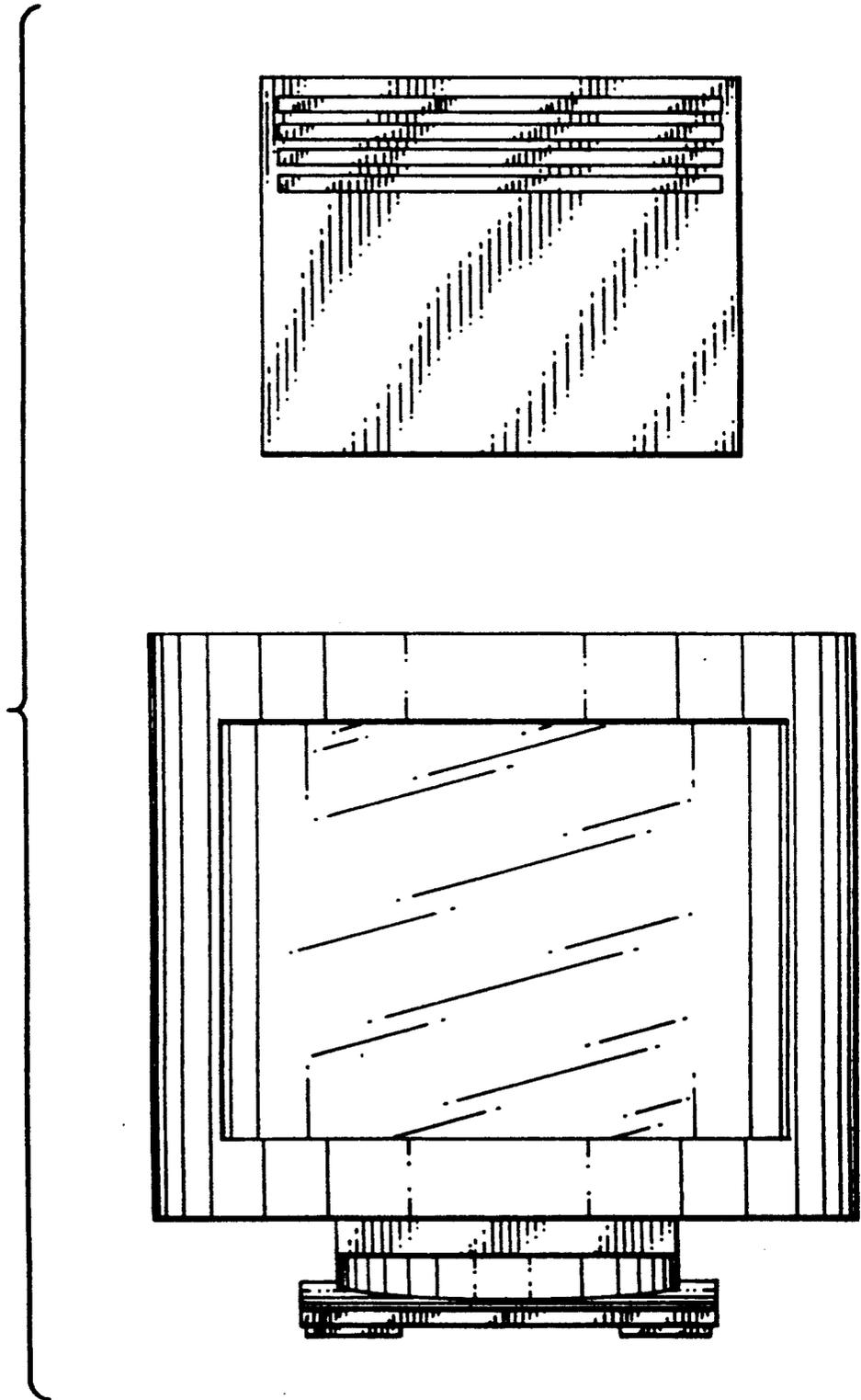
**FIG 4**

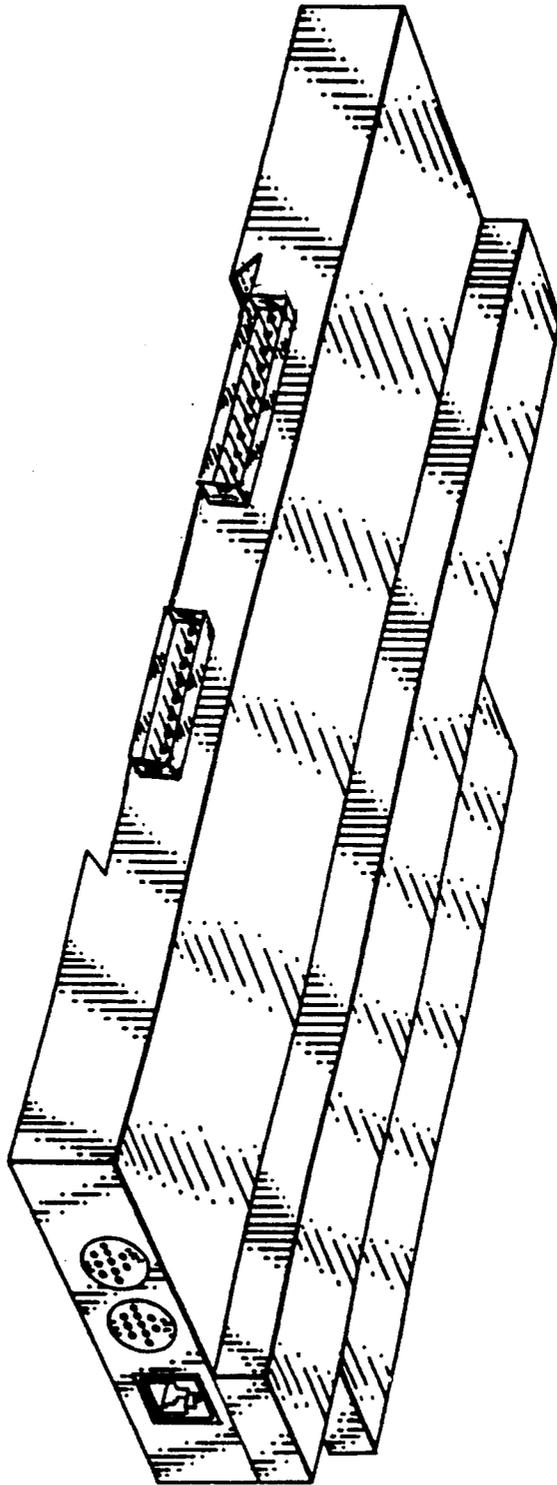




**FIG 5**

**FIG 6**





**FIG. 7**

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : Des. 342,236  
DATED : December 14, 1993  
INVENTOR(S) : Albano et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

In item [57] **DESCRIPTION**, please delete the old descriptions for Figures 2-4 and insert the following descriptions:

-- Figure 2 is an enlarged side view thereof shown in an operative position, the opposite side being identical in appearance.

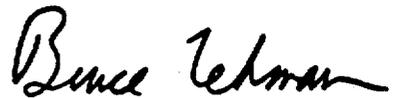
Figure 3 is an enlarged side view thereof with the Portable Processing Unit and the Input And Output Interface Module being unattached.

Figure 4 is an enlarged bottom rear and side perspective view of the Portable Processing Unit shown separately for clarity of illustration only. --

Signed and Sealed this

Third Day of February, 1998

Attest:



BRUCE LEHMAN

Attesting Officer

Commissioner of Patents and Trademarks