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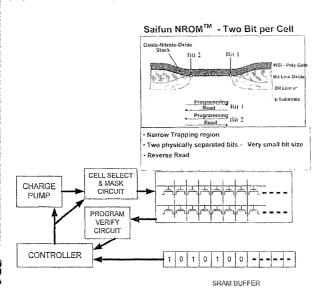
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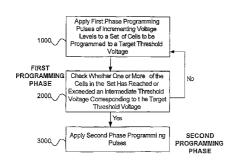
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(54) Title: METHOD, SYSTEM AND CIRCUIT FOR PROGRAMMING A NON-VOLATILE MEMORY ARRAY



(57) Abstract: The present invention is a multi-phase method, circuit and system for programming non-volatile memory ("NVM") cells in an NVM array (100). The present invention may include a controller (110) to determine when, during a first programming phase (2000), one or more NVM cells of a first set of cells reaches or exceeds a first intermediate voltage, and to cause a charge pump circuit (130) to apply to a terminal of the one or more cells (100) in the first set second phase programming (3000) pulses to induce relatively greater threshold voltage changes in cells having less stored charge than in cells having relatively more stored charge.



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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
appropriate, of the relevant	passages	Relevant to claim No.
97), Figs. 7 and 2, Column 9	Fines 1-12	1 - 6, 10 - 12, 14 - 20, 24 - 26, and 28 - 33
A US PUB 2004/0153621 A1 (Polansky et al.) 5 August 2004 (05.08.2004), [0012], [0015], and [0052] A US PUB 2002/0191444 A1 (Gregori et al.) 19 December 2002 (19.12.2002), abstract lines 3-9		1, 3, 11, 14, 15, 17, 20, 23, 26, 29, 32, and 1, 15, 29, 32, and 33
See patent famil	ly annex.	
"T" later document publ	olished after the intern	national filing date or priority
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	the extent that such document arms of data base and, where present the experimental properties of the relevant 197), Figs. 7 and 2, Column 9 gust 2004 (05.08.2004), [001] cember 2002 (19.12.2002), a later document pub date and not in conprinciple or theory "X" document of partice considered novel or when the document "Y" document of partice considered to involcombined with one being obvious to a present the properties of the present the properties of the present the pre	she extent that such documents are included in the extent that such document passages appropriate, of the relevant passages are the combined and not in conflict with the applicate principle or theory underlying the inverse when the document is taken alone appropriate with the document is taken alone appropriate with the document of particular relevance; the considered with note or inventive step to being obvious to a person skilled in the document member of the same patent fare. Date of mailing of the international search and the passage appropriate appropriate appropriate appropriate, and the passage appropriate appropriate appropriate and the passage appropriate appropr