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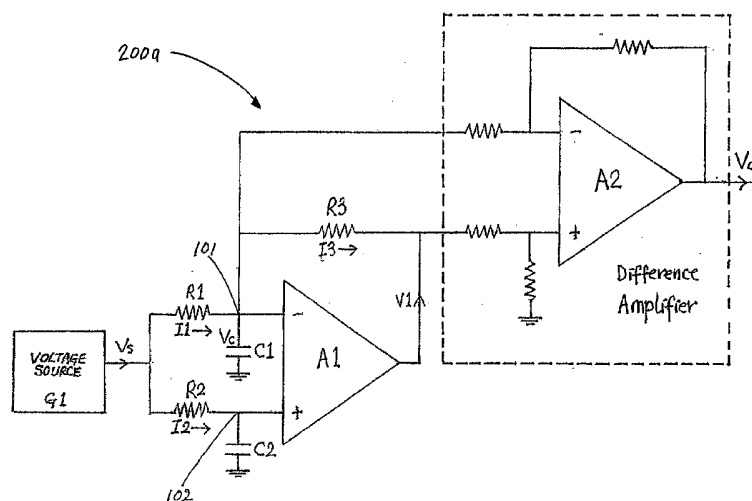
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(54) Title: LINEAR CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH



(57) Abstract: Capacitance measurement apparatus that enhances the sensitivity and accuracy of capacitive transducers, proximity sensors, and touchless switches. Each of two capacitors (C1, C2) under measurement has one end connected to ground and is kept at substantially the same voltage potential by operational amplifier (A1) or amplifiers (A0, A1) using negative feedback. The apparatus is driven by a periodic e.g. sinusoidal signal source (G1) or sources (G1, G2) and includes a difference amplifier (A2) operative to produce an electrical signal having a linear relationship with a specified arithmetic function of the capacitances of the two capacitors (C1, C2). A touchless switch is implemented using the capacitance measurement apparatus. The touchless switch includes two sensor electrodes (E1, E2) that correspond to the two capacitors (C1, C2) under measurement and in one embodiment has a front surface in the form of a container.

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TITLE OF THE INVENTION

LINEAR CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH

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CROSS REFERENCE TO RELATED APPLICATIONS

This application claims benefit of U.S. Provisional Patent Application No. 60/690,486 filed June 15, 2005 entitled LINEAR CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH, U.S. Provisional Patent Application No. 60/662,378 filed March 17, 2005 entitled CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH, U.S. Provisional Patent Application No. 60/619,697 filed October 19, 2004 entitled DIFFERENTIAL CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH, and U.S. Provisional Patent Application No. 60/601,610 filed August 16, 2004 entitled DIFFERENTIAL CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH.

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BACKGROUND OF THE INVENTION

The present invention relates generally to capacitance measurement apparatus and techniques, and more specifically to proximity detectors such as touchless switches that employ

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capacitance measurement techniques.

In recent years, there has been an increasing need for improved techniques of operating publicly accessible facilities and equipment without requiring a user to make physical contact
5 with a surface of a manual activation device such as a touch switch. Such facilities and equipment include elevators, vending machines, security access panels, information terminals, etc. By not requiring a user to physically touch a switch that may have been touched and contaminated by others who had previously used the
10 facilities or equipment, the spread of germs and diseases may be significantly reduced.

For example, a user typically operates a public facility such as an elevator by physically touching one or more switches, which may have been previously touched by a substantial number of
15 individuals. Some of these individuals may have come from environments where they may have been exposed to contaminants such as potentially harmful or contagious toxins or pathogenic disease organisms. When such individuals make physical contact with one or more of the switches required to operate an elevator, there
20 is a risk that the individuals may deposit contaminants onto the surface of the switches, where they may remain viable for an extended period of time. These contaminants may be later transferred from the switches to subsequent elevator users who physically touch the switches, thereby potentially causing the
25 subsequent users to become afflicted with diseases or other

serious medical conditions.

During outbreaks of the severe acute respiratory syndrome (SARS) in Asia, many members of the public were afraid to use any public facilities that required them to touch a manual activation
5 device such as a touch switch. To mitigate the fears of the public, programs were instituted for periodically cleaning and disinfecting the surfaces of these devices. Such programs are typically ineffective, because no matter how well these activation devices are cleaned and disinfected, they may become
10 contaminated once again by a subsequent user. As a result, the risk of transferring potentially harmful contaminants from manual activation devices such as touch switches to subsequent users of publicly accessible facilities and equipment continues unabated.

Capacitance-based proximity detectors have been employed to
15 implement activation devices that do not require a user to physically touch a surface of the device. Such proximity detectors operate according to the principle that an electric field and a capacitance are generated between two conductive objects that have different voltage potentials and are physically
20 separated from one another. The capacitance between the two conductive objects generally increases as the surface areas of the objects increase, or as the distance between the objects decreases.

Conventional capacitance-based proximity detectors have
25 drawbacks, however, when they are used to implement a touchless

switch. For example, it is generally difficult to adjust the sensitivity of a capacitance-based proximity detector to assure that a touchless switch employing such a proximity detector can be reliably activated by a broad range of users, and that the switch is not susceptible to noise and/or environmental changes. This is due to the relatively small equivalent capacitance that the capacitance-based proximity detector is required to measure when implementing a touchless switch.

Specifically, when a human body is very near or proximate to a sensor electrode of a capacitance-based proximity detector, the proximity detector effectively measures the equivalent capacitance of two series capacitors, assuming that the stray capacitance between the capacitance sensing circuitry and circuit ground is ignored. One of the series capacitors is formed between the sensor electrode and the human body, and the other capacitor is formed between the human body and earth ground. The amount of capacitance between the sensor electrode and the human body depends primarily on the distance between them, and to a lesser extent on the size and characteristics of the human body. For example, when the human body is not very near the sensor electrode, the amount of capacitance between the sensor electrode and the human body is significantly smaller than the amount of capacitance between the human body and ground. Accordingly, a touchless switch implemented using a capacitance-based proximity detector must measure an equivalent capacitance that is significantly

smaller than the capacitance typically measured by a conventional touch switch.

Fig. 1 depicts a touchless switch implemented using a capacitance-based proximity detector 100 including a sensor electrode 112, capacitance sensing circuitry 114, and the equivalent capacitances of the capacitors formed between a human finger and the sensor electrode 112 (C_A), the rest of the human body and the sensor electrode 112 (C_B), the human body and ground (C_C), and the capacitance sensing circuitry 114 and ground (C_D), which in this analysis can be ignored. When the human finger is proximate to the sensor electrode 112, the capacitance between the human body and the sensor electrode 112 can be taken as the sum of the capacitance C_A between the finger and the sensor electrode 112, and the capacitance C_B between the rest of the human body and the sensor electrode 112. If the human finger is not very near the sensor electrode 112, then any changes in the capacitance C_A between the finger and the sensor electrode 112 are typically very small. As a result, any extraneous common-mode disturbances resulting from electrical noise or interference, changes in the characteristics of the environment, changes in the capacitance C_C between the human body and ground, and/or changes in the capacitance C_B between the rest of the human body and the sensor electrode 112 due to changes in the distance between the rest of the human body and the sensor electrode 112, changes in the size or characteristics of the human body, etc., may be equal

to or greater than the corresponding changes in the capacitance C_A between the human finger and the sensor electrode 112.

Accordingly, if the sensitivity of the capacitance-based proximity detector 100 is adjusted to be highly sensitive, then
5 the proximity detector 100 may be actuated unintentionally, due to the various extraneous common-mode disturbances listed above. However, if the capacitance-based proximity detector 100 has reduced sensitivity, then the proximity detector 100 may be inoperable due to the inability to detect the small amount of
10 capacitance between the finger of a user and the sensor electrode 112 at a reasonable distance.

A touch switch implemented using the capacitance-based proximity detector 100 generally fares much better than a touchless switch because when a human finger touches the surface
15 of a touch switch, the area of contact is typically much larger than just the area of a fingertip. Further, the distance between a finger and a sensor electrode of the touch switch is typically much smaller than the corresponding distance between a finger and the sensor electrode 112 of the touchless switch, even if the
20 sensor electrode of the touch switch is disposed behind an insulating surface. The changes in capacitance between a human finger and the sensor electrode of a touch switch are therefore much larger than the corresponding changes in capacitance between a human finger and the sensor electrode 112 of a touchless switch.
25 Accordingly, the problems described above relating to the

detection of changes in the capacitance C_A between a human finger and the sensor electrode 112 of the touchless switch, e.g., the changes in the capacitance C_B or C_C due to different users, are relatively insignificant in a touch switch.

5 One way of avoiding the problems described above relating to extraneous common-mode disturbances in a touchless switch is to employ known differential signal measurement techniques. Such differential signal measurement techniques can be used in touchless switches that include two sensor electrodes arranged
10 so that the switch is actuated when the capacitance between a human finger and one of the sensor electrodes exceeds a preset threshold level relative to a second capacitance between the finger and the other sensor electrode. By directly comparing these first and second capacitances in a differential measurement to determine
15 whether to actuate the touchless switch, extraneous common-mode disturbances that can adversely affect the measurement can be effectively canceled out.

U.S. Patent No. 6,310,611 filed October 30, 2001 entitled DIFFERENTIAL TOUCH SENSOR AND CONTROL CIRCUIT THEREFORE (the '611
20 patent) discloses a touch sensor that employs a differential signal measurement technique. As disclosed in the '611 patent, the touch sensor includes a first sensor electrode, a second sensor electrode positioned proximate to the first electrode, a differential circuit connected to the first and second electrodes,
25 and a pulse or other signal source configured to generate an

electric field between the first and second electrodes. Although the touch sensor of the '611 patent is configured to perform a differential measurement, the touch sensor does not operate by measuring capacitance. Instead, the touch sensor measures changes in the voltage difference between the two sensor electrodes caused by the introduction of an object affecting the electric field around the two electrodes. The touch sensor employs a differential circuit to provide an output signal that is responsive to this difference in voltage between the two electrodes.

The touch sensor disclosed in the '611 patent has drawbacks, however, when used to implement a touchless switch. For example, the above-described changes in the voltage difference between the two sensor electrodes of the touch sensor resulting from the introduction of an object are caused by the interaction of the electric fields associated with the sensor electrodes and the object. This interaction of electric fields is relatively complex because the two sensor electrodes and the object are at different voltage potentials, and there is no precise relationship governing the voltage difference between the sensor electrodes and the proximity of the object to the sensor electrodes. Furthermore, the methods disclosed in the '611 patent to measure the voltage difference between the sensor electrodes are only effective if the voltage difference is significant enough as in the case of a touch switch. Therefore, the approach disclosed in

the '611 patent is not precise or sensitive enough to be used in a touchless switch.

U.S. Patent No. 6,456,477 filed September 24, 2002 entitled LINEAR CAPACITANCE DETECTION CIRCUIT (the '477 patent) discloses
5 capacitance detection circuitry that employs a differential signal measurement technique. As disclosed in the '477 patent, the linear capacitance detection circuitry includes a circuit that measures a difference in capacitance between a first capacitor and a second capacitor by driving the two capacitors
10 with pulses. The capacitance detection circuitry further includes an operational amplifier with negative feedback configured to maintain the two capacitors at substantially equal voltage potentials. As a result, there is a linear relationship between an electrical signal produced by the operational
15 amplifier and the ratio of the capacitances of the two capacitors. The approach disclosed in the '477 patent also has drawbacks, however, in that it requires pulse signals, which can introduce transient noises and instability to the operational amplifier and can adversely affect the accuracy of the operational amplifier
20 output. Although low pass filters and a feedback capacitor may be employed at the inputs of the operational amplifier to mitigate the effects of transient noises and instability, the addition of such components adversely affects the accuracy and sensitivity of the capacitance detection circuitry.

25 It would therefore be desirable to have a capacitance

measurement apparatus and technique, and a proximity detector such as a touchless switch employing a capacitance measurement technique, that avoid the drawbacks of the above-described approaches.

5

BRIEF SUMMARY OF THE INVENTION

In accordance with the present invention, a capacitance measurement apparatus and technique are provided that can be employed to enhance the sensitivity and accuracy of many different types of capacitive transducers, proximity sensors, and touchless switches. The presently disclosed capacitance measurement apparatus directly and accurately produces a linear response to changes in each of the ratios of the capacitance of a capacitor/capacitive transducer to the capacitance of one or more other different capacitors/capacitive transducers with adjustable offset, while maintaining all of the capacitors/capacitive transducers at substantially identical voltage potentials at all times. The presently disclosed capacitance measurement apparatus also produces a linear response to changes in each of the differences between the capacitance of a capacitor/capacitive transducer multiplied by a first constant factor and the capacitance of one or more other different capacitors/capacitive transducers each being multiplied by a respective second constant factor, while maintaining all of the capacitors/capacitive transducers at substantially identical

voltage potentials at all times.

Additionally, the presently disclosed capacitance measurement apparatus directly and accurately produces a linear response to changes in the capacitance, or changes in the reciprocal of the capacitance, of a capacitor/capacitive transducer with adjustable offset, without requiring special calibration or adjustment over a wide range of capacitance values. The presently disclosed capacitance measurement apparatus also provides a simple way of measuring the capacitance, or the reciprocal of the capacitance, of a large number of capacitors/capacitive transducers, or comparing the capacitance of a large number of capacitors/capacitive transducers with the capacitance of a large number of sets of capacitors/capacitive transducers.

The presently disclosed capacitance measurement apparatus employs a plurality of operational amplifiers for maintaining the voltage potentials of multiple capacitors/capacitive transducers undergoing comparison or measurement at substantially the same voltage potential at all times. Because the multiple capacitors/capacitive transducers are maintained at substantially the same voltage potential, there is essentially no capacitance between them. For this reason, the capacitance measurement apparatus can be employed to measure small changes in the capacitances of the multiple capacitors/capacitive transducers without having adjacent capacitors/capacitive

transducers affect the capacitance measurement, even when the capacitors/capacitive transducers are positioned relatively close to one another.

In one embodiment, the capacitance measurement apparatus
5 includes a first operational amplifier A1, and a second operational amplifier A2 configured as a difference amplifier. A difference amplifier, as the term is used herein, refers to a circuit or device that amplifies a difference between two input signals and includes different types of differential DC
10 amplifiers such as instrumentation amplifiers, etc. Each of two capacitors C1 and C2 undergoing comparison or measurement has one end connected to circuit ground, and another end connected to one of the differential inputs of operational amplifier A1. Capacitor C1 is connected to the inverting input of operational amplifier
15 A1, and capacitor C2 is connected to the non-inverting input of operational amplifier A1. Both capacitors C1 and C2 are driven by the output of a periodic varying voltage source such as a sinusoidal voltage source through respective resistors connected to corresponding inputs of operational amplifier A1. A feedback
20 resistor is connected between the output of operational amplifier A1 and its inverting input. Due to the high open loop gain of operational amplifier A1, capacitors C1 and C2 are maintained at substantially the same voltage potential at all times. There is a linear relationship between the magnitude of the current flowing
25 through the feedback resistor and the ratio of the capacitance

of capacitor C1 to the capacitance of capacitor C2. Further, the current flowing through the feedback resistor is in-phase or out-of-phase with the currents flowing through the resistors connected to the periodic varying voltage source, depending on whether the ratio is less than or greater than a specified value. The phase and magnitude of the current flowing through the feedback resistor can be measured by difference amplifier A2, having one of its differential inputs connected to the output of operational amplifier A1 and another differential input connected to one of the differential inputs of operational amplifier A1.

In a second embodiment, the capacitance measurement apparatus includes a first operational amplifier A1, and a second operational amplifier A2 configured as a difference amplifier. Each of two capacitors C1 and C2 undergoing comparison or measurement has one end connected to circuit ground, and another end connected to one of the differential inputs of operational amplifier A1. Capacitor C1 is connected to the inverting input of operational amplifier A1, and capacitor C2 is connected to the non-inverting input of operational amplifier A1. The non-inverting input of operational amplifier A1 is driven directly by the output of a first periodic varying current source such as a sinusoidal current source, while the inverting input of operational amplifier A1 is driven directly by a second periodic varying current source whose output is K (a constant) times that of the first periodic varying current source. A

feedback resistor is connected between the output of operational amplifier A1 and its inverting input. Due to the high open loop gain of operational amplifier A1, capacitors C1 and C2 are maintained at substantially the same voltage potential at all times. There is a linear relationship between the magnitude of the current flowing through the feedback resistor and the ratio of the capacitance of capacitor C1 to the capacitance of capacitor C2. Further, the current flowing through the feedback resistor is in-phase or out-of-phase with the outputs of the periodic varying current sources, depending on whether the ratio is less than or greater than the value of K. The phase and magnitude of the current flowing through the feedback resistor can be measured by difference amplifier A2, having one of its differential inputs connected to the output of operational amplifier A1 and another differential input connected to one of the differential inputs of operational amplifier A1.

In a third embodiment, the capacitance measurement apparatus includes first and second operational amplifiers A0 and A1, and a third operational amplifier A2 configured as a difference amplifier. Each of two capacitors C1 and C2 undergoing comparison or measurement, having capacitances of c_1 and c_2 , respectively, has one end connected to circuit ground, and another end connected to the inverting input of operational amplifier A0 or A1. Capacitor C1 is connected to the inverting input of operational amplifier A1, and capacitor C2 is connected to the inverting input

of operational amplifier A0. The non-inverting inputs of operational amplifiers A0 and A1 are both driven directly by a periodic varying voltage source such as a sinusoidal voltage source. A first feedback resistor R1 having a resistance of r_1 is connected between the output of operational amplifier A1 and its inverting input. A second feedback resistor R2 having a resistance of r_2 is connected between the output of operational amplifier A0 and its inverting input. Due to the high open loop gain of operational amplifiers A0 and A1, the two capacitors C1 and C2 are maintained at substantially the same voltage potential as the periodic varying voltage source at all times. The output of operational amplifier A1 is connected to the non-inverting input of difference amplifier A2, and the output of operational amplifier A0 is connected to the inverting input of difference amplifier A2. The output of difference amplifier A2 is proportional to $(r_1 \cdot c_1 - r_2 \cdot c_2)$, and is in-phase or out-of-phase with the currents flowing through resistors R1 and R2, depending on whether $(r_1 \cdot c_1 - r_2 \cdot c_2)$ is greater than or less than zero.

Each embodiment of the presently disclosed capacitance measurement apparatus can be configured for comparing the capacitance of a capacitor/capacitive transducer to the capacitance of a plurality of other different capacitors/capacitive transducers, while maintaining all of the capacitors/capacitive transducers at substantially the same voltage potential. Additionally, by switching the respective

capacitors/capacitive transducers in and out for subsequent comparison or measurement, each embodiment of the capacitance measurement apparatus can sequentially measure the capacitance, or the reciprocal of the capacitance, of multiple
5 capacitors/capacitive transducers, or compare the capacitance of multiple capacitors/capacitive transducers to the capacitance of multiple sets of capacitors/capacitive transducers.

Touchless switches and proximity sensors are also provided that employ embodiments of the presently disclosed capacitance
10 measurement apparatus. The touchless switches are configured to be actuated by a human finger or a finger-like object, requiring the finger or finger-like object to reach a specified boundary before actuating the switch. The touchless switches have reduced susceptibility to unintended actuations, and reduced sensitivity
15 to changes in environmental factors such as temperature, humidity, etc., and to electrical noise, while having a simple and rugged construction. The touchless switches can be used in hygiene-sensitive applications, industrial control panels, and a wide variety of facilities and equipment accessible to the
20 general public, including but not limited to elevators, vending machines, security access panels, information terminals, etc.

In one embodiment, the touchless switch includes a front surface, and two adjacent sensor electrodes maintained at substantially the same voltage potential disposed on or behind
25 the front surface of the switch. As a result, there is

substantially no capacitance between the two sensor electrodes, and therefore the sensor electrodes operate essentially independent of one another. One of the sensor electrodes is a center electrode, and the other sensor electrode is an outer electrode. The center electrode is spaced from and at least partly surrounded by the outer electrode. When the tip of a human finger or finger-like object is near or proximate to the center electrode, the presence of the finger or finger-like object can be detected using an embodiment of the capacitance measurement apparatus disclosed herein. The capacitance measurement apparatus can be employed to measure the ratio of the capacitance of the two sensor electrodes with respect to the finger or finger-like object, or the difference between the capacitance of one sensor electrode with respect to the finger or finger-like object multiplied by a first constant factor, and the capacitance of the other sensor electrode with respect to the finger or finger-like object multiplied by a second constant factor, thereby substantially canceling out extraneous common-mode disturbances such as the capacitance between the rest of the human body and the sensor electrodes, the capacitance between the human body and earth ground, environmental changes, electrical noise, etc., which tend to affect both sensor electrodes equally due to their close proximity to one another. The capacitance ratio and difference measurements performed by the capacitance measurement apparatus are facilitated by the fixed geometrical shape, size, and relative

positions of the two sensor electrodes. The outer electrode may be placed in front of the center electrode so that initially, when the finger or finger-like object moves toward the center electrode, the capacitance ratio or difference measurement is less than a
5 preset threshold. As the finger or finger-like object moves closer to the center electrode, the capacitance ratio or difference measurement eventually exceeds the preset threshold, thereby actuating the switch. The touchless switch may also include a guard electrode surrounding the back and sides of the
10 two sensor electrodes. The guard electrode and the sensor electrodes are maintained at substantially the same voltage potential so that each sensor electrode forms a capacitor only with objects disposed in front of it. Leads extending from the two sensor electrodes to the capacitance measurement apparatus
15 may also be guarded using a twin-axial cable or two coaxial cables, in which the outer conductors of the cables are employed as guard shields and maintained at substantially the same voltage potential as the inside cable conductors connected to the sensor electrodes. The front surface of the touchless switch may take
20 the form of the surface of a container, in which the brim of the container surface defines an imaginary boundary plane that the finger or finger-like object must reach to actuate the switch.

The presently disclosed capacitance measurement apparatus may be employed to detect the proximity of conductive objects
25 larger than a human finger such as the palm of a human hand. The

presently disclosed capacitance measurement apparatus may also be employed to detect the location, position, and/or movement of a conductive object, such as a human appendage within a specified area.

5 Other features, functions, and aspects of the invention will be evident from the Detailed Description of the Invention that follows.

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWINGS

10 The invention will be more fully understood with reference to the following Detailed Description of the Invention in conjunction with the drawings of which:

Fig. 1 illustrates various equivalent capacitances formed between a human body, earth ground, and a sensor electrode coupled
15 to capacitance sensing circuitry;

Fig. 2a is a schematic diagram of first capacitance measurement circuitry according to the present invention;

Fig. 2b is a schematic diagram of circuitry employing the first capacitance measurement circuitry of Fig. 2a, for producing
20 a linear response to changes in each of the ratios of the capacitance of a capacitor to the capacitance of one or more other different capacitors;

Fig. 3a is a schematic diagram of second capacitance measurement circuitry according to the present invention;

25 Fig. 3b is a schematic diagram of circuitry employing the

second capacitance measurement circuitry of Fig. 3a, for producing a linear response to changes in each of the ratios of the capacitance of a capacitor to the capacitance of one or more other different capacitors;

5 Fig. 4a is a schematic diagram of third capacitance measurement circuitry according to the present invention;

 Fig. 4b is a schematic diagram of circuitry employing the third capacitance measurement circuitry of Fig. 4a, for producing a linear response to changes in each of the differences between
10 the capacitance of a capacitor multiplied by a first constant factor and the capacitance of one or more other different capacitors, after each is multiplied by a respective second constant factor;

 Figs. 5a-5d are perspective views of illustrative shapes of
15 a front surface of a touchless switch according to the present invention;

 Figs. 6a-6d are cross-sectional views of illustrative arrangements and relative positions of two sensor electrodes and the front surfaces of the touchless switches of Figs. 5a-5d,
20 respectively;

 Fig. 7 is a cross-sectional view of an illustrative arrangement and relative positions of two sensor electrodes, a front surface, and a guard electrode of a touchless switch according to the present invention;

25 Fig. 8a is a diagram of a touchless switch employing the first

capacitance measurement circuitry of Fig. 2a;

Fig. 8b is a diagram of a set of touchless switches employing the first capacitance measurement circuitry of Fig. 2a;

Fig. 9a is a diagram of a touchless switch employing the
5 second capacitance measurement circuitry of Fig. 3a;

Fig. 9b is a diagram of a set of touchless switches employing the second capacitance measurement circuitry of Fig. 3a;

Fig. 10a is a diagram of a touchless switch employing the third capacitance measurement circuitry of Fig. 4a; and

10 Fig. 10b is a diagram of a set of touchless switches employing the third capacitance measurement circuitry of Fig. 4a.

DETAILED DESCRIPTION OF THE INVENTION

The entire disclosure of U.S. Provisional Patent Application
15 No. 60/690,486 filed June 15, 2005 entitled LINEAR CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH, U.S. Provisional Patent Application No. 60/662,378 filed March 17, 2005 entitled CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH, U.S. Provisional Patent Application No. 60/619,697 filed October 19, 2004 entitled
20 DIFFERENTIAL CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH, and U.S. Provisional Patent Application No. 60/601,610 filed August 16, 2004 entitled DIFFERENTIAL CAPACITANCE MEASUREMENT AND TOUCHLESS SWITCH, is incorporated herein by reference.

A capacitance measurement apparatus and technique are
25 disclosed that can be employed to enhance the sensitivity and

accuracy of many different types of capacitive transducers, proximity sensors, and touchless switches. Fig. 2a depicts a first illustrative embodiment of capacitance measurement circuitry 200a, in accordance with the present invention. In the

5 illustrated embodiment, the capacitance measurement circuitry 200a includes a periodic varying voltage source G1, a first operational amplifier A1, and a second operational amplifier A2 configured as a difference amplifier. Each of two capacitors/capacitive transducers C1 and C2 undergoing

10 comparison, having capacitances of c_1 and c_2 , respectively, has one end connected to circuit ground, and another end connected to one of the differential inputs of operational amplifier A1. Capacitor C1 is connected to the inverting input of operational amplifier A1 at node 101, and capacitor C2 is connected to the

15 non-inverting input of operational amplifier A1 at node 102. The nodes 101 and 102 are driven by an output V_s of the periodic varying voltage source G1, which may be a sinusoidal voltage source, through resistors R1 and R2, respectively. Resistor R1 has a resistance r_1 , and resistor R2 has a resistance r_2 . Output V_1

20 of operational amplifier A1 is fed back to the inverting input of operational amplifier A1 via feedback resistor R3 having a resistance of r_3 . Because operational amplifier A1 has a very high open loop gain, the two inputs of operational amplifier A1 are maintained at substantially the same voltage potential,

25 thereby causing the effective RC time constants for capacitors

C1 and C2 at nodes 101 and 102 to be substantially the same. The magnitude i_3 of current I3 flowing through resistor R3 is substantially equal to the magnitude i_2 of current I2 flowing into capacitor C2 multiplied by the factor $(r_2/r_1 - c_1/c_2)$, i.e.,
5 $i_3 = i_2 * (r_2/r_1 - c_1/c_2)$. Current I3 flowing through resistor R3 will be in-phase or out-of-phase with current I1 flowing through resistor R1 and current I2 flowing through resistor R2, depending on whether the ratio of the capacitances c_1/c_2 is less than or greater than the value r_2/r_1 . More specifically, if c_1/c_2 is less
10 than r_2/r_1 , then $(r_2/r_1 - c_1/c_2)$ is positive and the currents I2 and I3 will be in-phase but if c_1/c_2 is greater than r_2/r_1 , then $(r_2/r_1 - c_1/c_2)$ is negative and the currents I2 and I3 will be out of phase. At steady state, the magnitude i_2 of current I2 flowing into capacitor C2 is a function of time, and therefore the
15 magnitude i_3 of current I3 flowing through resistor R3 at a fixed time of a cycle of current I3 is an accurate measure of the value $(r_2/r_1 - c_1/c_2)$. The voltage across resistor R3 is equal to $i_3 * r_3$, and is equivalent to the difference of the voltage potential between node 101 (or node 102) and output V1 of operational
20 amplifier A1. This voltage can be measured by connecting node 101 (or node 102) to one of the two inputs of difference amplifier A2, and by connecting output V1 of operational amplifier A1 to the other input of difference amplifier A2. It should be noted that the configuration of difference amplifier A2, as shown in
25 Fig. 2a, is described herein for purposes of illustration, and

that other suitable circuit configurations may be employed. For example, alternative configurations of difference amplifier A2 may include more than one operational amplifier. Output Vd of difference amplifier A2 is proportional to the magnitude i3 of
5 current I3 flowing through resistor R3, and will be in-phase with currents I1 and I2 when $c1/c2$ is greater than $r2/r1$. It is noted that the phase of output Vd reverses if the inputs to difference amplifier A2 are interchanged.

Output Vd of difference amplifier A2 is therefore
10 proportional to a signal representing current I2 modulated by the value $(c1/c2 - r2/r1)$. If current I2 is sinusoidal, then the change in the ratio of the capacitances $c1/c2$ can be measured using a synchronous demodulator. Further, there is a linear relationship between output Vd at a fixed time of a cycle of the output (e.g.,
15 at the peak of the cycle), the average absolute value of its positive and/or negative cycles, or the signal extracted from output Vd using synchronous demodulation (if output Vs of the voltage source G1 is sinusoidal), and the ratio of the capacitances $c1/c2$. Accordingly, there is a linear relationship
20 between output Vd at a fixed time of a cycle of the output, the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation, and the capacitance c1 if capacitor C2 has a fixed capacitance, or the reciprocal of the capacitance c2 if capacitor
25 C1 has a fixed capacitance, which is particularly useful when

measuring distances because the capacitance between two conductive objects, e.g., two plates, is inversely proportional to the distance between them.

Fig. 2b depicts circuitry 200b employing capacitance measurement circuitry 200a1-200an to produce a linear response to changes in each of the ratios of the capacitance of a capacitor/capacitive transducer to the capacitance of one or more other different capacitors/capacitive transducers, while keeping all of the capacitors/capacitive transducers at substantially identical voltage potentials at all times. Each of a plurality of capacitors/capacitive transducers C11-C1n having capacitances of c11-c1n, respectively, is compared to the capacitance c2 of capacitor/capacitive transducer C2 coupled between the non-inverting input of an operational amplifier A0 and ground (see Fig. 2b). Each of the capacitance measurement circuitry 200a1-200an operates like the capacitance measurement circuitry 200a (see Fig. 2a), with the exception that the voltage potentials at capacitors C11-C1n are compared to the level of the output of operational amplifier A0, which is configured as a voltage follower to produce substantially the same voltage potential as that across capacitor C2. It is noted that capacitor C2 is driven by output Vs of the voltage source G1 through resistor R2. Thus, there is a linear relationship between each of outputs Vd1-Vdn at a fixed time of a cycle of the output (e.g., at the peak of the cycle), the average absolute value of its positive and/or

negative cycles, or the signal extracted from the output using synchronous demodulation (if output V_s of the voltage source G_1 is sinusoidal), and the ratio of the capacitances c_{11}/c_2 through c_{1n}/c_2 , respectively. As a result, there is a linear relationship
5 between each of outputs V_{d1} - V_{dn} at a fixed time of a cycle of the output, the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation, and the capacitances c_{11} - c_{1n} , respectively, if C_2 has a fixed capacitance.

10 Fig. 3a depicts a second illustrative embodiment of capacitance measurement circuitry 300a, in accordance with the present invention. In the illustrated embodiment, the capacitance measurement circuitry 300a includes periodic varying current sources G_1 and G_2 , a first operational amplifier A_1 , and
15 a second operational amplifier A_2 configured as a difference amplifier. Each of two capacitors/capacitive transducers C_1 and C_2 undergoing comparison, having capacitances c_1 and c_2 , respectively, has one end connected to circuit ground, and another end connected to one of the differential inputs of operational
20 amplifier A_1 . Capacitor C_1 is connected to the inverting input of operational amplifier A_1 at node 101, and capacitor C_2 is connected to the non-inverting input of operational amplifier A_1 at node 102. The node 102 is driven by output current I_2 of the periodic varying current source G_2 . The node 101 is driven by
25 output current I_1 of the periodic varying current source G_1 , in

which I_1 is equal to K times I_2 , K being a constant greater than or equal to zero. Output V_1 of operational amplifier A_1 is fed back to its inverting input via feedback resistor R_1 having a resistance r_1 . Because operational amplifier A_1 has a very high open loop gain, the two inputs of operational amplifier A_1 are maintained at substantially the same voltage potential. Therefore, the magnitude i_3 of current I_3 flowing through resistor R_1 is substantially equal to the magnitude i_2 of current I_2 flowing into capacitor C_2 multiplied by the factor $(K - c_1/c_2)$, i.e., $i_3 = i_2 * (K - c_1/c_2)$. Current I_3 flowing through resistor R_1 will be in-phase or out-of-phase with currents I_1 and I_2 , depending on whether the ratio of the capacitances c_1/c_2 is less than or greater than K . More specifically, if c_1/c_2 is less than K , then $(K - c_1/c_2)$ is positive and currents I_2 and I_3 will be in-phase but if c_1/c_2 is greater than K , then $(K - c_1/c_2)$ is negative and currents I_2 and I_3 will be out of phase. At steady state, the magnitude i_2 of current I_2 flowing into capacitor C_2 is only a function of time, and therefore the magnitude i_3 of current I_3 flowing through resistor R_1 at a fixed time of a cycle of the current I_3 is an accurate measure of the value $(K - c_1/c_2)$. The voltage across resistor R_1 is substantially equal to $i_3 * r_1$, and is equivalent to the difference of the voltage potential between node 101 (or node 102) and output V_1 of operational amplifier A_1 . The voltage across resistor R_1 can be measured by connecting node 101 (or node 102) to one of the two inputs of difference amplifier A_2 , and by

connecting output V1 of operational amplifier A1 to the other input of difference amplifier A2. It should be noted that the configuration of difference amplifier A2, as shown in Fig. 3a, is described herein for purposes of illustration, and that other
5 suitable circuit configurations may be employed. For example, alternative configurations of difference amplifier A2 may include more than one operational amplifier. Output Vd of difference amplifier A2 is proportional to the magnitude i_3 of current I3 flowing through resistor R1, and will be in-phase with currents
10 I1 and I2 when the ratio of the capacitances c_1/c_2 is greater than K. It is noted that the phase of output Vd reverses if the inputs to difference amplifier A2 are interchanged. Output Vd of difference amplifier A2 is therefore proportional to a signal representing current I2 modulated by the value $(c_1/c_2 - K)$. In the
15 event the constant K equals 0, i.e., there is no current source G1, output Vd of difference amplifier A2 is proportional to a signal representing current I2 modulated by the value c_1/c_2 . If current I2 is sinusoidal, then the change in the ratio of the capacitances c_1/c_2 can be measured using a synchronous
20 demodulator for all values of K. Further, there is a linear relationship between output Vd at a fixed time of a cycle of the output (e.g., at the peak of the cycle), the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation (if output I2 of
25 the current source G2 is sinusoidal), and the ratio of the

capacitances c_1/c_2 . Thus, there is a linear relationship between output V_d at a fixed time of a cycle of the output, the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation, and the capacitance c_1 if capacitor C_2 has a fixed capacitance, or the reciprocal of the capacitance c_2 if capacitor C_1 has a fixed capacitance, which is particularly useful when measuring distances because the capacitance between two conductive objects, e.g., two plates, is inversely proportional to the distance between them. It should be noted that if the current sources G_1 and G_2 have significant dc components in their outputs, then bypass resistors may be placed across capacitors C_1 and C_2 .

Fig. 3b depicts circuitry 300b employing capacitance measurement circuitry 300a1-300an to produce a linear response to changes in each of the ratios of the capacitance of a capacitor/capacitive transducer to the capacitance of one or more other different capacitors/capacitive transducers, while keeping all of the capacitors/capacitive transducers at substantially identical voltage potentials at all times. Each of the capacitors/capacitive transducers $C_{11}-C_{1n}$ having capacitances $c_{11}-c_{1n}$, respectively, is compared to the capacitance c_2 of capacitor/capacitive transducer C_2 coupled between the non-inverting input of operational amplifier A_0 and ground (see Fig. 3b). Capacitors $C_{11}-C_{1n}$ are driven by current sources $G_{11}-G_{1n}$, respectively. Each of the respective outputs $I_{11}-I_{1n}$

of the current sources G_{11} - G_{1n} is equal to I_2 times a respective constant K_{11} - K_{1n} , in which each of the constants K_{11} - K_{1n} is greater than or equal to zero. Each of the capacitance measurement circuitry 300a1-300an operates like the capacitance measurement circuitry 300a (see Fig. 3a), with the exception that the voltage potentials at capacitors C_{11} - C_{1n} are compared to the level of the output of operational amplifier A0, which is configured as a voltage follower to produce substantially the same voltage potential as that across capacitor C2, which is driven by output I_2 of current source G2. Therefore, there is a linear relationship between each of outputs V_{d1} - V_{dn} at a fixed time of a cycle of the output (e.g., at the peak of the cycle), the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation (if output I_2 of the current source G2 is sinusoidal), and the ratio of the capacitances c_{11}/c_2 through c_{1n}/c_2 , respectively. As a result, there is a linear relationship between each of outputs V_{d1} - V_{dn} at a fixed time of a cycle of the output, the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation, and the capacitances c_{11} - c_{1n} , respectively, if C2 has a fixed capacitance.

Fig. 4a depicts a third illustrative embodiment of capacitance measurement circuitry 400a, in accordance with the present invention. In the illustrated embodiment, the

capacitance measurement circuitry 400a includes a periodic varying voltage source G1, a first operational amplifier A0, a second operational amplifier A1, and a third operational amplifier A2 configured as a difference amplifier. Each of two
5 capacitors/capacitive transducers C1 and C2 undergoing comparison, having capacitances c1 and c2, respectively, has one end connected to circuit ground, and another end connected to the inverting input of operational amplifier A0 or operational amplifier A1. Capacitor C1 is connected to the inverting input
10 of operational amplifier A1 at node 101, and capacitor C2 is connected to the inverting input of operational amplifier A0 at node 102. A first feedback resistor R1 having resistance r1 is connected between the output of operational amplifier A1 and its inverting input. Similarly, a second feedback resistor R2 having
15 resistance r2 is connected between the output of operational amplifier A0 and its inverting input. The non-inverting inputs of operational amplifiers A1 and A0 are both driven by output Vs of the periodic varying voltage source G1, which may be a sinusoidal voltage source. Due to the high open loop gain of
20 operational amplifiers A1 and A0, capacitors C1 and C2 are maintained at substantially the same voltage potential as output Vs of the voltage source G1 at all times. V0 is the output of the operational amplifier A0 and V1 is the output of the operational amplifier A1. (V1-Vs) is equal to the time derivative of Vs
25 multiplied by the value (r1*c1), i.e., $(V1-Vs) = r1*c1*dVs/dt$,

and is in-phase with current I_1 flowing through resistor R_1 into capacitor C_1 . $(V_0 - V_s)$ is equal to the time derivative of V_s multiplied by the value $(r_2 \cdot c_2)$, i.e., $(V_0 - V_s) = r_2 \cdot c_2 \cdot dV_s/dt$, and is in-phase with current I_2 flowing through resistor R_2 into capacitor C_2 . When output V_1 of operational amplifier A_1 is provided to the non-inverting input of difference amplifier A_2 , while output V_0 of operational amplifier A_0 is provided to the inverting input of difference amplifier A_2 , output V_d of difference amplifier A_2 is proportional to a signal representing the time derivative of V_s modulated by the value $(r_1 \cdot c_1 - r_2 \cdot c_2)$, and is in-phase or out-of-phase with current flowing through resistors R_1 and R_2 , depending on whether $(r_1 \cdot c_1 - r_2 \cdot c_2)$ is greater than or less than zero (the phase of output V_d reverses if the inputs to difference amplifier A_2 are interchanged). It should be noted that the configuration of difference amplifier A_2 , as shown in Fig. 4a, is described herein for purposes of illustration, and that other suitable circuit configurations may be employed. For example, alternative configurations of difference amplifier A_2 may include more than one operational amplifier. If voltage V_s is sinusoidal, then the change in the value of $(r_1 \cdot c_1 - r_2 \cdot c_2)$ can be measured using a synchronous demodulator. At steady state, the time derivative of V_s is only a function of time, and therefore there is a linear relationship between output V_d at a fixed time of a cycle of the output (e.g., at the peak of the cycle), the average absolute value of its positive and/or negative cycles,

or the signal extracted from the output using synchronous demodulation (if output V_s of the voltage source $G1$ is sinusoidal), and the value $(r1 \cdot c1 - r2 \cdot c2)$. As a result, there is a linear relationship between output V_d at a fixed time of a cycle of the output, the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation, and the capacitance $c1$ if capacitor $C2$ has a fixed capacitance, or the capacitance $c2$ if capacitor $C1$ has a fixed capacitance.

Fig. 4b depicts circuitry 400b employing capacitance measurement circuitry 400a1-400an to produce a linear response to changes in each of the differences between the capacitance of a capacitor/capacitive transducer multiplied by a first constant factor, and the capacitance of one or more other different capacitors/capacitive transducers after each is multiplied by a respective second constant factor, while keeping all of the capacitors/capacitive transducers at substantially identical voltage potentials at all times. Each of capacitors/capacitive transducers $C11-C1n$ having capacitances of $c11-c1n$, respectively, is compared to the capacitance $c2$ of capacitor/capacitive transducer $C2$. Each of the capacitance measurement circuitry 400a1-400an in conjunction with operational amplifier $A0$, feedback resistor $R2$, and capacitor $C2$, operates like the capacitance measurement circuitry 400a (see Fig. 4a). There is therefore a linear relationship between each of outputs $Vd1-Vdn$

at a fixed time of a cycle of the output (e.g., at the peak of the cycle), the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation (if output V_s of the voltage source G_1 is sinusoidal), and its respective value ($r_{1n} \cdot c_{1n} - r_2 \cdot c_2$), in which " r_{1n} " is the resistance of the respective feedback resistor R_{1n} associated with the respective operational amplifier A_{1n} . Accordingly, there is a linear relationship between each of outputs V_{d1} - V_{dn} at a fixed time of a cycle of the output, the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation, and the capacitances c_{11} - c_{1n} , respectively, if capacitor C_2 has a fixed capacitance.

By switching capacitors/capacitive transducers in and out for subsequent measurement, each embodiment of the presently disclosed capacitance measurement circuitry can sequentially produce linear responses to changes in the capacitance or the reciprocal of the capacitance of a large number of capacitors/capacitive transducers, or compare the capacitances of a large number of capacitors/capacitive transducers to the capacitances of a large number of sets of capacitors/capacitive transducers. It is noted that any suitable type of capacitive transducer may be employed in each embodiment of the capacitance measurement circuitry described above, including but not limited to any suitable type of capacitive transducer for sensing force,

pressure, strain, acceleration, sound, mechanical displacement, fluid flow, etc. It is further noted that each embodiment of the capacitance measurement circuitry described above may employ any suitable type of double-ended power supply, or single-ended power supply, if an appropriate circuit ground reference can be provided
5 e.g., by a voltage splitter circuit.

Figs. 5a-5d depict illustrative embodiments of a front surface of a touchless switch, in accordance with the present invention. The front surface of the presently disclosed touchless
10 switch can take the form of any suitable type of container such as containers 500a-500c depicted in Figs. 5a-5c, respectively. As shown in Figs. 5a-5c, each of the containers 500a-500c includes a base portion such as base portions 502a-502c of Figs. 5a-5c, respectively, and a brim portion such as brim portions 504a-504c
15 of Figs. 5a-5c, respectively. Alternatively, the front surface of the switch can be flat like a front surface 500d (see Fig. 5d), or any other suitable surface configuration. The presently disclosed touchless switch includes two sensor electrodes, specifically, a center electrode and an outer electrode, which
20 are disposed on or behind the front surface of the switch and are maintained at substantially the same voltage potential. The center electrode is spaced from and at least partly surrounded by the outer electrode. The center and outer electrodes can be of any suitable shape, form, or size, and may not be a solid piece,
25 e.g., an electrode may be a wire mesh. Figs. 6a-6d depict

illustrative arrangements and positions of the center and outer electrodes relative to each other, and relative to the front surfaces 500a-500d of Figs. 5a-5d, respectively. As shown in Figs. 6a-6c, when the front surface is in the form of a container, the center electrode is disposed near the base of the container, and the outer electrode is disposed near the brim of the container.

The presence of the tip of a human finger or a finger-like object near or proximate to the center electrode of the touchless switch can be detected using an embodiment of the presently disclosed capacitance measurement circuitry. When detecting the presence of the human finger or finger-like object, the capacitance measurement circuitry compares the capacitances of the capacitors formed between the two sensor electrodes (*i.e.*, the center electrode and the outer electrode) and the human finger or finger-like object, thereby substantially canceling out extraneous common-mode disturbances, for example, the capacitance between the rest of the human body and the sensor electrodes, the capacitance between the human body and ground, environmental changes, electrical noise, *etc.* Such extraneous common-mode disturbances tend to affect both sensor electrodes equally due to their close proximity to one another. Additionally, the outer electrode can be positioned in the touchless switch so that the tip of the human finger or finger-like object is required to go past a specified boundary before actuating the switch. In one embodiment, this is accomplished by positioning the outer

electrode a specified distance in front of the center electrode, and configuring the spacing between the two sensor electrodes and their relative surface areas so that when an object is near the electrodes, but more than a specified distance away from the center electrode, the ratio of the capacitance between the object and the center electrode to the capacitance between the object and the outer electrode, or the difference between the capacitance between the object and the center electrode multiplied by a first constant and the capacitance between the object and the outer electrode multiplied by a second constant, is less than a preset threshold. It is understood that the capacitance ratio and difference measurements are performed by an embodiment of the above-described capacitance measurement circuitry, and are facilitated by the fixed geometrical shape, size, and relative position of the two sensor electrodes. In one embodiment, the touchless switch is actuated when the measured capacitance ratio or difference exceeds the preset threshold.

Accordingly, the touchless switch is not actuated by a human finger or finger-like object until the finger passes through the specified boundary. In the event the front surface of the touchless switch has the form of the surface of a container (see, e.g., Figs. 5a-5c), the specified boundary coincides with an imaginary plane defined by the brim of the container. As the tip of a human finger or finger-like object moves toward the center electrode and breaks the plane of the specified boundary, the

capacitance associated with the center electrode increases more rapidly than the capacitance associated with the outer electrode. The touchless switch is actuated when the ratio of the capacitances associated with the center electrode and the outer electrode, or the difference between the capacitance associated with the center electrode multiplied by a first constant and the capacitance associated with the outer electrode multiplied by a second constant, exceeds the preset threshold.

Fig. 7 depicts an illustrative embodiment of a touchless switch 700, in accordance with the present invention. In the illustrated embodiment, the touchless switch 700 includes a front surface 702 in the form of a container, a center electrode 704, an outer electrode 706, and a guard electrode 708 surrounding the back and the sides of the two sensor electrodes 704, 706. All of the electrodes 704, 706, 708 are maintained at substantially the same voltage potential. As a result, the two sensor electrodes 704, 706 are operative to form electric fields only between the sensor electrodes and objects disposed in front of the switch, i.e., above the switch 700, as depicted in Fig. 7. Leads extending from the two sensor electrodes to the capacitance measurement circuitry may also be guarded using a twin-axial cable or two coaxial cables, in which the outer conductors of the cables are employed as guard shields and maintained at substantially the same voltage potential as the inside cable conductors connected to the sensor electrodes.

Fig. 8a depicts a first illustrative circuit implementation 800a of a touchless switch, in accordance with the present invention. As shown in Fig. 8a, the circuit implementation 800a comprises a center electrode E1, an outer electrode E2, a guard electrode E3, a startup delay section 203, a switching decision section 205, a switching output section 207, and capacitance measurement circuitry 802a, which includes a periodic varying voltage source G1, operational amplifiers A0 and A1, resistors R1-R3, and an operational amplifier A2 configured as a difference amplifier. The center electrode E1 is connected to the inverting input of operational amplifier A1 at node 201, and the outer electrode E2 is connected at node 202 to the non-inverting input of operational amplifier A0, which is configured as a voltage follower to provide the voltage potential of outer electrode E2 to the non-inverting input of operational amplifier A1. The nodes 201 and 202 are both driven by an output Vs of the periodic varying voltage source G1. Electrodes E1 and E2 correspond to capacitors C1 and C2 of Fig. 2a, respectively. Guard electrode E3 is connected to the output of operational amplifier A0, and therefore the voltage potential of guard electrode E3 is substantially equal to the voltage potentials of sensor electrodes E1 and E2. Guard electrode E3 may be configured to surround the back and the sides of sensor electrodes E1 and E2 so that capacitances can only be formed between conducting objects disposed in front of the touchless switch and sensor electrodes E1 and E2. It is noted

that operational amplifiers A1 and A2 of Fig. 8a are like operational amplifiers A1 and A2 of Fig. 2a, respectively, resistor R2 of Fig. 8a is like resistor R2 of Fig. 2a, resistors R1 and R3 of Fig. 8a are like resistors R1 and R3 of Fig. 2a, respectively, and the periodic varying voltage source G1 of Fig. 8a is like the periodic varying voltage source G1 of Fig. 2a. Thus, there is a linear relationship between output Vd of difference amplifier A2 at a fixed time of a cycle of the output (e.g., at the peak of the cycle), or the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation (if output Vs of voltage source G1 is sinusoidal), and the ratio of the capacitance associated with center electrode E1 to the capacitance associated with outer electrode E2. Difference amplifier A2 provides output Vd to the switching decision section 205, which determines whether to actuate the touchless switch based on signal Vd. For example, the switching decision section 205 can base its decision on the phase, the amplitude, an average, and/or any other suitable property of signal Vd. Alternatively, the switching decision section 205 can require a specified number of consecutive detections of the required phase and/or amplitude of signal Vd, or the satisfaction of certain criteria, before deciding to actuate the touchless switch. If voltage Vs is sinusoidal, then a synchronous demodulator can be included in the switching decision section 205 so that the change in the ratio of the

capacitance associated with center electrode E1 to the capacitance associated with outer electrode E2 can be obtained with a high degree of accuracy, even at a high noise level. It should be noted that the switching decision section 205 may require one or more signals in addition to signal Vd to determine whether or not to actuate the switch. For example, the switching decision section 205 may require a reference signal to determine the phase of signal Vd. The switching decision section 205 provides a logic signal 206 representing its decision to the switching output section 207, which implements the required switching action. It is noted that the switching output section 207 may be implemented using any suitable number of logical outputs (normally high or low), solid state switch outputs, and/or dry contact outputs (normally open or closed) in any suitable switching mode, including but not limited to pulse mode, momentarily mode, toggle mode, etc. The switching output section 207 can also be configured to produce audio and/or visual outputs to indicate the status of the touchless switch. Because the capacitance measurement circuitry 802a takes several cycles of output Vs of the voltage source G1 to stabilize, the startup delay section 203 outputs a startup signal 204 to the switching decision section 205 during the startup period to prevent the switching decision section 205 from inadvertently actuating the switch. When sensor electrodes E1 and E2 are disposed at a distance away from the inputs of operational amplifiers A0 and A1, the leads

from center electrode E1 and outer electrode E2 may be guarded using a twin-axial cable or two coaxial cables of equal length, using the outer conductors as the guard shields connected to guard electrode E3 and maintained at substantially the same voltage potential as the inside conductors connected to respective sensor electrodes E1 and E2, so that no stray capacitance is introduced and any other unwanted effects introduced by the leads are substantially cancelled out.

Fig. 8b depicts a first illustrative circuit implementation 800b of a set of touchless switches, including a periodic varying voltage source G1, an operational amplifier A0, capacitance measurement circuitry 802a1-802an, the startup delay section 203, the switching decision section 205, and the switching output section 207. It is noted that each of the capacitance measurement circuitry 802a1-802an in conjunction with operational amplifier A0 is like the capacitance measurement circuitry 802a (see Fig. 8a), and corresponds to a respective touchless switch in the set of touchless switches. Specifically, electrode E2 coupled to the non-inverting input of operational amplifier A0 corresponds to a common outer electrode of the set of touchless switches, and electrode E3 coupled to the output of operational amplifier A0 corresponds to a common guard electrode of the set of touchless switches. Each of electrodes E11-E1n corresponds to a center electrode of a respective touchless switch. It is noted that operational amplifier A0 of Fig. 8b is like operational amplifier

A0 of Fig. 2b, operational amplifiers A11-A1n of Fig. 8b are like operational amplifiers A11-A1n of Fig. 2b, respectively, difference amplifiers A21-A2n of Fig. 8b are like difference amplifiers A21-A2n of Fig. 2b, respectively, resistor R2 of Fig. 8b is like resistor R2 of Fig. 2b, resistors R11-R1n of Fig. 8b are like resistors R11-R1n of Fig. 2b, respectively, resistors R31-R3n of Fig. 8b are like resistors R31-R3n of Fig. 2b, respectively, and the periodic varying voltage source G1 of Fig. 8b is like the periodic varying voltage source G1 of Fig. 2b.

Difference amplifiers A21-A2n provide output signals Vd1-Vdn, respectively, to the switching decision section 205, which determines when to actuate each switch based on the respective signals Vd1-Vdn. The switching decision section 205 provides logic signals 206 representing its respective decisions to the switching output section 207, which implements the required switching action for each switch. It is noted that the switching output section 207 may be implemented using any suitable number of logical outputs (normally high or low), solid state switch outputs, and/or dry contact outputs (normally open or closed) in any suitable switching mode, including but not limited to pulse mode, momentarily mode, toggle mode, etc., for each switch. The switching output section 207 can also be configured to produce audio and/or visual outputs to indicate the status of each switch. The startup delay section 203 of Fig. 8b is like the corresponding section 203 described above with reference to Fig. 8a, and each

switch of circuit implementation 800b (see Fig. 8b) basically operates like the switch of circuit implementation 800a (see Fig. 8a).

Fig. 9a depicts a second illustrative circuit implementation 5 900a of a touchless switch, in accordance with the present invention. As shown in Fig. 9a, the circuit implementation 900a comprises a center electrode E1, an outer electrode E2, a guard electrode E3, a startup delay section 203, a switching decision section 205, a switching output section 207, and capacitance 10 measurement circuitry 902a, which includes periodic varying current sources G1 and G2, operational amplifiers A0 and A1, resistor R1, and an operational amplifier A2 configured as a difference amplifier. The center electrode E1 is connected to the inverting input of operational amplifier A1 at node 201, and 15 outer electrode E2 is connected at node 202 to the non-inverting input of operational amplifier A0, which is configured as a voltage follower to provide the voltage potential of outer electrode E2 to the non-inverting input of operational amplifier A1. The node 201 is driven by an output current I1 of the periodic 20 varying current source G1 and node 202 is driven by an output current I2 of the periodic varying current source G2. Electrodes E1 and E2 correspond to capacitors C1 and C2 of Fig. 3a, respectively. Guard electrode E3 is connected to the output of operational amplifier A0, and therefore the voltage potential of 25 guard electrode E3 is substantially the same as the voltage

potentials of sensor electrodes E1 and E2. Guard electrode E3 may be configured to surround the back and the sides of sensor electrodes E1 and E2 so that capacitances can be formed only between conducting objects disposed in front of the touchless switch and the sensor electrodes E1 and E2. It is noted that operational amplifiers A1 and A2 of Fig. 9a are like operational amplifiers A1 and A2 of Fig. 3a, respectively, resistor R1 of Fig. 9a is like resistor R1 of Fig. 3a, and the periodic varying current sources G1 and G2 of Fig. 9a are like the periodic varying current sources G1 and G2 of Fig. 3a, respectively. Thus, there is a linear relationship between output Vd of difference amplifier A2 at a fixed time of a cycle of the output (e.g., at the peak of the output), or the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation (if output I2 of current source G2 is sinusoidal), and the ratio of the capacitance associated with center electrode E1 to the capacitance associated with outer electrode E2. Difference amplifier A2 provides output signal Vd to the switching decision section 205, which determines whether to actuate the touchless switch based on signal Vd. For example, the switching decision section 205 can base its decision on the phase, the amplitude, an average, and/or any other suitable property of signal Vd. Alternatively, the switching decision section 205 can require a specified number of consecutive detections of the required phase and/or amplitude of signal Vd,

or the satisfaction of certain criteria, before deciding to actuate the switch. If current I2 is sinusoidal, then a synchronous demodulator can be included in the switching decision section 205 so that the change in the ratio of the capacitance associated with center electrode E1 to the capacitance associated with outer electrode E2 can be obtained with a high degree of accuracy, even at a high noise level. It should be noted that the switching decision section 205 may require one or more signals in addition to signal Vd to determine whether or not to actuate the switch. For example, the switching decision section 205 may require a reference signal to determine the phase of signal Vd. The switching decision section 205 provides a logic signal 206 representing its decision to the switching output section 207, which implements the required switching action. It is noted that the switching output section 207 may be implemented using any suitable number of logical outputs (normally high or low), solid state switch outputs, and/or dry contact outputs (normally open or closed) in any switching mode, including but not limited to pulse mode, momentarily mode, toggle mode, etc. The switching output section 207 can also be configured to produce audio and/or visual outputs to indicate the status of the switch. Because the capacitance measuring circuitry 902a takes several cycles of the output I2 of the periodic varying current source G2 to stabilize, a startup delay section 203 provides a startup signal 204 to the switching decision section 205 during the startup period to

prevent it from inadvertently actuating the switch. When sensor electrodes E1 and E2 are disposed at a distance away from the inputs of operational amplifiers A0 and A1, the leads from sensor electrodes E1 and E2 may be guarded using a twin-axial cable or two coaxial cables of equal length, with the outer conductors employed as guard shields connected to guard electrode E3 and maintained at substantially the same voltage potential as the inside conductors connected to respective sensor electrodes E1 and E2, so that no stray capacitance is introduced and any other unwanted effects introduced by the leads are substantially cancelled out.

Fig. 9b depicts a second illustrative circuit implementation 900b of a set of touchless switches, including periodic varying current sources G11-G1n, a periodic varying current source G2, an operational amplifier A0, capacitance measurement circuitry 902a1-902an, the startup delay section 203, the switching decision section 205, and the switching output section 207. It is noted that each of the capacitance measurement circuitry 902a1-902an, in conjunction with operational amplifier A0, is like the capacitance measurement circuitry 902a (see Fig. 9a), and corresponds to a respective touchless switch in the set of touchless switches. Specifically, electrode E2 coupled to the non-inverting input of operational amplifier A0 corresponds to a common outer electrode of the set of touchless switches, and electrode E3 coupled to the output of operational amplifier A0

corresponds to a common guard electrode of the set of touchless switches. Each of electrodes E11-E1n corresponds to a center electrode of a respective touchless switch. Further, operational amplifier A0 of Fig. 9b is like operational amplifier A0 of Fig. 3b, operational amplifiers A11-A1n of Fig. 9b are like operational amplifiers A11-A1n of Fig. 3b, respectively, difference amplifiers A21-A2n of Fig. 9b are like difference amplifiers A21-A2n of Fig. 3b, respectively, resistors R11-R1n of Fig. 9b are like resistors R11-R1n of Fig. 3b, respectively, the periodic
10 varying current source G2 of Fig. 9b is like the periodic varying current source G2 of Fig. 3b, and the periodic varying current sources G11-G1n of Fig. 9b are like the periodic varying current sources G11-G1n of Fig. 3b. Difference amplifiers A21-A2n provide output signals Vd1-Vdn, respectively, to the switching decision
15 section 205, which determines when to actuate each switch based on the respective signals Vd1-Vdn. The switching decision section 205 provides logic signals 206 representing its respective decisions to the switching output section 207, which implements the required switching action for each switch. It is noted that
20 the switching output section 207 may be implemented using any suitable number of logical outputs (normally high or low), solid state switch outputs, and/or dry contact outputs (normally open or closed) in any suitable switching mode, including but not limited to pulse mode, momentarily mode, toggle mode, etc., for
25 each switch. The switching output section 207 can also be

configured to produce audio and/or visual outputs to indicate the status of each switch. The startup delay section 203 of Fig. 9b is like the startup delay section 203 of Fig. 9a, and each switch of circuit implementation 900b (see Fig. 9b) basically operates
5 like the switch of circuit implementation 900a (see Fig. 9a).

Fig. 10a depicts a third illustrative circuit implementation 1000a of a touchless switch, in accordance with the present invention. As shown in Fig. 10a, the circuit implementation 1000a comprises a center electrode E1, an outer electrode E2, a guard
10 electrode E3, a startup delay section 203, a switching decision section 205, a switching output section 207, and capacitance measurement circuitry 1002a, which includes a periodic varying voltage source G1, operational amplifiers A0 and A1, resistors R1 and R2, and an operational amplifier A2 configured as a
15 difference amplifier. The non-inverting inputs of operational amplifier A0 and A1 are both driven by an output Vs of the periodic varying voltage source G1. The center electrode E1 is connected to the inverting input of operational amplifier A1 at node 201, and outer electrode E2 is connected at node 202 to the inverting
20 input of operational amplifier A0. It is noted that sensor electrodes E1 and E2 correspond to capacitors C1 and C2 of Fig. 4a, respectively. Guard electrode E3 is connected to the output of the periodic varying voltage source G1, and is therefore substantially the same as the voltage potential of the two sensor
25 electrodes E1 and E2. Guard electrode E3 may be configured to

surround the back and the sides of the sensor electrodes E1 and E2 so that capacitances can be formed only between conducting objects disposed in front of the touchless switch and the sensor electrodes E1 and E2. It is noted that operational amplifiers

5 A0 and A1 of Fig. 10a are like operational amplifiers A0 and A1 of Fig. 4a, respectively, resistor R1 of Fig. 10a is like resistor R1 of Fig. 4a, resistor R2 of Fig. 10a is like resistor R2 of Fig. 4a, and the periodic varying voltage source G1 of Fig. 10a is like the periodic varying voltage source G1 of Fig. 4a. Thus, there

10 is a linear relationship between output Vd of difference amplifier A2 at a fixed time of a cycle of the output (e.g., at the peak of the cycle), or the average absolute value of its positive and/or negative cycles, or the signal extracted from the output using synchronous demodulation (if output Vs of the voltage source G1

15 is sinusoidal) and the value $(r1 \cdot c1 - r2 \cdot c2)$, in which r1 and r2 are the respective resistances of resistors R1 and R2 and c1 and c2 are the respective capacitances associated with sensor electrodes E1 and E2. The difference amplifier A2 provides output signal Vd to the switching decision section 205, which determines

20 whether to actuate the switch based on signal Vd. For example, the switching decision section 205 can base its decision on the phase, the amplitude, an average, and/or any other suitable property of signal Vd. Alternatively, the switching decision section 205 can require a specified number of consecutive

25 detections of the required phase and/or amplitude of signal Vd,

or the satisfaction of certain criteria, before deciding to actuate the switch. If voltage V_s is sinusoidal, then a synchronous demodulator can be included in the switching decision section 205 so that the change in the value of $(r_1 \cdot c_1 - r_2 \cdot c_2)$ can be obtained with a high degree of accuracy, even at a high noise level. It should be noted that the switching decision section 205 may require one or more signals in addition to signal V_d to determine whether or not to actuate the switch. For example, the switching decision section 205 may require a reference signal to determine the phase of output signal V_d . The switching decision section 205 provides a logic signal 206 representing its decision to the switching output section 207, which implements the required switching action. It is noted that the switching output section 207 may be implemented using any suitable number of logical outputs (normally high or low), solid state switch outputs, and/or dry contact outputs (normally open or closed) in any switching mode, including but not limited to pulse mode, momentarily mode, toggle mode, etc. The switching output section 207 can also be configured to produce audio and/or visual outputs to indicate the status of the switch. Because the capacitance measuring circuit 1002a takes several cycles of output V_s of the periodic varying voltage source G_1 to stabilize, a startup delay section 203 provides a startup signal 204 to the switching decision section 205 during the startup period to prevent it from inadvertently actuating the switch. When sensor electrodes E_1 and E_2 are

disposed at a distance away from the inputs of operational amplifiers A0 and A1, the leads from sensor electrodes E1 and E2 may be guarded using a twin-axial cable or two coaxial cables of equal length, with the outer conductors employed as guard shields
5 connected to the guard electrode E3 and maintained at substantially the same voltage potential as the inside conductors connected to respective sensor electrodes E1 and E2, so that no stray capacitance is introduced and any other unwanted effects introduced by the leads are substantially cancelled out.

10 Fig. 10b depicts a third illustrative circuit implementation 1000b of a set of touchless switches, including a periodic varying voltage source G1, an operational amplifier A0, capacitance measurement circuitry 1002a1-1002an, the startup delay section 203, the switching decision section 205, and the switching output
15 section 207. It is noted that each of the capacitance measurement circuitry 1002a1-1002an, in conjunction with operational amplifier A0, is like the capacitance measurement circuitry 1002a (see Fig. 10a), and corresponds to a respective touchless switch in the set of touchless switches. Specifically, electrode E2
20 coupled to the inverting input of operational amplifier A0 corresponds to a common outer electrode of the set of touchless switches, and electrode E3 coupled to the output Vs of voltage source G1 corresponds to a common guard electrode of the set of touchless switches. Each of electrodes E11-E1n corresponds to
25 a center electrode of a respective touchless switch. Further,

operational amplifier A0 of Fig. 10b is like operational amplifier A0 of Fig. 4b, operational amplifiers A11-A1n of Fig. 10b are like operational amplifiers A11-A1n of Fig. 4b, respectively, difference amplifiers A21-A2n of Fig. 10b are like difference amplifiers A21-A2n of Fig. 4b, respectively, resistor R2 of Fig. 10b is like resistor R2 of Fig. 4b, resistors R11-R1n of Fig. 10b are like resistors R11-R1n of Fig. 4b, respectively, and the periodic varying voltage source G1 of Fig. 10b is like the periodic varying voltage source G1 of Fig. 4b. The difference amplifiers A21-A2n provide output signals Vd1-Vdn, respectively, to the switching decision section 205, which determines when to actuate each switch based on the respective signals Vd1-Vdn. The switching decision section 205 provides logic signals 206 representing its respective decisions to the switching output section 207, which implements the required switching action for each switch. It is noted that the switching output section 207 may be implemented using any suitable number of logical outputs (normally high or low), solid state switch outputs, and/or dry contact outputs (normally open or closed) in any suitable switching mode, including but not limited to pulse mode, momentarily mode, toggle mode, etc., for each switch. The switching output section 207 can also be configured to produce audio and/or visual outputs to indicate the status of each switch. The startup delay section 203 of Fig. 10b is like the startup delay section 203 of Fig. 10a, and each switch of circuit implementation

1000b (see Fig. 10b) operates like the switch of circuit implementation 1000a (see Fig. 10a).

Having described the above illustrative embodiments, other alternative embodiments or variations may be made. For example, each of the presently disclosed circuit implementations of touchless switches can be scaled up to detect the proximity of a larger similar shaped conducting object, e.g., the palm of a human hand. It can also be adapted to detect the position or movement of a conducting object, e.g., a human appendage, by measuring the respective capacitance between the conducting object and a respective one of an array of sensor electrodes, using one of the capacitance measuring techniques described above and analyzing the results using electronic circuitry or a computer. It is noted that the outer electrode may be positioned in front of or behind or at any suitable position relative to the center electrode or set of center electrodes, depending upon the applications. In addition, while the present invention may be embodied using hardware components, it is appreciated that the functions necessary to implement the invention may alternatively be embodied in whole or in part using hardware or software or some combination thereof using micro-controllers, microprocessors, digital signal processors, programmable logic arrays, or any other suitable hardware and/or software.

It will be appreciated by those of ordinary skill in the art that further modifications to and variations of the

above-described linear capacitance measurement and touchless switch may be made without departing from the inventive concepts disclosed herein. Accordingly, the invention should not be viewed as limited except as by the scope and spirit of the appended claims.

CLAIMS

What is claimed is:

1. An apparatus for measuring relative changes in capacitances
5 of at least one first capacitor and at least one second capacitor,
comprising:

at least one operational amplifier operative to employ
negative feedback to maintain the at least one first capacitor
and the at least one second capacitor at substantially equal
10 voltage potentials, each of the at least one first capacitor and
the at least one second capacitor having one end connected to a
common ground; and

at least one difference amplifier operative to provide an
electrical signal, wherein a linear relationship exists between
15 the electrical signal and a specified arithmetic function of the
capacitances of the at least one first capacitor and the at least
one second capacitor,

wherein at least one operational amplifier is operative to
provide an output to an input of at least one difference amplifier.
20

2. The apparatus of claim 1 further comprising:

a periodic varying voltage source operative to drive a first
resistor connected to the first capacitor at a first node, and
to drive a second resistor connected to the second capacitor at
25 a second node,

wherein the at least one operational amplifier includes a first operational amplifier,

wherein a voltage potential at the first node is provided to an inverting input of the first operational amplifier, and a
5 voltage potential at the second node is provided to a non-inverting input of the first operational amplifier,

wherein a third resistor is connected between an output of the first operational amplifier and the first node, whereby feedback current flowing through the third resistor maintains the
10 voltage potential at the first node substantially equal to the voltage potential at the second node,

wherein the voltage potential at one of the first and second nodes is provided to a first differential input of the difference amplifier, and the output of the first operational amplifier is
15 provided to a second differential input of the difference amplifier, and

wherein at steady state, an output of the difference amplifier is proportional to a signal representing a current which is a periodic function of time flowing through the second resistor
20 being modulated by a value $(c1/c2 - r2/r1)$, wherein $c1$ is the capacitance of the first capacitor, $c2$ is the capacitance of the second capacitor, $r1$ is the resistance of the first resistor, and $r2$ is the resistance of the second resistor.

25 3. The apparatus of claim 2 wherein at steady state, a linear

relationship exists between the output of the difference amplifier at a fixed time of a cycle of the output and the value $(c1/c2-r2/r1)$.

5 4. The apparatus of claim 2 wherein at steady state, a linear relationship exists between an average absolute value of a positive and/or negative cycle of the output of the difference amplifier and the value $(c1/c2-r2/r1)$.

10 5. The apparatus of claim 2 further including a synchronous demodulator, wherein the periodic varying voltage source is a sinusoidal voltage source, and wherein the synchronous demodulator is operative to extract a signal proportional to the value $(c1/c2-r2/r1)$ from the output of the difference amplifier
15 by synchronous demodulation.

6. The apparatus of claim 1 further comprising:

a first periodic varying current source operative to provide a first output current to the first capacitor, the first capacitor
20 being connected to the first periodic varying current source at a first node, and

a second periodic varying current source operative to provide a second output current to the second capacitor, the second capacitor being connected to the second periodic varying
25 current source at a second node,

wherein a magnitude of the first output current is equal to a constant K times a magnitude of the second output current,

wherein the at least one operational amplifier includes a first operational amplifier,

5 wherein a voltage potential at the first node is provided to an inverting input of the first operational amplifier, and a voltage potential at the second node is provided to a non-inverting input of the first operational amplifier,

10 wherein a first resistor is connected between an output of the first operational amplifier and the first node, whereby feedback current flowing through the first resistor maintains the voltage potential at the first node substantially equal to the voltage potential at the second node,

15 wherein the voltage potential at one of the first and second nodes is provided to a first differential input of the difference amplifier, and the output of the first operational amplifier is provided to a second differential input of the difference amplifier, and

20 wherein at steady state, the output of the difference amplifier is proportional to a signal representing a current which is a periodic function of time flowing into the second capacitor being modulated by a value $(c1/c2-K)$, wherein $c1$ is the capacitance of the first capacitor, and $c2$ is the capacitance of the second capacitor.

25

7. The apparatus of claim 6 wherein at steady state, a linear relationship exists between the output of the difference amplifier at a fixed time of a cycle of the output and the value $(c1/c2-K)$.

5

8. The apparatus of claim 6 wherein at steady state, a linear relationship exists between an average absolute value of a positive and/or negative cycle of the output of the difference amplifier and the value $(c1/c2-K)$.

10

9. The apparatus of claim 6 further including a synchronous demodulator, wherein the first and second periodic varying current sources are respective sinusoidal current sources, and wherein the synchronous demodulator is operative to extract a signal proportional to the value of $(c1/c2-K)$ from the output of the difference amplifier by synchronous demodulation.

15

10. The apparatus of claim 1

wherein the at least one operational amplifier includes first and second operational amplifiers, and further comprising:

20

a periodic varying voltage source operative to provide an output voltage to a non-inverting input of the first operational amplifier and a non-inverting input of the second operational amplifier,

25

wherein a first resistor is connected between an output of the first operational amplifier and the first capacitor at a first node,

wherein a voltage potential at the first node is provided to an inverting input of the first operational amplifier, whereby feedback current flowing through the first resistor maintains the voltage potential at the first node substantially equal to a voltage potential of the output voltage of the periodic varying voltage source,

wherein a second resistor is connected between an output of the second operational amplifier and the second capacitor at a second node,

wherein a voltage potential at the second node is provided to an inverting input of the second operational amplifier, whereby feedback current flowing through the second resistor maintains the voltage potential at the second node substantially equal to the voltage potential of the output voltage of the periodic varying voltage source,

wherein the output of the first operational amplifier is provided to a first differential input of the difference amplifier, and the output of the second operational amplifier is provided to a second differential input of the difference amplifier, and

wherein at steady state, the output of the difference amplifier is proportional to a signal representing a time derivative of the output voltage of the periodic varying voltage

source which is a periodic function of time being modulated by a value $(r1 \cdot c1 - r2 \cdot c2)$, wherein $c1$ is the capacitance of the first capacitor, $c2$ is the capacitance of the second capacitor, $r1$ is the resistance of the first resistor, and $r2$ is the resistance of the second resistor.

11. The apparatus of claim 10 wherein at steady state, a linear relationship exists between the output of the difference amplifier at a fixed time of a cycle of the output and the value of $(r1 \cdot c1 - r2 \cdot c2)$.

12. The apparatus of claim 10 wherein at steady state, a linear relationship exists between an average absolute value of a positive and/or negative cycle of the output of the difference amplifier and the value of $(r1 \cdot c1 - r2 \cdot c2)$.

13. The apparatus of claim 10 further including a synchronous demodulator, wherein the periodic varying voltage source is a sinusoidal voltage source, and wherein the synchronous demodulator is operative to extract a signal proportional to the value $(r1 \cdot c1 - r2 \cdot c2)$ from the output of the difference amplifier by synchronous demodulation.

14. The apparatus of claim 1 wherein each of the first capacitors is a capacitive transducer.

15. The apparatus of claim 1 wherein each of the second capacitors is a capacitive transducer.

5 16. The apparatus of claim 1 wherein each of the first capacitors is a capacitive sensor.

17. The apparatus of claim 1 wherein each of the second capacitors is a capacitive sensor.

10

18. The apparatus of claim 1 further including first and second sensor electrodes, wherein the first capacitor is formed between a conducting object and the first sensor electrode, wherein the second capacitor is formed between the conducting object and the
15 second sensor electrode, and wherein an output of the difference amplifier is indicative of a proximity or movement of the conducting object relative to the sensor electrodes.

19. The apparatus of claim 1 further including first and second
20 sensor electrodes, wherein the first capacitor is formed between a human appendage and the first sensor electrode, wherein the second capacitor is formed between the human appendage and the second sensor electrode, and

further comprising:

25 a switching decision section operative to generate a first

control signal based on the output of the difference amplifier,
and

a switching output section operative to indicate a status
condition in response to the first control signal.

5

20. The apparatus of claim 19 further comprising

a startup delay section operative to delay the generation
of the first control signal by the switching decision section
until the apparatus is in a steady state condition.

10

21. The apparatus of claim 19 further comprising a guard
electrode,

wherein the guard electrode is electrically isolated from
the first and second sensor electrodes, wherein the guard
15 electrode is maintained at substantially the same voltage
potential as the first and second sensor electrodes, and wherein
the guard electrode is configured to surround a back and sides
of the first and second sensor electrodes.

20 22. The apparatus of claim 19 wherein the first sensor electrode
is a center electrode and the second sensor electrode is an outer
electrode, and wherein the center electrode is spaced from and
at least partly surrounded by the outer electrode.

25 23. The apparatus of claim 22 further including a surface in a

form of a container having a base and a brim, wherein the center and outer electrodes are disposed on one of the sides of the surface, and wherein the center electrode is disposed near the base of the container and the outer electrode is disposed near
5 the brim of the container.

24. The apparatus of claim 1 wherein the at least one second capacitor is a selected one of a plurality of second capacitors.

10 25. The apparatus of claim 1 wherein the at least one first capacitor is one of a set of first capacitors,

wherein the at least one operational amplifier is one of a plurality of first operational amplifiers, each one of the plurality of first operational amplifiers being associated with
15 a respective one of the set of first capacitors,

wherein the at least one difference amplifier is one of a plurality of difference amplifiers, each one of the plurality of difference amplifiers being associated with a respective one of the set of first capacitors,

20 wherein each one of the plurality of first operational amplifiers associated with a respective one of the set of first capacitors uses negative feedback to maintain the respective one of the set of first capacitors and the second capacitor at substantially equal voltage potentials,

25 wherein each one of the plurality of first operational

amplifiers is operative to provide an output to an input of a respective one of the plurality of difference amplifiers, and

wherein each one of the plurality of difference amplifiers is operative to provide a respective one of a plurality of
5 electrical signals, wherein a linear relationship exists between each respective electrical signal and a specified arithmetic function of the capacitances of the respective one of the set of first capacitors and the second capacitor.

10 26. The apparatus of claim 25 wherein the set of first capacitors is a selected one of a plurality of sets of first capacitors.

27. The apparatus of claim 25 further comprising:

a periodic varying voltage source operative to drive a
15 plurality of first resistors, each first resistor being connected to a respective one of the set of first capacitors at a respective one of a plurality of first nodes, and to drive a second resistor connected to the second capacitor at a second node,

wherein a voltage potential at a respective one of the
20 plurality of first nodes is provided to an inverting input of a respective one of the plurality of first operational amplifiers, and a voltage potential at the second node is provided to a non-inverting input of the respective one of the plurality of first operational amplifiers,

25 wherein a respective one of a plurality of third resistors

is connected between an output of a respective one of the plurality of first operational amplifiers and a respective one of the plurality of first nodes, whereby feedback current flowing through the respective one of the plurality of third resistors maintains the voltage potential at the respective one of the plurality of first nodes substantially equal to the voltage potential at the second node,

wherein the voltage potential at the second node is provided to a first differential input of a respective one of the plurality of difference amplifiers, and the output of a respective one of the plurality of operational amplifiers is provided to a second differential input of the respective one of the plurality of difference amplifiers, and

wherein at steady state, the output of a respective one of the plurality of difference amplifiers is proportional to a signal representing a current which is a periodic function of time flowing through the second resistor being modulated by a respective value $(c1/c2 - r2/r1)$, wherein $c1$ is the capacitance of a respective one of the set of first capacitors, $c2$ is the capacitance of the second capacitor, $r1$ is the resistance of a respective one of the plurality of first resistors, and $r2$ is the resistance of the second resistor.

28. The apparatus of claim 27 wherein at steady state, a linear relationship exists between the output of a respective one of the

plurality of difference amplifiers at a fixed time of a cycle of the output and the respective value $(c1/c2 - r2/r1)$.

29. The apparatus of claim 27 wherein at steady state, a linear relationship exists between an average absolute value of a positive and/or negative cycle of the output of a respective one of the plurality of difference amplifiers and the respective value $(c1/c2-r2/r1)$.

30. The apparatus of claim 27 wherein the periodic varying voltage source is a sinusoidal voltage source, and further including a synchronous demodulator operative to extract a respective one of signals proportional to the respective value $(c1/c2-r2/r1)$ from the output of a respective one of the plurality of difference amplifiers by synchronous demodulation.

31. The apparatus of claim 25 further comprising:

a plurality of first periodic varying current sources, each of the first periodic varying current sources being operative to provide a first output current to a respective one of the set of first capacitors, each of the set of first capacitors being connected to a respective one of the first periodic varying current sources at a respective one of a plurality of first nodes, and

a second periodic varying current source operative to

provide a second output current to the second capacitor, the second capacitor being connected to the second periodic varying current source at a second node,

wherein a magnitude of a respective one of the first output
5 currents is equal to a respective constant K times a magnitude of the second output current,

wherein a voltage potential at a respective one of the plurality of first nodes is provided to an inverting input of a respective one of the plurality of first operational amplifiers,
10 and a voltage potential at the second node is provided to a non-inverting input of the respective one of the plurality of first operational amplifiers,

wherein a respective one of a plurality of first resistors is connected between the output of a respective one of the plurality of first operational amplifiers and a respective one
15 of the plurality of first nodes, whereby feedback current flowing through the respective one of the plurality of first resistors maintains the voltage potential at the respective one of the plurality of first nodes substantially equal to the voltage
20 potential at the second node,

wherein the voltage potential at the second node is provided to a first differential input of a respective one of the plurality of difference amplifiers, and the output of a respective one of the plurality of first operational amplifiers is provided to a
25 second differential input of the respective one of the plurality

of difference amplifiers, and

wherein at steady state, the output of a respective one of the plurality of difference amplifiers is proportional to a signal representing a current which is a periodic function of time
5 flowing into the second capacitor being modulated by a respective value $(c1/c2-K)$, wherein $c1$ is the capacitance of a respective one of the set of first capacitors, and $c2$ is the capacitance of the second capacitor.

10 32. The apparatus of claim 31 wherein at steady state, a linear relationship exists between the output of a respective one of the plurality of difference amplifiers at a fixed time of a cycle of the output and the respective value $(c1/c2-K)$.

15 33. The apparatus of claim 31 wherein at steady state, a linear relationship exists between an average absolute value of a positive and/or negative cycle of the output of a respective one of the plurality of difference amplifiers and the respective value $(c1/c2-K)$.

20

34. The apparatus of claim 31 further including a synchronous demodulator, wherein the plurality of first periodic varying current sources and the second periodic varying current source are respective sinusoidal current sources, and wherein the
25 synchronous demodulator is operative to extract a respective one

of signals proportional to the respective value $(c1/c2-K)$ from the output of a respective one of the plurality of difference amplifiers by synchronous demodulation.

5 35. The apparatus of claim 25 further comprising:

a periodic varying voltage source operative to provide an output voltage to respective non-inverting inputs of the plurality of first operational amplifiers and a non-inverting input of a second operational amplifier,

10 wherein a respective one of a plurality of first resistors is connected between an output of a respective one of the plurality of first operational amplifiers and a respective one of the set of first capacitors at a respective one of a plurality of first nodes,

15 wherein a voltage potential at a respective one of the plurality of first nodes is provided to an inverting input of a respective one of the plurality of first operational amplifiers, whereby feedback current flowing through a respective one of the plurality of first resistors maintains the voltage potential at
20 the respective one of the plurality of first nodes substantially equal to a voltage potential of the output voltage of the periodic varying voltage source,

wherein a second resistor is connected between an output of the second operational amplifier and the second capacitor at a
25 second node,

wherein a voltage potential at the second node is provided to an inverting input of the second operational amplifier, whereby feedback current flowing through the second resistor maintains the voltage potential at the second node substantially equal to the voltage potential of the output voltage of the periodic
5 varying voltage source,

wherein the output of a respective one of the plurality of first operational amplifiers is provided to a first differential input of a respective one of the plurality of difference
10 amplifiers, and the output of the second operational amplifier is provided to a second differential input of the respective one of the plurality of difference amplifiers, and

wherein at steady state, the output of a respective one of the plurality of difference amplifiers is proportional to a signal
15 representing a time derivative of the output voltage of the periodic varying voltage source which is a periodic function of time being modulated by a respective value $(r1 \cdot c1 - r2 \cdot C2)$, wherein $c1$ is the capacitance of a respective one of the set of first capacitors, $c2$ is the capacitance of the second capacitor, $r1$ is
20 the resistance of a respective one of the plurality of first resistors, and $r2$ is the resistance of the second resistor.

36. The apparatus of claim 35 wherein at steady state, a linear relationship exists between the output of a respective one of the
25 plurality of difference amplifiers at a fixed time of a cycle of

the output and the respective value of $(r1*c1-r2*c2)$.

37. The apparatus of claim 35 wherein at steady state, a linear relationship exists between an average absolute value of a positive and/or negative cycle of the output of a respective one of the plurality of difference amplifiers and the respective value $(r1*c1-r2*c2)$.

38. The apparatus of claim 35 further including a synchronous demodulator, wherein the periodic varying voltage source is a sinusoidal voltage source, and wherein the synchronous demodulator is operative to extract a respective one of signals proportional to the respective value $(r1*c1-r2*c2)$ from the output of a respective one of the plurality of difference amplifiers by synchronous demodulation.

39. The apparatus of claim 25 further including a set of first sensor electrodes and a common second sensor electrode, wherein each of the set of first capacitors is formed between a human appendage and a respective one of the set of first sensor electrodes and wherein the second capacitor is formed between the human appendage and the common second sensor electrode, and further comprising:

a switching decision section operative to generate each one of a plurality of first control signals based on the output a

respective one of the plurality of difference amplifiers, and
a switching output section operative to indicate a
respective one of status conditions in response to a respective
one of the plurality of first control signals.

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40. The apparatus of claim 39 further comprising

a startup delay section operative to delay the generation
of each one of the plurality of first control signals by the
switching decision section until the apparatus is in a steady
10 state condition.

41. The apparatus of claim 39 further comprising a common guard
electrode,

wherein the guard electrode is electrically isolated from
15 the set of first sensor electrodes and the second sensor electrode,
wherein the guard electrode is maintained at substantially the
same voltage potential as the set of first sensor electrodes and
the second sensor electrode, and wherein the guard electrode is
configured to surround a back and sides of the set of first sensor
20 electrodes and the second electrode.

42. The apparatus of claim 39 wherein a respective one of the
set of first sensor electrodes is a respective one of a set of
center electrodes and the second sensor electrode is a common
25 outer electrode, and

wherein each one of the set of center electrodes is spaced from and at least partly surrounded by the outer electrode.

43. The apparatus of claim 42 further including a surface
5 containing many depressed areas each in a form of a container having a base and a brim, wherein the outer electrode and the set of center electrodes are disposed on one of the sides of the surface, and wherein each one of the set of center electrodes are disposed near the base of a respective one of the containers and
10 the outer electrode is disposed near the brims of the containers.

44. The apparatus of claim 25 further including a set of first sensor electrodes and a common second sensor electrode, wherein each of the set of first capacitors is formed between a conducting
15 object and a respective one of the set of first sensor electrodes and wherein the second capacitor is formed between the conducting object and the common second sensor electrode, and outputs of the plurality of difference amplifiers are indicative of a proximity or movement of the conducting object relative to the first sensor
20 electrodes.

45. The apparatus of claim 25 further including a set of first sensor electrodes and a common second sensor electrode, wherein each of the set of first capacitors is formed between a human
25 appendage and a respective one of the set of first sensor

electrodes and wherein the second capacitor is formed between the human appendage and the common second sensor electrode,

wherein the set of first sensor electrodes is arranged to form a two dimensional array, and

5 wherein outputs of the plurality of difference amplifiers are indicative of a position or movement of the human appendage relative to the first sensor electrodes.

46. The apparatus of claim 25 further including a plurality of
10 sets of first sensor electrodes and a common second sensor electrode,

wherein the set of first capacitors is a selected set of a plurality of sets of first capacitors,

wherein each capacitor of a respective set of the plurality
15 of sets of first capacitors is formed between a human appendage and a respective one of a respective set of the plurality of sets of first sensor electrodes and wherein the second capacitor is formed between the human appendage and the common second sensor electrode,

20 wherein the plurality of sets of first sensor electrodes are arranged to form a two dimensional array, and

wherein outputs of the plurality of difference amplifiers are indicative of a position or movement of the human appendage relative to the first sensor electrodes.

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AMENDED CLAIMS

**[received by the International Bureau on 17 January 2006 (17.01.06);
original claims 1 – 13, 18 – 21, 23, 25, and 27 – 46 amended;
new claims 47 – 53 added; remaining claims unchanged (18 pages)]**

1. An apparatus for measuring relative changes in capacitances of at least one first capacitor (C1) and at least one second capacitor (C2), the apparatus comprising:

at least one operational amplifier (A0, A1) operative to employ negative feedback to maintain the at least one first capacitor and the at least one second capacitor at substantially equal voltage potentials, each of the at least one first capacitor and the at least one second capacitor having one end connected to a common ground; and

at least one difference amplifier (A2) operative to provide an electrical signal (Vd), wherein a substantially linear relationship exists between the electrical signal and a specified arithmetic function of the capacitances of the at least one first capacitor and the at least one second capacitor,

wherein one of the at least one operational amplifier (A1) is operative to provide an output (V1) directly or indirectly to an input of the at least one difference amplifier.

2. The apparatus of claim 1 further comprising:

a periodic varying voltage source (G1) operative to drive a first resistor (R1) connected to the first capacitor at a first node (101), and to drive a second resistor (R2) connected to the second capacitor at a second node (102),

wherein the at least one operational amplifier includes a first operational amplifier (A1),

wherein a voltage potential at the first node is provided to an inverting input of the first operational amplifier, and a voltage potential at the second node is provided to a non-inverting input of the first operational amplifier,

wherein a third resistor (R3) is connected between an output of the first operational amplifier and the first node, whereby a feedback current (I3) flowing through the third

resistor maintains the voltage potential at the first node substantially equal to the voltage potential at the second node,

wherein the voltage potential at one of the first and second nodes is provided to a first differential input of the difference amplifier, and the output of the first operational amplifier is provided to a second differential input of the difference amplifier, and

wherein at steady state, the electrical signal provided by the difference amplifier is substantially proportional to a signal representing a current (I_2) which is a periodic function of time flowing through the second resistor being modulated by a value $(c_1/c_2 - r_2/r_1)$, wherein c_1 is the capacitance of the first capacitor, c_2 is the capacitance of the second capacitor, r_1 is the resistance of the first resistor, and r_2 is the resistance of the second resistor.

3. The apparatus of claim 2 wherein at steady state, a substantially linear relationship exists between the electrical signal provided by the difference amplifier at a fixed time of a cycle of the electrical signal and the value $(c_1/c_2 - r_2/r_1)$.

4. The apparatus of claim 2 wherein at steady state, a substantially linear relationship exists between an average absolute value of a positive and/or negative cycle of the electrical signal provided by the difference amplifier and the value $(c_1/c_2 - r_2/r_1)$.

5. The apparatus of claim 2 further including a synchronous demodulator, wherein the periodic varying voltage source is a sinusoidal voltage source, and wherein the synchronous demodulator is operative to extract a signal substantially proportional to the value $(c_1/c_2 - r_2/r_1)$ from the electrical signal provided by the difference amplifier by synchronous demodulation.

6. The apparatus of claim 1 further comprising:

a first periodic varying current source (G1) operative to provide a first output current (I1) to the first capacitor, the first capacitor being connected to the first periodic varying current source at a first node (101), and

a second periodic varying current source (G2) operative to provide a second output current (I2) to the second capacitor, the second capacitor being connected to the second periodic varying current source at a second node (102),

wherein a magnitude of the first output current is equal to a constant K times a magnitude of the second output current,

wherein the at least one operational amplifier includes a first operational amplifier (A1),

wherein a voltage potential at the first node is provided to an inverting input of the first operational amplifier, and a voltage potential at the second node is provided to a non-inverting input of the first operational amplifier,

wherein a first resistor (R1) is connected between an output of the first operational amplifier and the first node, whereby a feedback current (I3) flowing through the first resistor maintains the voltage potential at the first node substantially equal to the voltage potential at the second node,

wherein the voltage potential at one of the first and second nodes is provided to a first differential input of the difference amplifier, and the output of the first operational amplifier is provided to a second differential input of the difference amplifier, and

wherein at steady state, the electrical signal provided by the difference amplifier is substantially proportional to a signal representing a current (I2) which is a periodic function of time flowing into the second capacitor being modulated by a value $(c1/c2-K)$, wherein $c1$ is the capacitance of the first capacitor, and $c2$ is the capacitance of the second capacitor.

7. The apparatus of claim 6 wherein at steady state, a substantially linear relationship exists between the electrical signal provided by the difference amplifier at a fixed time of a cycle of the electrical signal and the value $(c1/c2-K)$.

8. The apparatus of claim 6 wherein at steady state, a substantially linear relationship exists between an average absolute value of a positive and/or negative cycle of the electrical signal provided by the difference amplifier and the value $(c1/c2-K)$.

9. The apparatus of claim 6 further including a synchronous demodulator, wherein the first and second periodic varying current sources are respective sinusoidal current sources, and wherein the synchronous demodulator is operative to extract a signal substantially proportional to the value of $(c1/c2-K)$ from the electrical signal provided by the difference amplifier by synchronous demodulation.

10. The apparatus of claim 1

wherein the at least one operational amplifier includes first and second operational amplifiers (A1, A0), and further comprising:

a periodic varying voltage source (G1) operative to provide an output voltage (V_s) to a non-inverting input of the first operational amplifier and a non-inverting input of the second operational amplifier,

wherein a first resistor (R_1) is connected between an output of the first operational amplifier and the first capacitor at a first node (101),

wherein a voltage potential at the first node is provided to an inverting input of the first operational amplifier, whereby a feedback current (I_1) flowing through the first resistor maintains the voltage potential at the first node

substantially equal to a voltage potential of the output voltage of the periodic varying voltage source,

wherein a second resistor (R2) is connected between an output of the second operational amplifier and the second capacitor at a second node (102),

wherein a voltage potential at the second node is provided to an inverting input of the second operational amplifier, whereby a feedback current (I2) flowing through the second resistor maintains the voltage potential at the second node substantially equal to the voltage potential of the output voltage of the periodic varying voltage source,

wherein the output of the first operational amplifier is provided to a first differential input of the difference amplifier, and the output of the second operational amplifier is provided to a second differential input of the difference amplifier, and

wherein at steady state, the electrical signal provided by the difference amplifier is substantially proportional to a signal representing a time derivative of the output voltage of the periodic varying voltage source which is a periodic function of time being modulated by a value $(r1 \cdot c1 - r2 \cdot c2)$, wherein $c1$ is the capacitance of the first capacitor, $c2$ is the capacitance of the second capacitor, $r1$ is the resistance of the first resistor, and $r2$ is the resistance of the second resistor.

11. The apparatus of claim 10 wherein at steady state, a substantially linear relationship exists between the electrical signal provided by the difference amplifier at a fixed time of a cycle of the electrical signal and the value of $(r1 \cdot c1 - r2 \cdot c2)$.

12. The apparatus of claim 10 wherein at steady state, a substantially linear relationship exists between an average absolute value of a positive and/or negative cycle of the electrical signal provided by the difference amplifier and the value of $(r1 \cdot c1 - r2 \cdot c2)$.

13. The apparatus of claim 10 further including a synchronous demodulator, wherein the periodic varying voltage source is a sinusoidal voltage source, and wherein the synchronous demodulator is operative to extract a signal substantially proportional to the value $(r1*c1-r2*c2)$ from the electrical signal provided by the difference amplifier by synchronous demodulation.

14. The apparatus of claim 1 wherein each of the first capacitors is a capacitive transducer.

15. The apparatus of claim 1 wherein each of the second capacitors is a capacitive transducer.

16. The apparatus of claim 1 wherein each of the first capacitors is a capacitive sensor.

17. The apparatus of claim 1 wherein each of the second capacitors is a capacitive sensor.

18. The apparatus of claim 1 wherein each of the second capacitors comprises an internal capacitance associated with an input of an operational amplifier.

19. The apparatus of claim 1 further including first and second sensor electrodes (E1, E2), wherein the first capacitor is formed between a human appendage and the first sensor electrode, wherein the second capacitor is formed between the human appendage and the second sensor electrode, and

further comprising:

a switching decision section (205) operative to generate a first control signal (206) based on the electrical signal provided by the difference amplifier, and

a switching output section (207) operative to indicate a status condition in response to the first control signal.

20. The apparatus of claim 19 further comprising

a startup delay section (203) operative to delay the generation of the first control signal by the switching decision section until the apparatus is in a steady state condition.

21. The apparatus of claim 19 further comprising a guard electrode (E3), wherein the guard electrode is electrically isolated from the first and second sensor electrodes, wherein the guard electrode is maintained at substantially the same voltage potential as the first and second sensor electrodes, and wherein the guard electrode is configured to surround a back and sides of the first and second sensor electrodes.

22. The apparatus of claim 19 wherein the first sensor electrode is a center electrode and the second sensor electrode is an outer electrode, and wherein the center electrode is spaced from and at least partly surrounded by the outer electrode.

23. The apparatus of claim 19 further including a surface in a form of a container having a base and a brim, wherein the first and second sensor electrodes are disposed on one of the sides of the surface, and wherein the first sensor electrode is disposed near the base of the container and the second sensor electrode is disposed near the brim of the container.

24. The apparatus of claim 1 wherein the at least one second capacitor is a selected one of a plurality of second capacitors.

25. The apparatus of claim 1 wherein the at least one first capacitor is one of a set of first capacitors (C11-C1n),

wherein the at least one operational amplifier is one of a plurality of first operational amplifiers (A11-A1n), each one of the plurality of first operational amplifiers being associated with a respective one of the set of first capacitors,

wherein the at least one difference amplifier is one of a plurality of difference amplifiers (A21-A2n), each one of the plurality of difference amplifiers being associated with a respective one of the set of first capacitors,

wherein each one of the plurality of first operational amplifiers associated with a respective one of the set of first capacitors uses negative feedback to maintain the respective one of the set of first capacitors and the second capacitor at substantially equal voltage potentials,

wherein each one of the plurality of first operational amplifiers is operative to provide an output directly or indirectly to an input of a respective one of the plurality of difference amplifiers, and

wherein each one of the plurality of difference amplifiers is operative to provide a respective one of a plurality of electrical signals (Vd1-Vdn), wherein a substantially linear relationship exists between each respective electrical signal and a specified arithmetic function of the capacitances of the respective one of the set of first capacitors and the second capacitor.

26. The apparatus of claim 25 wherein the set of first capacitors is a selected one of a plurality of sets of first capacitors.

27. The apparatus of claim 25 further comprising:

a periodic varying voltage source (G1) operative to drive a plurality of first resistors (R11-R1n), each first resistor being connected to a respective one of the set of first capacitors at a respective one of a plurality of first nodes,

and to drive a second resistor (R2) connected to the second capacitor at a second node,

wherein a voltage potential at a respective one of the plurality of first nodes is provided to an inverting input of a respective one of the plurality of first operational amplifiers, and a voltage potential at the second node is provided to a non-inverting input of the respective one of the plurality of first operational amplifiers,

wherein a respective one of a plurality of third resistors (R31-R3n) is connected between an output of a respective one of the plurality of first operational amplifiers and a respective one of the plurality of first nodes, whereby a feedback current flowing through the respective one of the plurality of third resistors maintains the voltage potential at the respective one of the plurality of first nodes substantially equal to the voltage potential at the second node,

wherein the voltage potential at the second node is provided to a first differential input of a respective one of the plurality of difference amplifiers, and the output of a respective one of the plurality of first operational amplifiers is provided to a second differential input of the respective one of the plurality of difference amplifiers, and

wherein at steady state, the electrical signal provided by a respective one of the plurality of difference amplifiers is substantially proportional to a signal representing a current which is a periodic function of time flowing through the second resistor being modulated by a respective value ($c1/c2-r2/r1$), wherein $c1$ is the capacitance of a respective one of the set of first capacitors, $c2$ is the capacitance of the second capacitor, $r1$ is the resistance of a respective one of the plurality of first resistors, and $r2$ is the resistance of the second resistor.

28. The apparatus of claim 27 wherein at steady state, a substantially linear relationship exists between the electrical

signal provided by a respective one of the plurality of difference amplifiers at a fixed time of a cycle of the electrical signal and a respective value $(c1/c2 - r2/r1)$.

29. The apparatus of claim 27 wherein at steady state, a substantially linear relationship exists between an average absolute value of a positive and/or negative cycle of the electrical signal provided by a respective one of the plurality of difference amplifiers and a respective value $(c1/c2 - r2/r1)$.

30. The apparatus of claim 27 wherein the periodic varying voltage source is a sinusoidal voltage source, and further including a synchronous demodulator operative to extract a respective one of signals substantially proportional to a respective value $(c1/c2 - r2/r1)$ from the electrical signal provided by a respective one of the plurality of difference amplifiers by synchronous demodulation.

31. The apparatus of claim 25 further comprising:

a plurality of first periodic varying current sources ($G11-G1n$), each of the first periodic varying current sources being operative to provide a first output current ($I11-I1n$) to a respective one of the set of first capacitors, each of the set of first capacitors being connected to a respective one of the first periodic varying current sources at a respective one of a plurality of first nodes, and

a second periodic varying current source ($G2$) operative to provide a second output current ($I2$) to the second capacitor, the second capacitor being connected to the second periodic varying current source at a second node,

wherein a magnitude of a respective one of the first output currents is equal to a respective constant K times a magnitude of the second output current,

wherein a voltage potential at a respective one of the plurality of first nodes is provided to an inverting input of a

respective one of the plurality of first operational amplifiers, and a voltage potential at the second node is provided to a non-inverting input of the respective one of the plurality of first operational amplifiers,

wherein a respective one of a plurality of first resistors (R_{11} - R_{1n}) is connected between the output of a respective one of the plurality of first operational amplifiers and a respective one of the plurality of first nodes, whereby a feedback current flowing through the respective one of the plurality of first resistors maintains the voltage potential at the respective one of the plurality of first nodes substantially equal to the voltage potential at the second node,

wherein the voltage potential at the second node is provided to a first differential input of a respective one of the plurality of difference amplifiers, and the output of a respective one of the plurality of first operational amplifiers is provided to a second differential input of the respective one of the plurality of difference amplifiers, and

wherein at steady state, the electrical signal provided by a respective one of the plurality of difference amplifiers is substantially proportional to a signal representing a current which is a periodic function of time flowing into the second capacitor being modulated by a respective value (c_1/c_2-K), wherein c_1 is the capacitance of a respective one of the set of first capacitors, and c_2 is the capacitance of the second capacitor.

32. The apparatus of claim 31 wherein at steady state, a substantially linear relationship exists between the electrical signal provided by a respective one of the plurality of difference amplifiers at a fixed time of a cycle of the electrical signal and a respective value (c_1/c_2-K).

33. The apparatus of claim 31 wherein at steady state, a substantially linear relationship exists between an average

absolute value of a positive and/or negative cycle of the electrical signal provided by a respective one of the plurality of difference amplifiers and a respective value $(c1/c2-K)$.

34. The apparatus of claim 31 further including a synchronous demodulator, wherein the plurality of first periodic varying current sources and the second periodic varying current source are respective sinusoidal current sources, and wherein the synchronous demodulator is operative to extract a respective one of signals substantially proportional to a respective value $(c1/c2-K)$ from the electrical signal provided by a respective one of the plurality of difference amplifiers by synchronous demodulation.

35. The apparatus of claim 25 further comprising:

a second operational amplifier (A0); and

a periodic varying voltage source (G1) operative to provide an output voltage (V_s) to respective non-inverting inputs of the plurality of first operational amplifiers and a non-inverting input of the second operational amplifier,

wherein a respective one of a plurality of first resistors ($R_{11}-R_{1n}$) is connected between an output of a respective one of the plurality of first operational amplifiers and a respective one of the set of first capacitors at a respective one of a plurality of first nodes,

wherein a voltage potential at a respective one of the plurality of first nodes is provided to an inverting input of a respective one of the plurality of first operational amplifiers, whereby a feedback current flowing through a respective one of the plurality of first resistors maintains the voltage potential at the respective one of the plurality of first nodes substantially equal to a voltage potential of the output voltage of the periodic varying voltage source,

wherein a second resistor is connected between an output of the second operational amplifier and the second capacitor at a second node,

wherein a voltage potential at the second node is provided to an inverting input of the second operational amplifier, whereby a feedback current flowing through the second resistor maintains the voltage potential at the second node substantially equal to the voltage potential of the output voltage of the periodic varying voltage source,

wherein the output of a respective one of the plurality of first operational amplifiers is provided to a first differential input of a respective one of the plurality of difference amplifiers, and the output of the second operational amplifier is provided to a second differential input of the respective one of the plurality of difference amplifiers, and

wherein at steady state, the electrical signal provided by a respective one of the plurality of difference amplifiers is substantially proportional to a signal representing a time derivative of the output voltage of the periodic varying voltage source which is a periodic function of time being modulated by a respective value $(r1 \cdot c1 - r2 \cdot C2)$, wherein $c1$ is the capacitance of a respective one of the set of first capacitors, $c2$ is the capacitance of the second capacitor, $r1$ is the resistance of a respective one of the plurality of first resistors, and $r2$ is the resistance of the second resistor.

36. The apparatus of claim 35 wherein at steady state, a substantially linear relationship exists between the electrical signal provided by a respective one of the plurality of difference amplifiers at a fixed time of a cycle of the electrical signal and a respective value $(r1 \cdot c1 - r2 \cdot c2)$.

37. The apparatus of claim 35 wherein at steady state, a substantially linear relationship exists between an average absolute value of a positive and/or negative cycle of the

electrical signal provided by a respective one of the plurality of difference amplifiers and a respective value $(r1*c1-r2*c2)$.

38. The apparatus of claim 35 further including a synchronous demodulator, wherein the periodic varying voltage source is a sinusoidal voltage source, and wherein the synchronous demodulator is operative to extract a respective one of signals substantially proportional to a respective value $(r1*c1-r2*c2)$ from the electrical signal provided by a respective one of the plurality of difference amplifiers by synchronous demodulation.

39. The apparatus of claim 25 further including a set of first sensor electrodes (E11-E1n) and a second sensor electrode (E2), wherein each of the set of first capacitors is formed between a human appendage and a respective one of the set of first sensor electrodes and wherein the second capacitor is formed between the human appendage and the second sensor electrode, and

further comprising:

a switching decision section (205) operative to generate a respective one of a plurality of first control signals (206) based on the electrical signal provided by a respective one of the plurality of difference amplifiers, and

a switching output section (207) operative to indicate a respective one of status conditions in response to a respective one of the plurality of first control signals.

40. The apparatus of claim 39 further comprising

a startup delay section (203) operative to delay the generation of each one of the plurality of first control signals by the switching decision section until the apparatus is in a steady state condition.

41. The apparatus of claim 39 further comprising a guard electrode (E3), wherein the guard electrode is electrically

isolated from the set of first sensor electrodes and the second sensor electrode,

wherein the guard electrode is maintained at substantially the same voltage potential as the set of first sensor electrodes and the second sensor electrode, and

wherein the guard electrode is configured to surround a back and sides of the set of first sensor electrodes and the second sensor electrode.

42. The apparatus of claim 39 wherein a respective one of the set of first sensor electrodes is a respective one of a set of center electrodes and the second sensor electrode is a common outer electrode, and

wherein each one of the set of center electrodes is spaced from and at least partly surrounded by the common outer electrode.

43. The apparatus of claim 39 further including a surface containing many depressed areas, each depressed area in a form of a container having a base and a brim, wherein the second sensor electrode and the set of first sensor electrodes are disposed on one of the sides of the surface, and

wherein each one of the set of first sensor electrodes is disposed near the base of a respective one of the containers and the second sensor electrode is disposed near the brims of the containers.

44. The apparatus of claim 25 further including a set of first sensor electrodes, wherein each of the set of first capacitors is formed between a conducting object and a respective one of the set of first sensor electrodes, and

wherein the plurality of electrical signals provided by the plurality of difference amplifiers are indicative of proximity of the conducting object with respective to the first sensor electrodes.

45. The apparatus of claim 25 further including a set of first sensor electrodes, wherein each of the set of first capacitors is formed between a conducting object and a respective one of the set of first sensor electrodes,

wherein the set of first sensor electrodes is arranged to form an array, and

wherein the plurality of electrical signals provided by the plurality of difference amplifiers are indicative of a position or movement of the conducting object relative to the first sensor electrodes.

46. The apparatus of claim 25 further including a second sensor electrode, wherein the second capacitor is formed between the conducting object and the second sensor electrode.

47. The apparatus of claim 1 wherein each of the first capacitors comprises an internal capacitance associated with an input of an operational amplifier.

48. The apparatus of claim 1 further including a sensor electrode, wherein the first capacitor is formed between a conducting object and the sensor electrode, and

wherein the electrical signal provided by of the difference amplifier is indicative of a proximity of the conducting object with respect to the sensor electrode.

49. The apparatus of claim 1 further including a sensor electrode, wherein the second capacitor is formed between a conducting object and the sensor electrode, and

wherein the electrical signal provided by the difference amplifier is indicative of a proximity of the conducting object with respect to the sensor electrode.

50. The apparatus of claim 1 further including first and second sensor electrodes (E1, E2), wherein the first capacitor is formed between a conducting object and the first sensor electrode, wherein the second capacitor is formed between the conducting object and the second sensor electrode, and

wherein the electrical signal provided by of the difference amplifier is indicative of a proximity of the conducting object relative to the first and second sensor electrodes.

51. A capacitive sensing apparatus for generating at least one logical signal in response to proximity of a human appendage, the capacitive sensing apparatus comprising:

a surface having a first side and a second side;

at least one first sensor electrode disposed on the first side of the surface;

a second sensor electrode disposed on one of the first and second sides of the surface, the second sensor electrode being adjacent to and at least partly surrounding the at least one first sensor electrode; and

an apparatus configured to maintain the at least one first sensor electrode and the second sensor electrode at substantially equal voltage potentials, and to measure relative changes in a first capacitance between the human appendage and the at least one first sensor electrode with respect to a second capacitance between the human appendage and the second sensor electrode,

wherein the at least one logical signal is indicative of a status condition that the relative changes in the first and second capacitances exceed at least one preset threshold.

52. The apparatus of claim 51 wherein the surface contains at least one depressed area, each depressed area in a form of a container having a base and a brim,

wherein the at least one first sensor electrode is disposed near the base of a corresponding container of the at least one depressed area, and

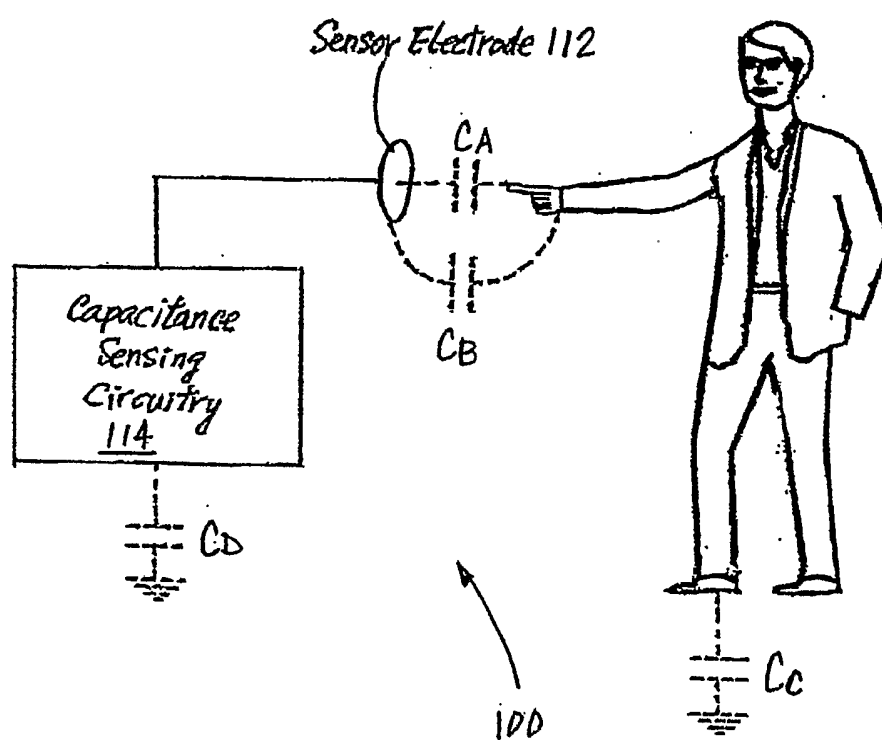
wherein the second sensor electrode is disposed near the brim of each container.

53. The apparatus of claim 51 further comprising a guard electrode, wherein the guard electrode is electrically isolated from the at least one first sensor electrode and the second sensor electrode,

wherein the guard electrode is maintained at substantially the same voltage potential as the at least one first sensor electrode and the second sensor electrode, and

wherein the guard electrode is configured to at least partly surround the at least one first sensor electrode and the second sensor electrode.

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**Fig. 1**

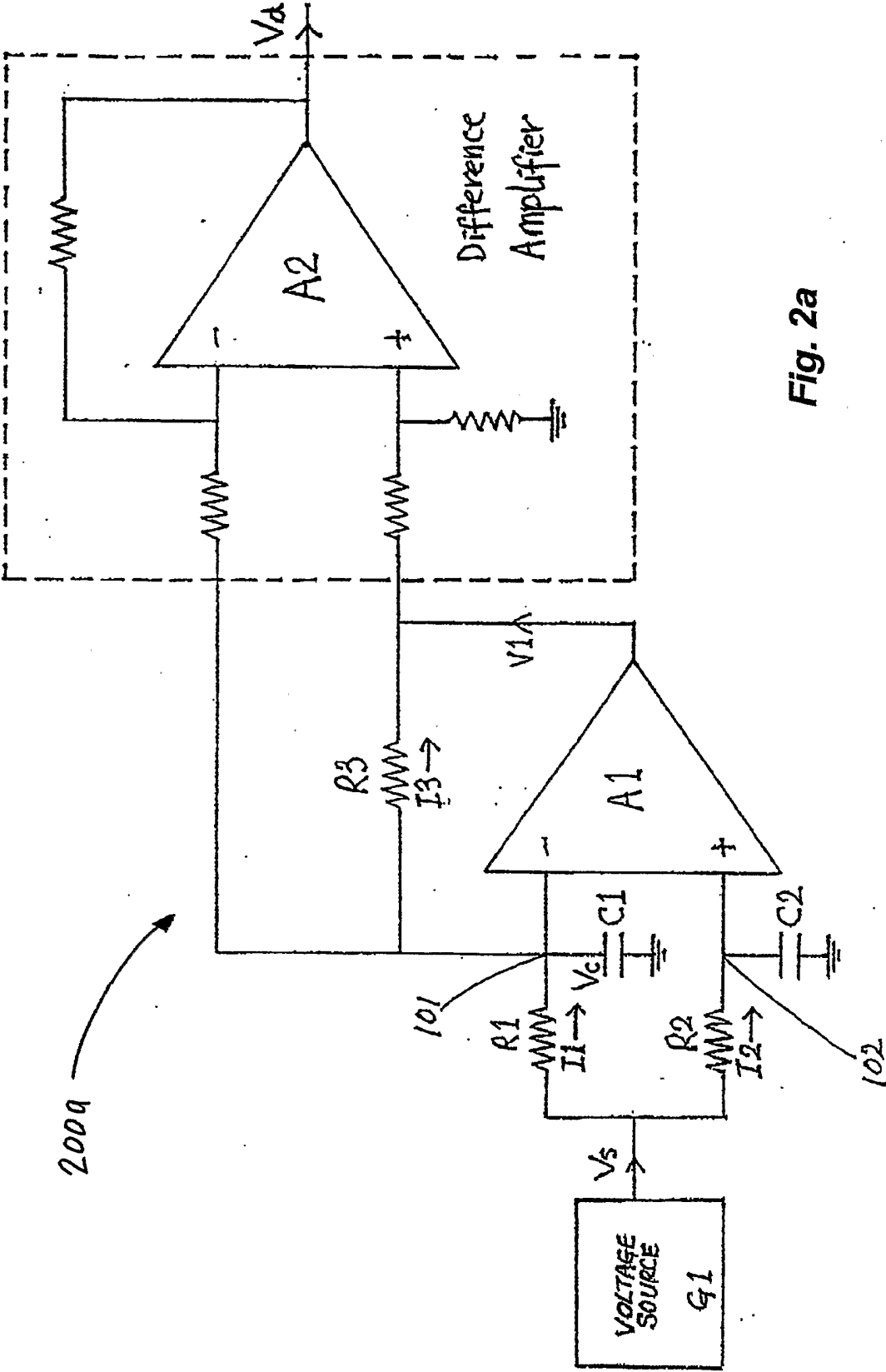
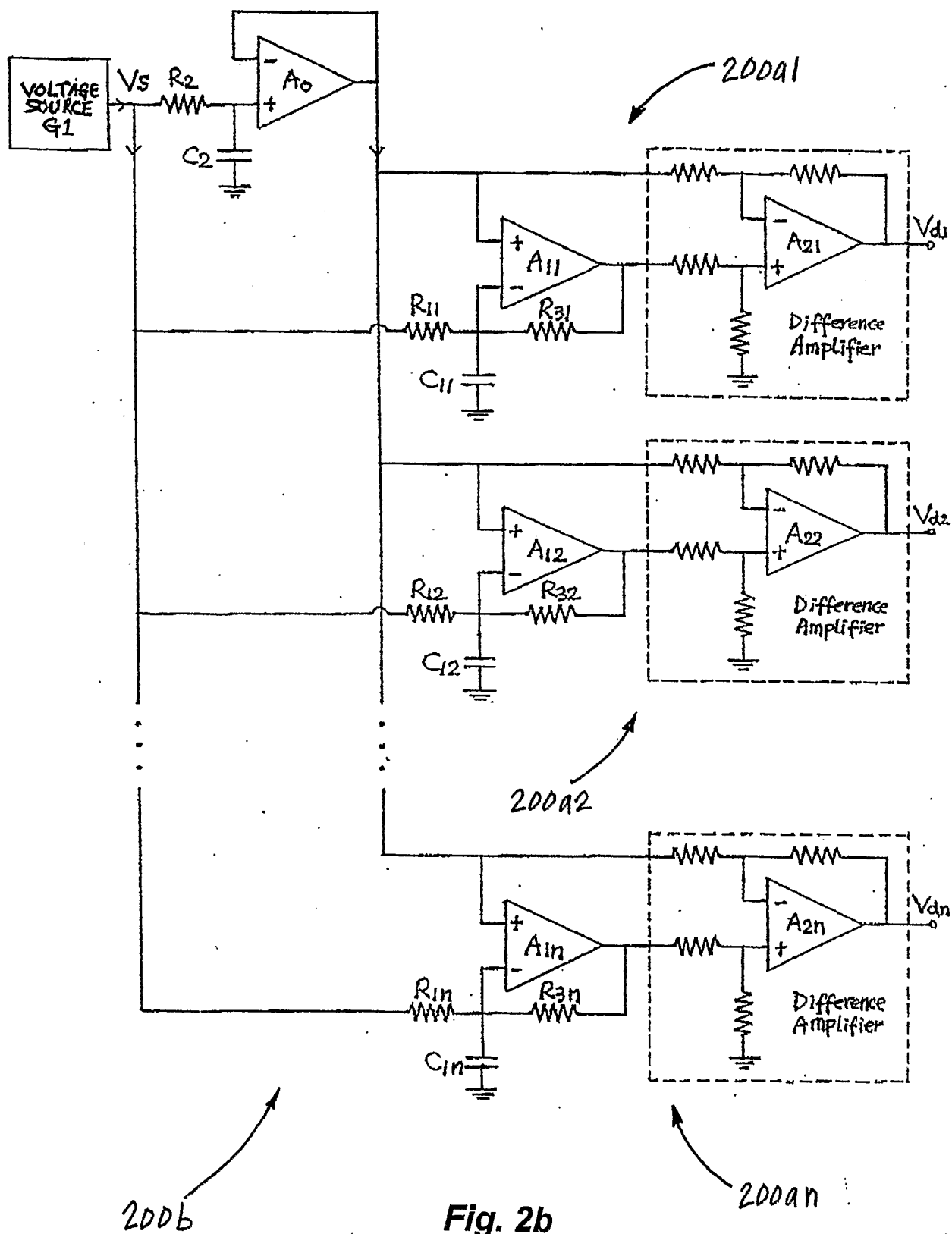


Fig. 2a

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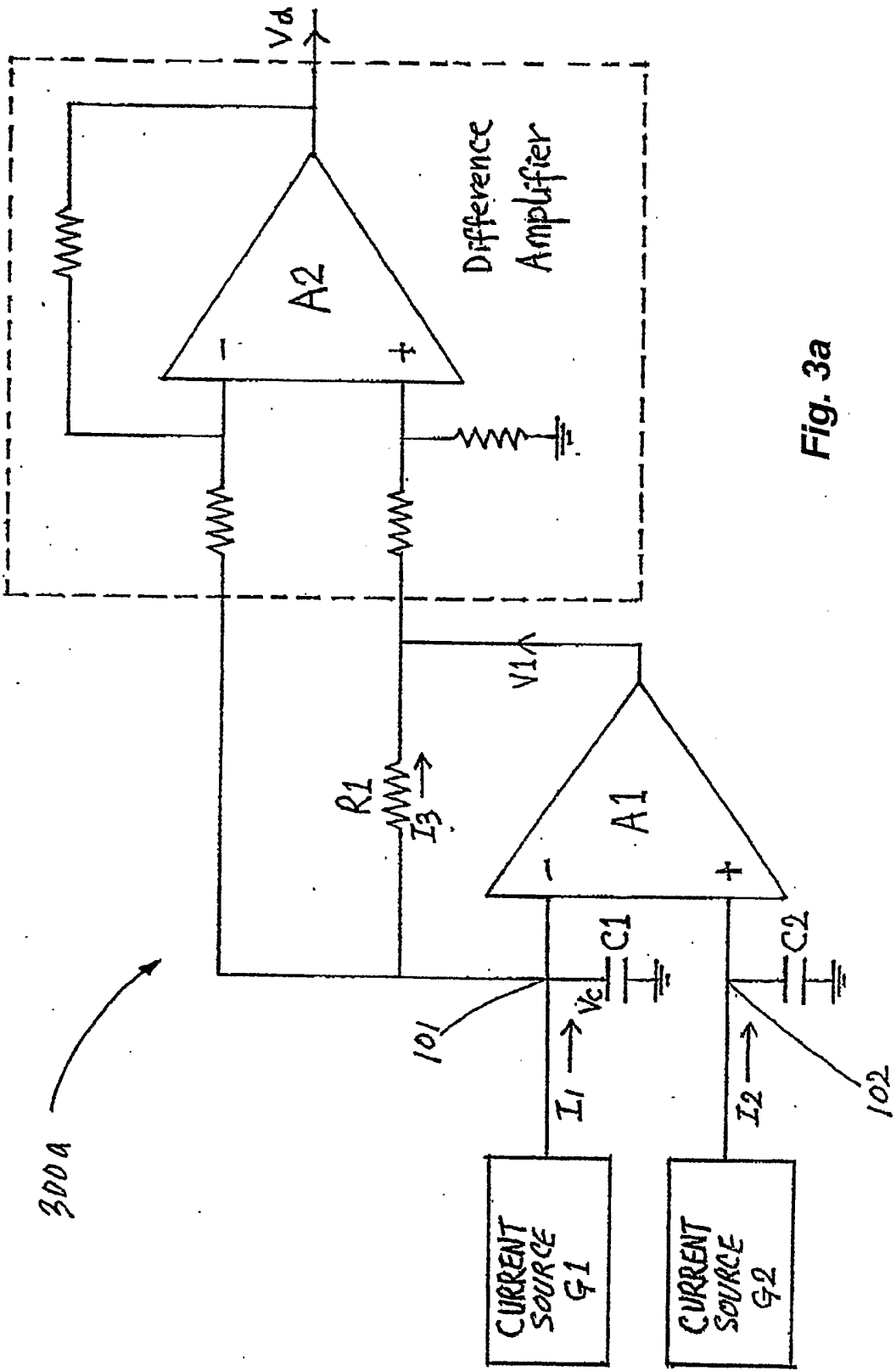
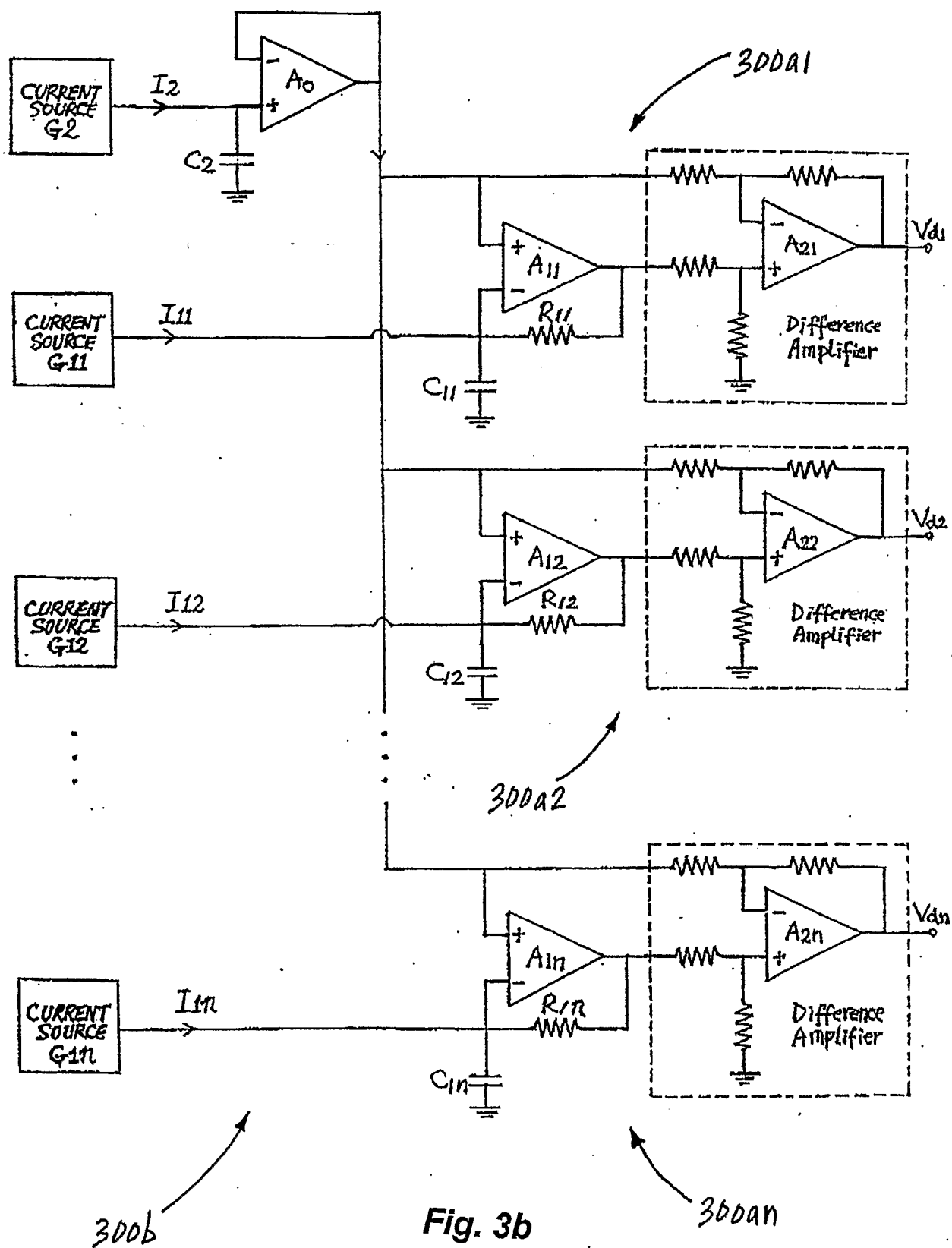


Fig. 3a

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**Fig. 3b**

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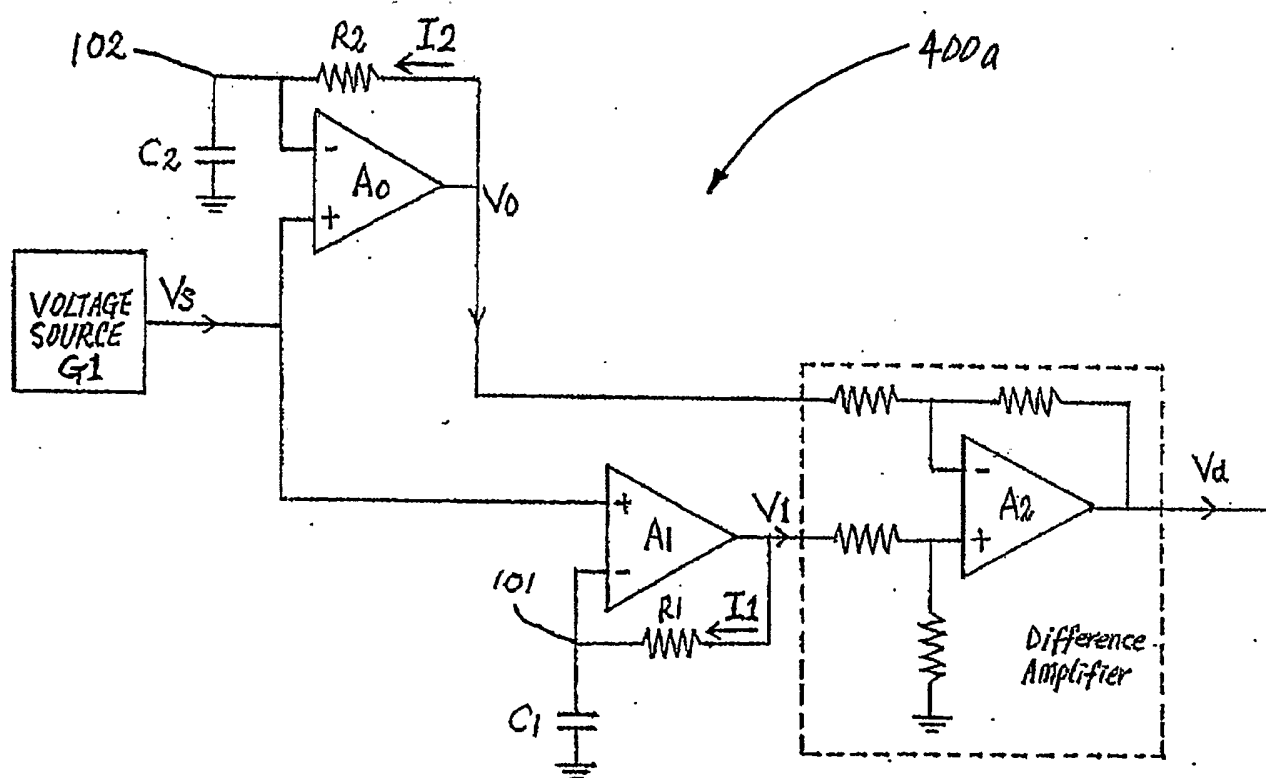


Fig. 4a

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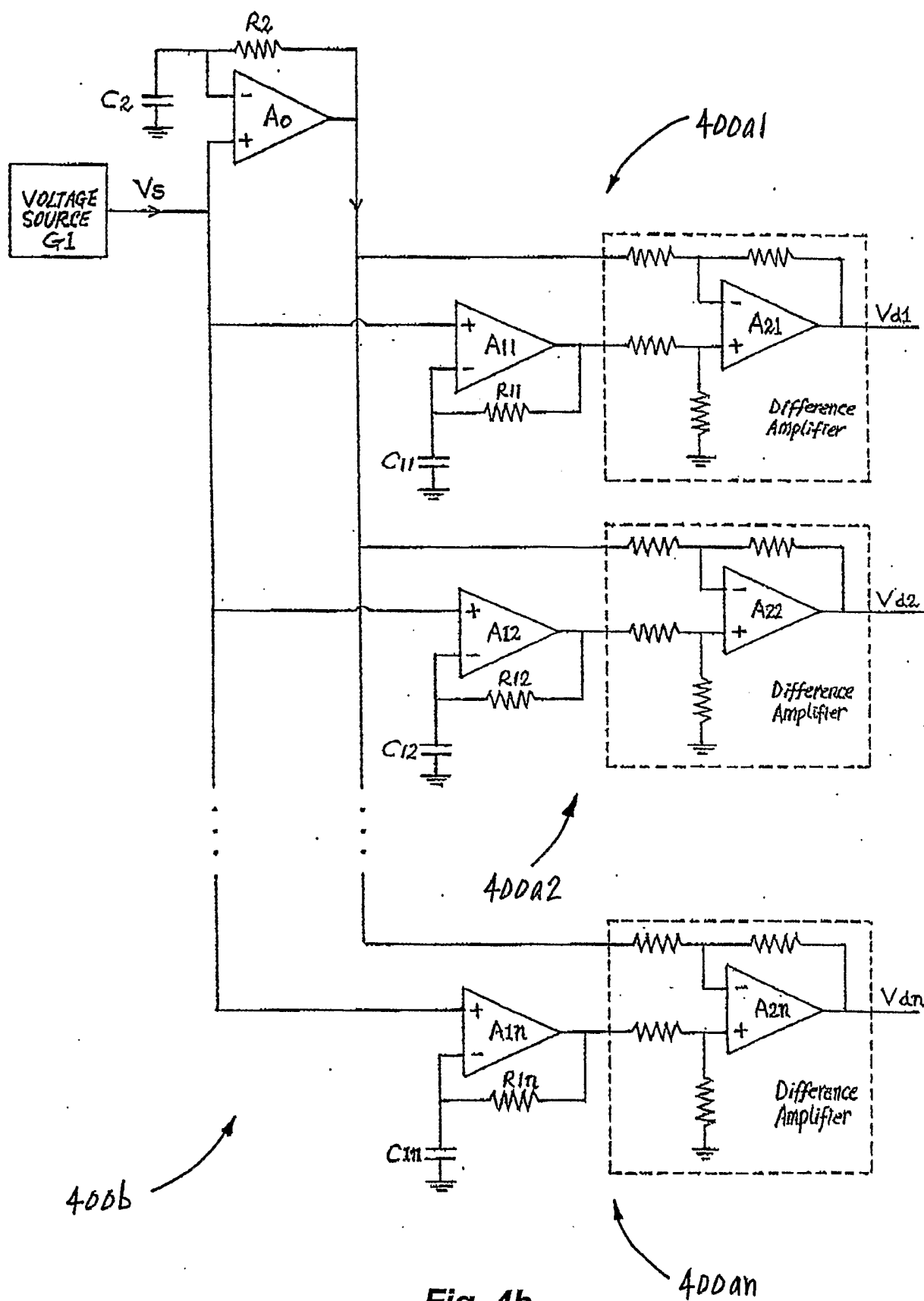
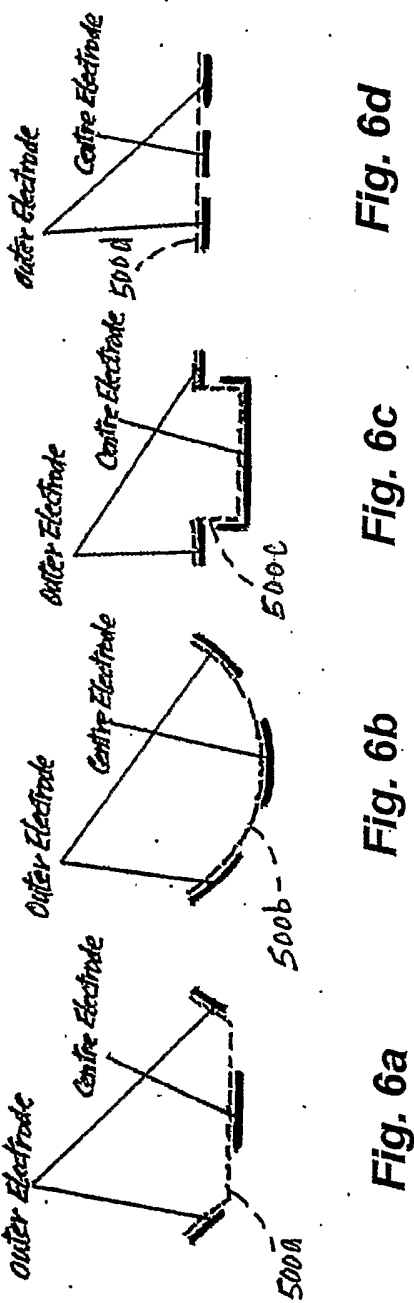
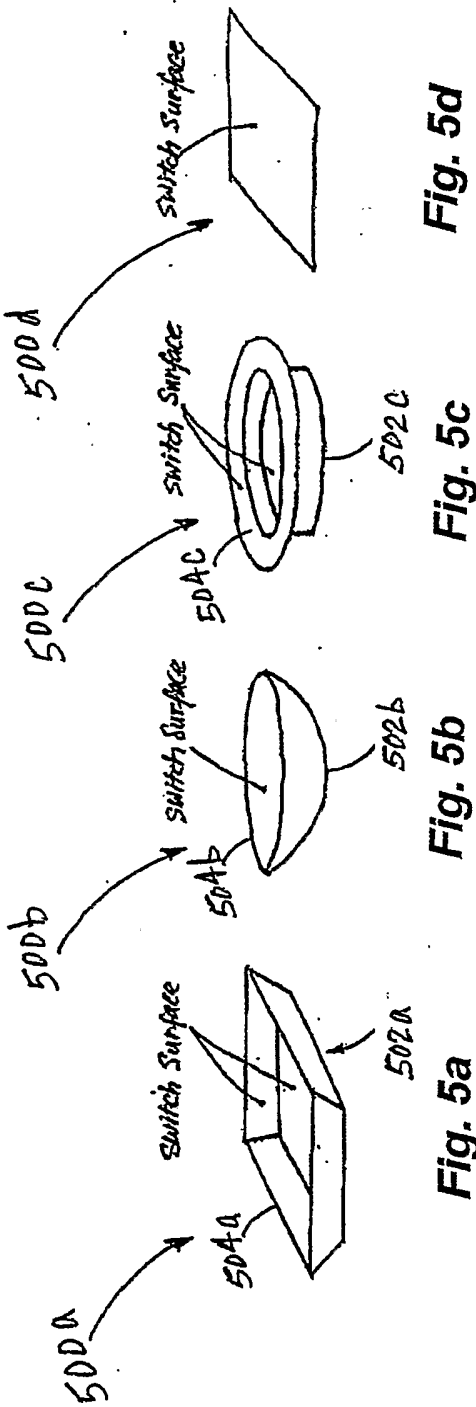
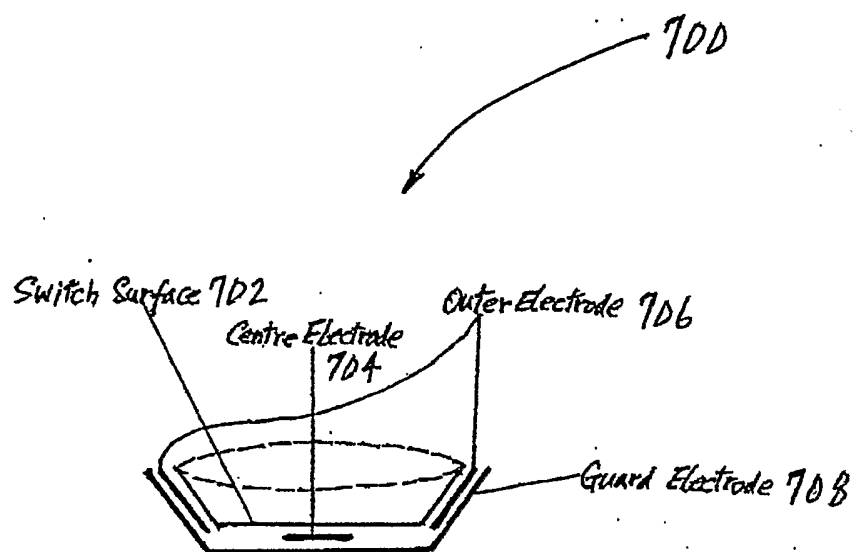


Fig. 4b



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**Fig. 7**

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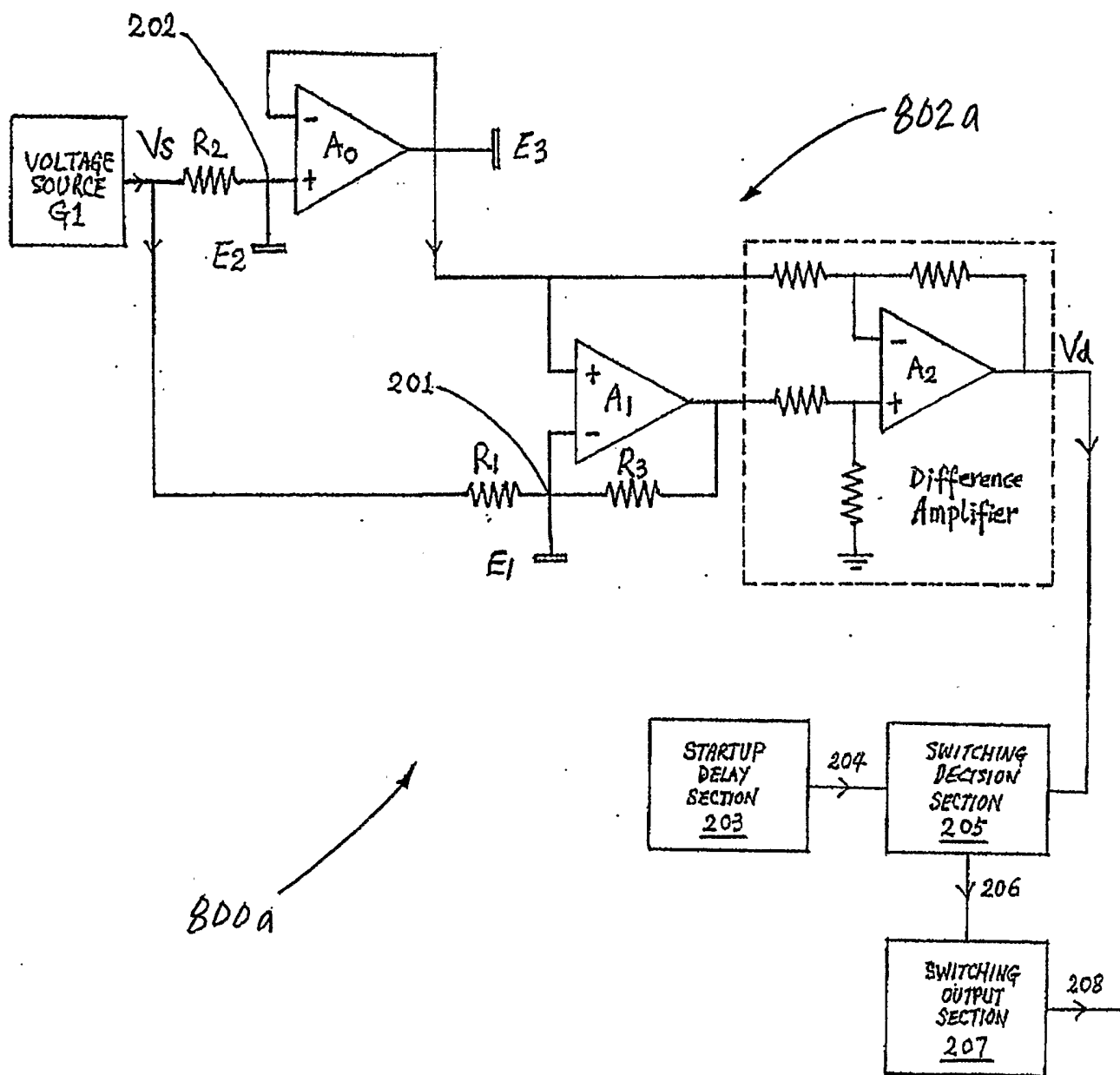


Fig. 8a

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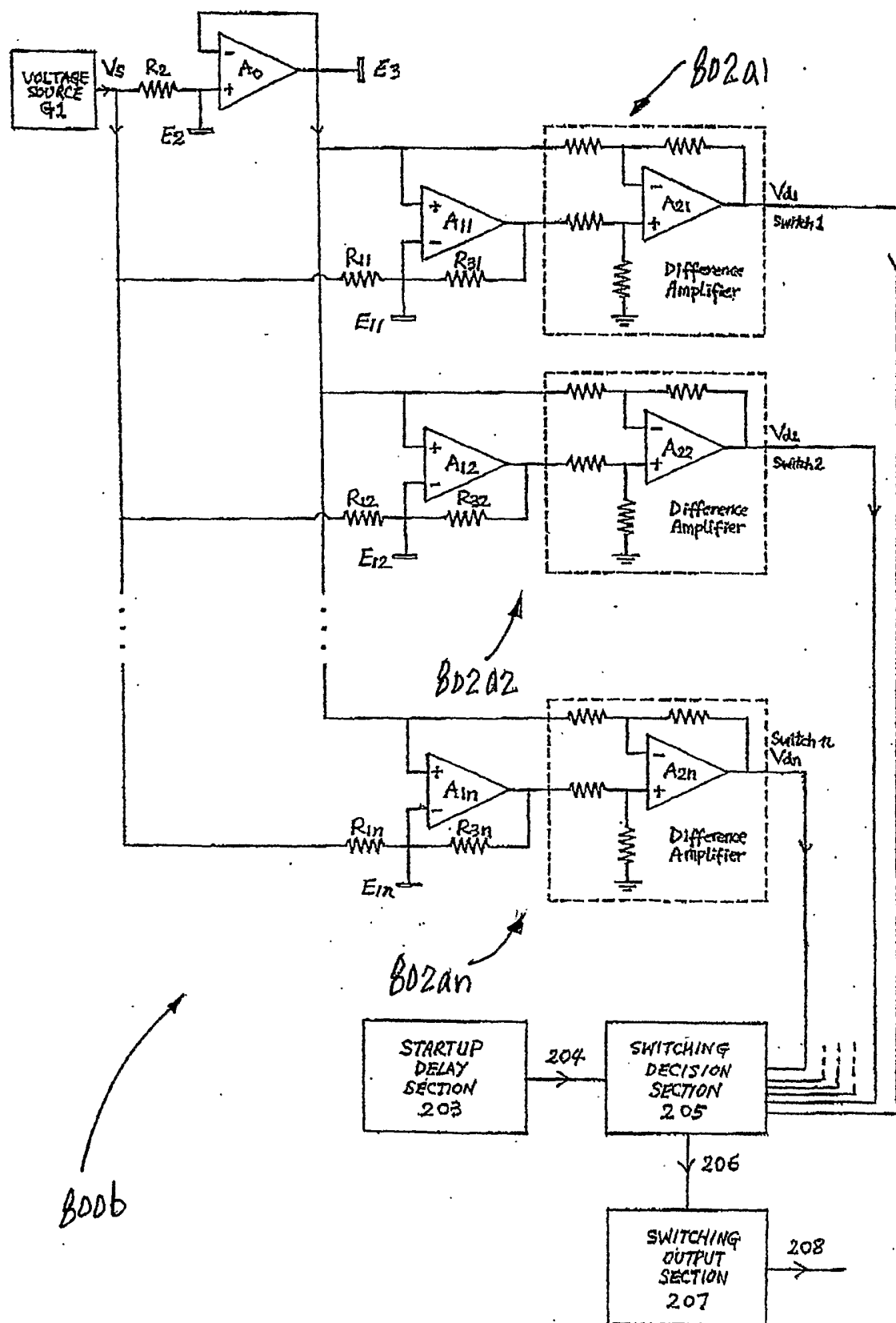


Fig. 8b

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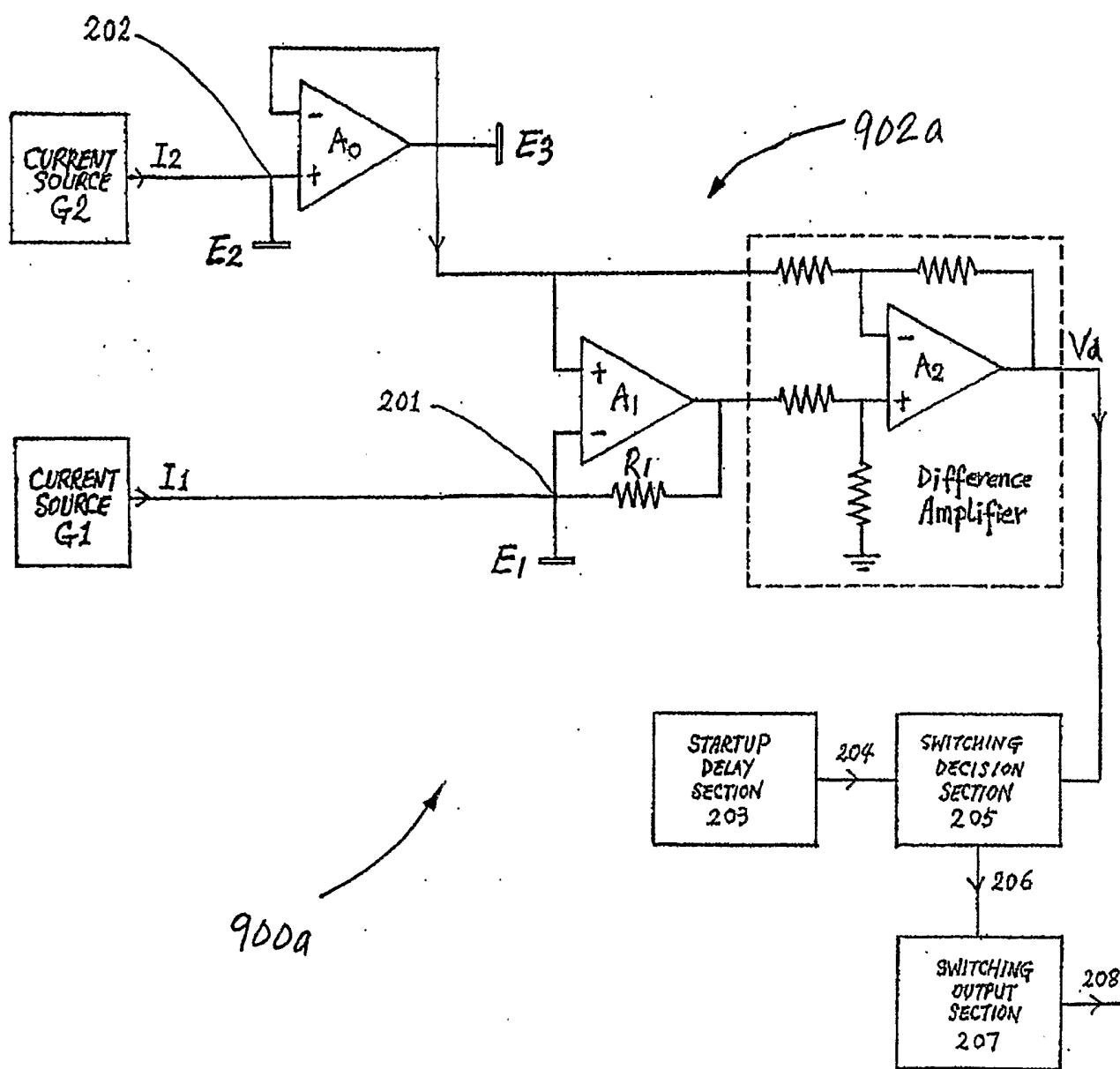


Fig. 9a

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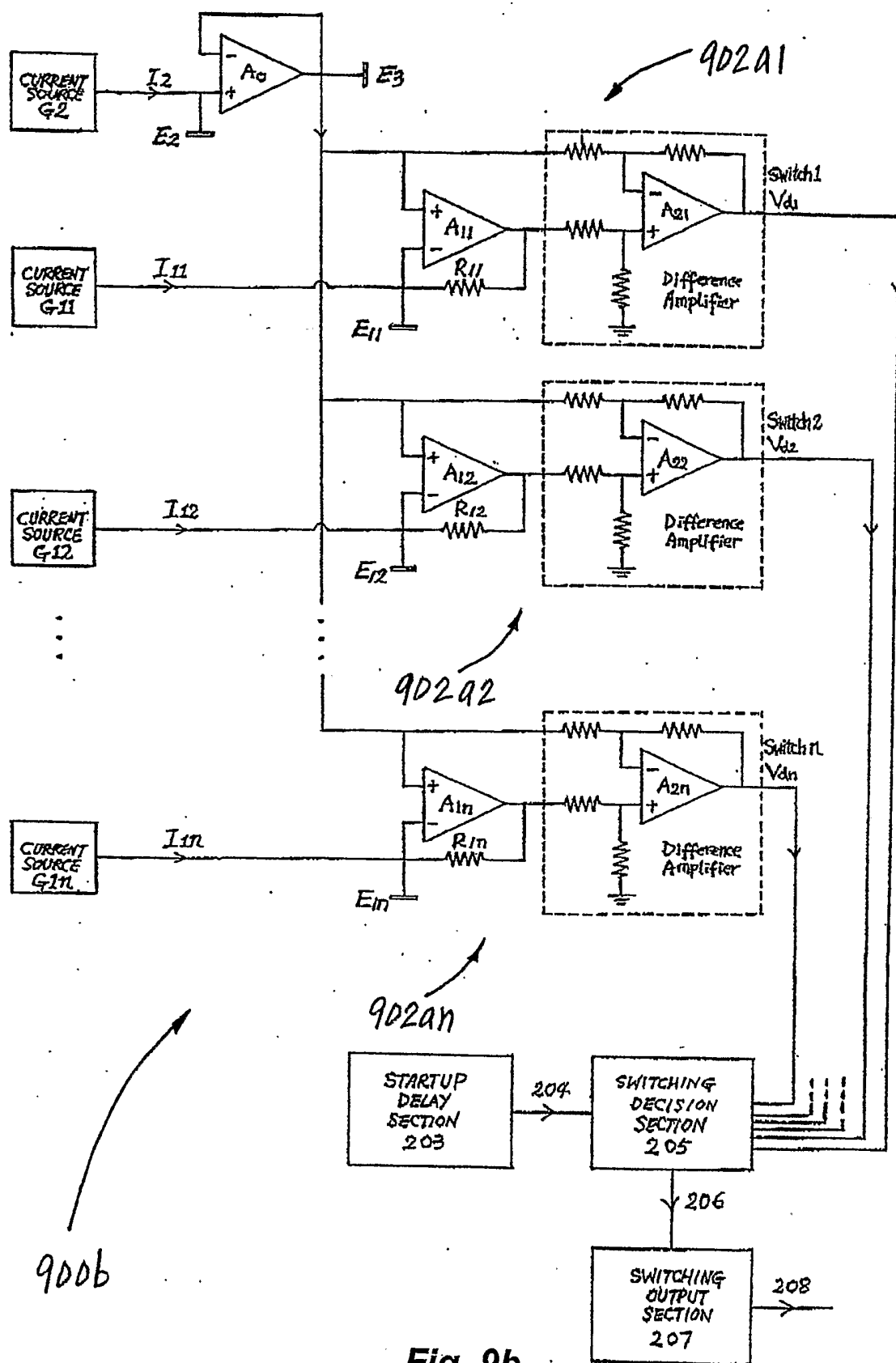


Fig. 9b

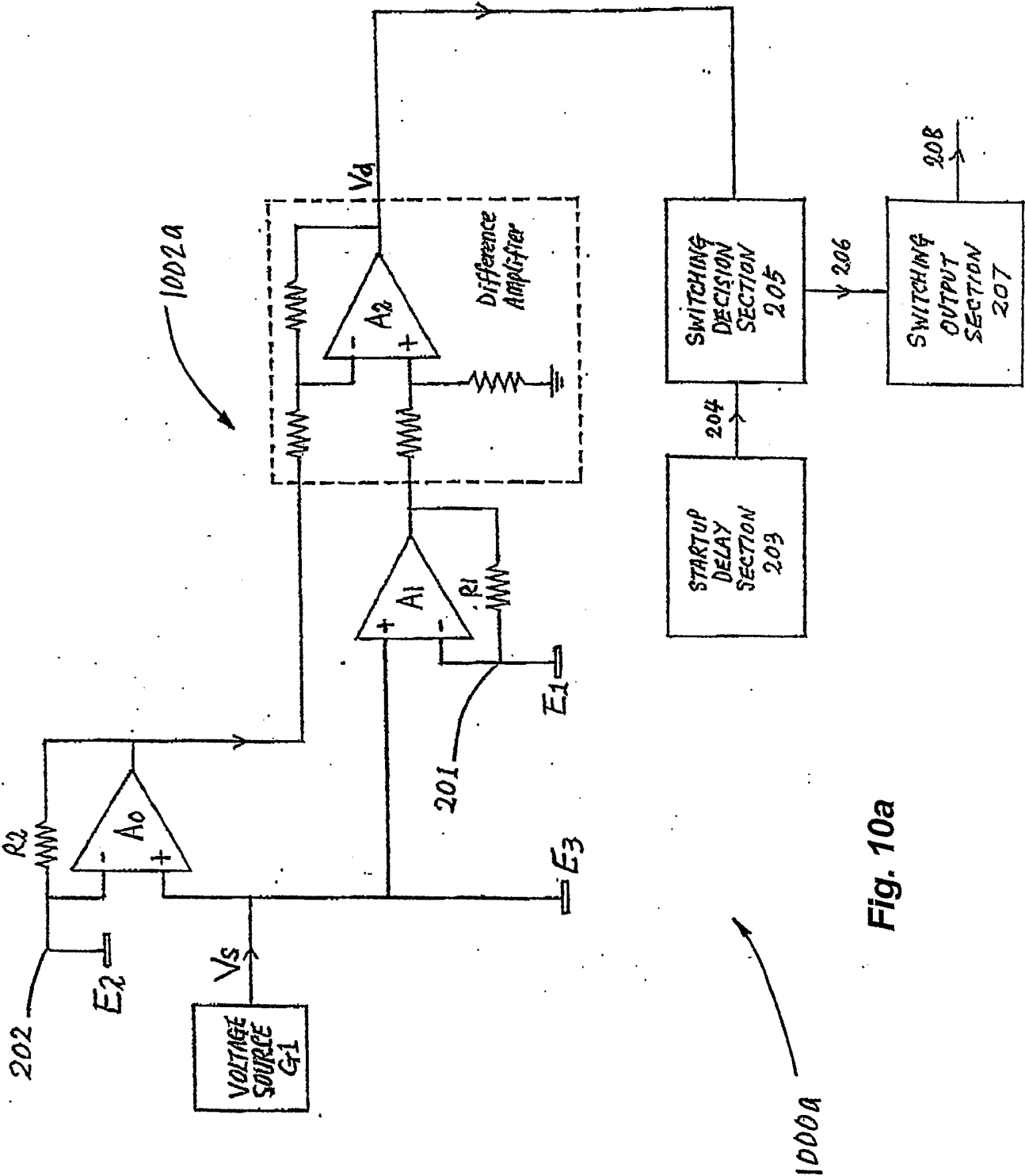


Fig. 10a

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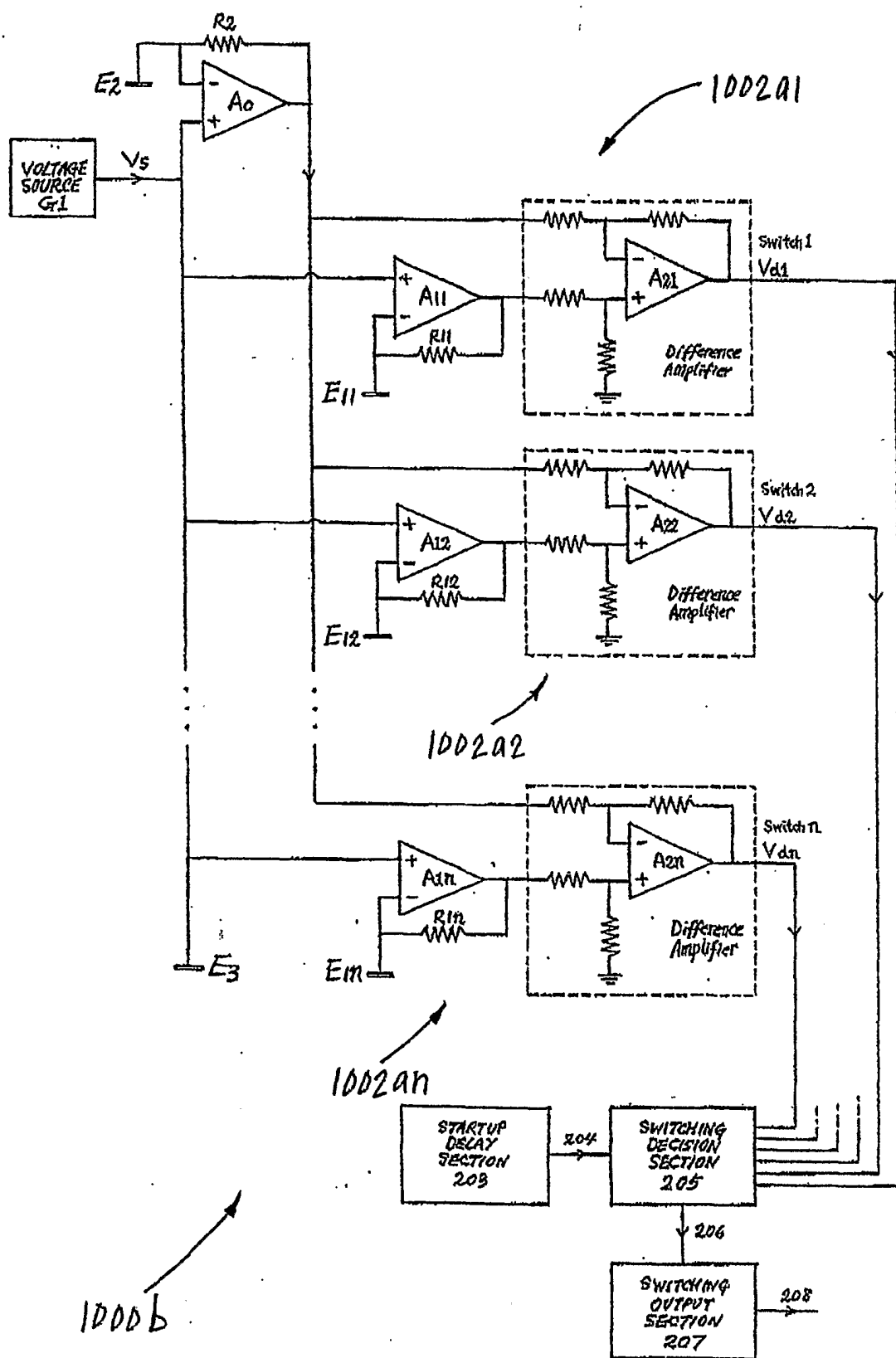


Fig. 10b

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2005/001277

A. CLASSIFICATION OF SUBJECT MATTER

(IPC⁷)H03F3/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

(IPC⁷)H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

chinese document

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPI EPODOC PAJ: linear、capacitance、switch、touchless、amplifier、voltage

CNPAT:

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US,A,4871976(Watanabe et al),03.Oct.1989(03.10.1989), see the whole document	1-46
A	JP,A,9-0280806(NISSAN MOTOR),31.Oct.1997(31.10.1997), see the whole document	1-46
A	JP,A,11-067030(TIETECH CO LTD),09.Mar.1999(09.03.1999), see the whole document	1-46

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents:

“A” document defining the general state of the art which is not considered to be of particular relevance

“E” earlier application or patent but published on or after the international filing date

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“P” document published prior to the international filing date but later than the priority date claimed

“T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

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“Y” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

“&” document member of the same patent family

Date of the actual completion of the international search

07. Nov. 2005(07. 11. 2005)

Date of mailing of the international search report

24 · NOV 2005 (24 · 11 · 2005)

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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/CN2005/001277

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
US4871976A	03.Oct.1989(03.10.1989)	GB2205210A	30.Nov.1988(30.11.1988)
		GB2205210B	24.Apr.1991(24.04.1991)
		DE3818142A	08.Dec.1988(08.11.1988)
		DE3818142C	19.Sep.1991(19.09.1991)
		CA1284189C	14.May.1991(14.05.1991)
		JP63294109A	30.Nov.1988(30.11.1988)
		JP63294110A	30.Nov.1988(30.11.1988)
JP9280806A	31.Oct.1997(31.10.1997)	NONE	
JP11067030A	09.Mar.1999(09.03.1999)	NONE	