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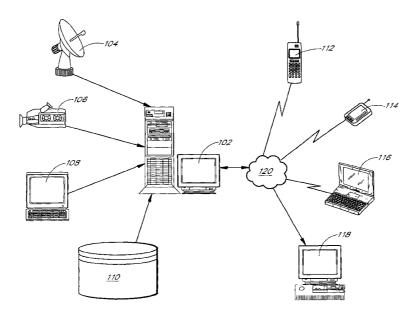
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[Continued on next page]

(54) Title: SYSTEMS AND METHODS OF ERROR RESILIENCE IN A VIDEO DECODER



(57) Abstract: The method and apparatus conceal errors in images of a corrupted video bitstream (2); that cover usable video data from partially corrupted data; that advantageously reconstruct and decode video data (3), such as video object planes (VOPs), using forward error correction (FEC) codes embedded in the video bitstream; and that manage data received in a video bitstream. Various concealment techniques are performed in this apparatus. Embodiments inspect corrupted data packets identify the location or locations of an error, whether the corrupted data packet contains data expected to be error-free (6), data should be used. Backward compatible techniques of decoding FEC codes embedded in video bitstreams are shown (9).



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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched IEEE			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EAST, WEST, IEEE			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where ap	propriate, of the relevant passages	Relevant to claim No.
Y	US 5,912,707 A (KIM) 15 JUNE 1999, figs. 1-3.		1-99
Y	US 5,936,674 A (KIM) 10 AUGUST 1999, figs. 1-2		1-99
Α	US 5,995,171 A (ENARI et al.) 30 NOVEMBER 1999, fig. 1.		1-99
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Further	documents are listed in the continuation of Box C.	See patent family annex.	
* S	pecial categories of cited documents:	"T" later document published after the inte	
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