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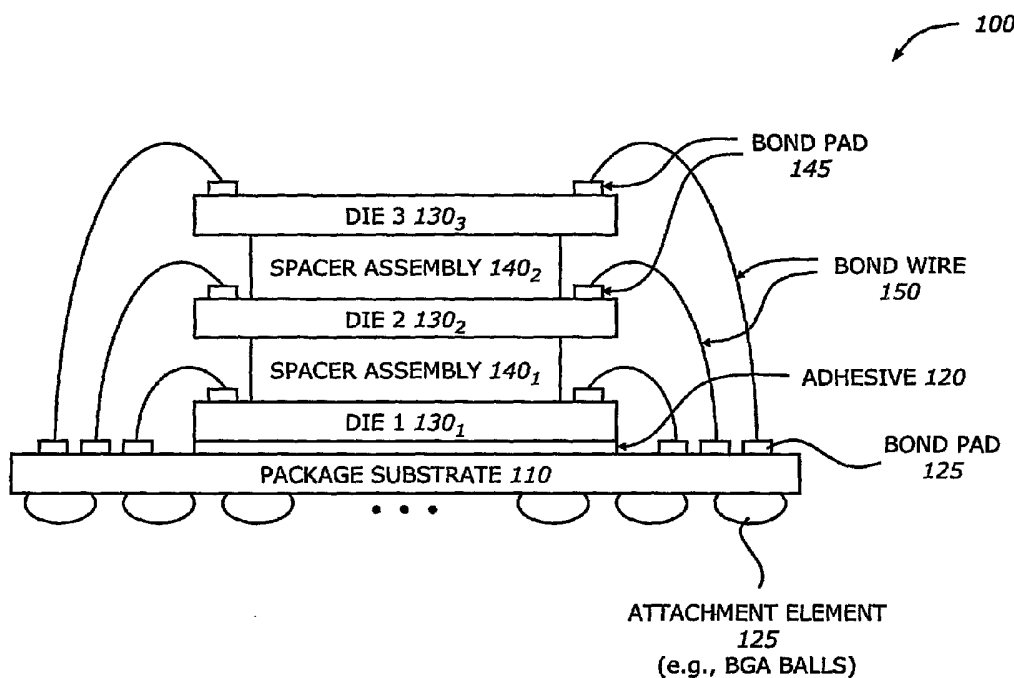
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- (71) Applicant (for all designated States except US): **INTEL CORPORATION** [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **KANG, Jung** [KR/US]; 5682 West Orchid Lane, Chandler, AZ 85226 (US). **RADHAKRISHNAN, Kaladhar** [IN/US]; 1111 North Mission Park, #2091, Chandler, AZ 85224 (US). **CHICKAMENAHALLI, Shamala** [IN/US]; 521 West Iris Drive, Chandler, AZ 85248 (US).
- (74) Agents: **VINCENT, Lester, J.** et al.; Blakely, Sokoloff, Taylor & Zafman, 7th floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025 (US).
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[Continued on next page]

(54) Title: INTEGRATING PASSIVE COMPONENTS ON SPACER IN STACKED DIES



(57) Abstract: An embodiment of the present invention is a technique to integrate passive components in a die assembly. A capacitor, inductor, or resistor is integrated on a spacer between upper and lower dies in stacked dies. Conductors are attached to the capacitor, inductor or resistor to connect the capacitor, inductor, or resistor to at least one of the upper and lower dies.

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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 864 177 A (SUNDSTROM ET AL) 26 January 1999 (1999-01-26) column 2, line 46 - column 3, line 50; figures 1-3	1-3, 6, 8-11, 14, 16-19, 22, 24
X	US 2003/173673 A1 (VAL CHRISTIAN) 18 September 2003 (2003-09-18) paragraph '0032! - paragraph '0042!; figure 2	1-3, 6-11, 14-19, 22-24

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Patent family members are listed in annex.

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Name and mailing address of the ISA
European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
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