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(57) Sammendrag:

Isolation circuit (44, 200), turbine data acquisition systems, and related methods are disclosed. One example isolation circuit includes a voltage divider circuit (102, 202) for coupling to an operational sensor, a clamping circuit (104, 204) connected to said voltage divider circuit and a gain circuit (106, 206) connected to said clamping circuit. The voltage divider circuit is configured to divide an amplitude of a signal received from the sensor. The clamping circuit is configured to limit voltage from said voltage divider circuit. The gain circuit includes an output (108). The isolation circuit provides a single-ended output signal to the output of the gain circuit as a function of the sensor signal and independent of ground.

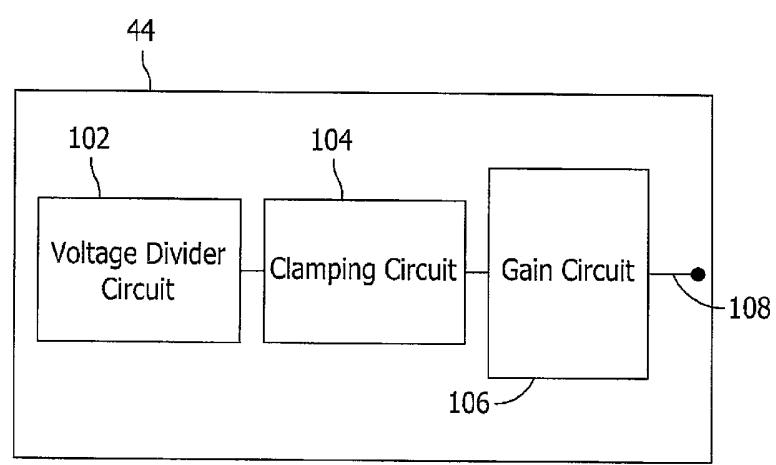


FIG. 2

SYSTEMS AND METHODS FOR USE IN
PROVIDING A SENSOR SIGNAL INDEPENDENT
OF GROUND

5 BACKGROUND OF THE INVENTION

[0001] The field of the invention relates generally to sensor isolation circuits and, more particularly, to systems and methods for use in providing a sensor signal independent of ground.

10 [0002] Gas turbine engine assemblies are often used as a power source within a variety of applications, such as power generation plants and aircraft. At least some known gas turbine engine systems include multiple sensors to monitor various conditions/operations of the gas turbine engine system. For example, gas turbine engine assemblies may include a speed sensor to provide a signal indicative of 15 a rotational speed of a turbine shaft.

[0003] A sensor may be connected to multiple circuits within the gas turbine engine assembly for purposes of control, feedback, and/or monitoring, for example. When the sensor is coupled to more than one circuit, isolation is often 20 required to avoid ground loops which may adversely affect the accuracy of the sensor and/or degrade the circuit performance. At least some known gas turbine engine assemblies employ independent power supplies for each circuit connected to a sensor. Alternatively, other known gas turbine engine assemblies use one or more 25 transformers to electrically isolate the sensor from the circuits. However, transformers are generally bulky and may provide only a limited frequency response.

Zumbahlen, H (ed), Basic Linear Design, (2007) ISBN 0-916550-28-1, Chapter 2: Other Linear Circuits, page 2.29, figure 2.26 discloses an input overvoltage protection for a gain circuit having a single-ended

output. The overvoltage protection comprises a first diode coupled between one of the amplifiers' positive and negative inputs and a positive voltage source, and a second diode coupled between one of the amplifiers' positive and negative inputs and a negative voltage source. This acts as a clamping circuit. The overvoltage protection furthermore comprises a transient voltage suppression device coupled across the positive and negative inputs of the amplifier. The overvoltage protection finally comprises two serially coupled resistors.

BRIEF DESCRIPTION OF THE INVENTION

[0004] In one aspect, an isolation circuit for use in providing a sensor signal independent of ground is provided. The isolation circuit includes a voltage 10 divider circuit for coupling to an operational sensor, a clamping circuit connected to the voltage divider circuit and a gain circuit connected to the clamping circuit. The voltage divider circuit is configured to divide an amplitude of a signal received from the sensor. The clamping circuit is configured to limit voltage from the voltage divider circuit. The gain circuit includes an output. The isolation circuit provides a 15 single-ended output signal to the output of the gain circuit as a function of the sensor signal and independent of ground. The clamping circuit comprises at least one transient voltage suppression (TVS) device. The voltage divider circuit comprises a resistor network, the resistor network comprising at least three resistors. The clamping circuit is coupled in parallel with at least one of said at least three resistors in said resistor network. The clamping circuit comprises at least two capacitors connected in-line between 20 said divider circuit and said gain circuit to block DC voltage between said voltage divider circuit and said gain circuit.

[0005]

25 [0006]

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] Fig. 1 is a block diagram of an exemplary gas turbine engine assembly.

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[0008] Fig. 2 is a block diagram of an exemplary isolation circuit used with the gas turbine engine assembly shown in Fig. 1.

[0009] Fig. 3 is an exemplary schematic diagram of the isolation circuit shown in Fig. 2.

[0010] Fig. 4 is a flowchart of an exemplary method for use in providing a sensor signal independent of ground.

15 DETAILED DESCRIPTION OF THE INVENTION

[0011] Fig. 1 is a schematic diagram of an exemplary gas turbine engine assembly 10. In the exemplary embodiment, gas turbine engine assembly 10 includes a compressor 12, a combustor 14, a turbine 16 that is drivingly coupled to compressor 12 via a rotor shaft 22, a control system 18, and a fuel control assembly 24. Combustor 14 is coupled to compressor 12 such that combustor 14 is in flow communication with compressor 12. Fuel control assembly 24 is coupled to combustor 14 for channeling fuel into combustor 14. An inlet duct 20 channels ambient air to compressor 12. In one embodiment, injected water and/or other 25 humidifying agents are also channeled to compressor 12 through inlet duct 20. Inlet duct 20 may include multiple ducts, filters, screens and/or sound-absorbing devices that contribute to pressure losses of ambient air flowing through inlet duct 20 into one

or more inlet guide vanes 21 of compressor 12.

[0012] During operation, inlet duct 20 channels air towards compressor 12 that compresses the inlet air to higher pressures and temperatures.

5 Compressor 12 discharges compressed air towards combustor 14 wherein it is mixed with fuel and ignited to generate combustion gases that flow to turbine 16, which drives compressor 12. Combustor 14 channels combustion gases to turbine 16 wherein gas stream thermal energy is converted to mechanical rotational energy.

10 [0013] In the exemplary embodiment, gas turbine engine assembly 10 may be used to drive a load 38, such as a generator (not shown) coupled to rotor shaft 22. In an alternative embodiment, load 38 may be coupled to a forward extension (not shown) of rotor shaft 22. In still other embodiments, gas turbine engine assembly 10 may be employed in a vehicle, such as an aircraft.

15 [0014] The operation of gas turbine engine assembly 10 may be monitored by several sensors 26-36 that detect various operating conditions of turbine 16, load 38, and/or ambient environment. For example, sensors 26-36 may also include flow sensors, speed sensors, flame detector sensors, valve position sensors, 20 guide vane angle sensors, and/or other sensors that sense various parameters (e.g., temperatures, pressures, and gas flows at defined locations, etc.) relative to the operation of gas turbine engine assembly 10. In the exemplary embodiment, sensor 34 is a speed sensor for use in generating a differential speed signal indicative of rotational speed of the rotor shaft 22.

25 [0015] In the exemplary embodiment, control system 18 includes a monitoring module 40 and a control module 42. Speed sensor 34 is coupled to

monitoring module 40 and to control module 42. Monitoring module 40, however, is coupled to speed sensor 34 via isolation circuit 44. In combination, speed sensor 34 and isolation circuit 44 form a turbine data acquisition system. It should be appreciated that any one or more of the sensors 26-36 illustrated in Fig. 1 may be 5 coupled via an isolation circuit to control system 18, including one or more modules therein. Further, in other embodiments, an isolation circuit may be included in a control module, rather than being included in a monitoring module. And, in still other embodiments, a monitoring module and a control module may each include an isolation circuit.

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[0016] Fig. 2 is a block diagram of isolation circuit 44. In the exemplary embodiment, isolation circuit 44 includes a voltage divider circuit 102 for use in coupling to sensor 34. Voltage divider circuit 102 divides an amplitude of a sensor signal from sensor 34. Isolation circuit 44 includes a clamping circuit 104 15 coupled to voltage divider circuit 102 and a gain circuit 106 coupled to clamping circuit 104. Clamping circuit 104 limits voltage from divider circuit 102, and gain circuit 106 includes an output 108. Isolation circuit 44 provides a single-ended output signal to output 108 of gain circuit 106 as a function of the differential sensor signal of speed sensor 34 and independent of ground.

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[0017] In such an embodiment, isolation circuit 44 enables speed sensor 34 to be coupled to monitoring module 40 and to control module 42, without a duplicate ground connection. As such, multiple ground loops between sensor 34 and modules 40 and 42 are not created. Accordingly, isolation circuit 44 may also 25 eliminate a need for a transformer(s) and/or a separate power source(s) for monitoring module 40 and for control module 42. Moreover, and as explained below, isolation circuit 44 may provide improved frequency response over known transformers.

Further still, in some embodiments, isolation circuit 44 may be substantially only include off-the-shelf components, thus improving cost savings, and/or circuit board space savings, as compared to known transformers and/or using separate power sources.

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[0018] FIG. 3 is an exemplary schematic of an alternate isolation circuit 200 that may be used to provide a sensor signal independent of ground. In the exemplary embodiment, isolation circuit 200 includes a voltage divider circuit 202, a clamping circuit 204 connected to voltage divider circuit 202, and a gain circuit 206 connected to clamping circuit 204.

[0019] In the exemplary embodiment, voltage divider circuit 202 includes a resistor network 207. More specifically, resistor network 207 includes three resistors R1, R2, and R3. Resistors R1-R3 are coupled in series between input terminals 208 and 210. Resistors R1-R3 are coupled to divide an amplitude of an input signal to terminals 208 and 210 according to the proportional resistance of resistors R1-R3. Specifically, in the exemplary embodiment, clamping circuit 204 is connected in parallel with resistor R2. Accordingly, during operation, voltage divider circuit 202 provides a signal that is approximately equal to the input signal multiplied by the resistance of resistor R2 and divided by the total resistance of resistors R1-R3 (i.e., $V_{input} \times R2/(R1+R2+R3)$). It should be appreciated that various kinds of resistances and/or any number of resistors may be employed to provide a desired voltage to clamping circuit 204. In at least one embodiment, a voltage divider circuit may include any combination of resistors, capacitors and/or inductors that enables an amplitude of a sensor signal to be divided as described herein.

[0020] Clamping circuit 204 includes a transient voltage suppression

(TVS) device 212. TVS device 212 is coupled in parallel with resistor R2 to clamp the voltage across resistor R2. TVS device 212 may be selected to clamp the voltage to desired voltage ranges. In this exemplary embodiment, TVS device 212 device is a bi-directional device that is structured to clamp voltages at 26VDC (e.g., component 5 SMBJ26 commercially available from Vishay Semiconductors, Microsemi Corporation, etc.). Moreover, in this exemplary embodiment, TVS device 212 is structured to provide electromagnetic interference (EMI) protection and/or electrostatic discharge (ESD) protection to isolation circuit 200.

10 [0021] As shown, in the exemplary embodiment, clamping circuit 204 includes capacitor C1 coupled in series between TVS device 212 and node A and capacitor C2 coupled in series between TVS device 212 and node B. During operation, capacitors C1 and C2 block a DC voltage component of a differential sensor signal, if any, provided from a sensor through voltage divider circuit 202.

15 [0022] In addition to TVS device 212 and capacitors C1 and C2, clamping circuit 204 includes diodes D1 -D4. More specifically, in the exemplary embodiment, diode D1 is connected between a negative voltage source V- and node A, and diode D2 is connected between a positive voltage source V+ and node A. 20 Additionally, diode D3 is connected between negative voltage source V- and node B, and diode D4 is connected between positive voltage source V+ and node B. Resistor R4 is connected between node A and node B.

25 [0023] Each diode D1 -D4 defines a bias voltage. Accordingly, during operation, diodes D1-D4 clamp voltage at respective nodes A and B to within its bias voltage of negative voltage source V- and/or positive voltage source V+. As such, diodes D1 -D4 may be selected to clamp voltages at nodes A and B within a

desired range. For example, if positive voltage source V+ is 5VDC and negative voltage sources V- is -24VDC, diodes D 1 -D4 may be selected to clamp voltage at nodes A and B to substantially between -24VDC and 5VDC (\pm bias voltage of diodes D 1 -D4). Other voltages and different configurations of diodes (e.g., number, types, 5 position, etc.) may be used in other embodiments to clamp voltages above/below one or more desired values. In some example embodiments, diodes may be tied to readily accessible voltage, such as voltage sources of monitoring module 40 and/or control module 42. In still other embodiments, one or more diodes connected to a voltage source may be omitted.

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[0024] It should be appreciated that clamping circuit 204 may be constructed from one or more different types of devices to clamp voltages to desired levels and/or to provide electromagnetic interference (EMI) and/or electro-static discharge (ESD) protection. For example, in an alternative embodiment, clamping 15 circuit 204 may include two Zener diodes, one rated for 5VDC and the other rated for 15VDC. The anode of the 5VDC Zener diode may be connected to the cathode of the 15VDC Zener diode, and the cathode of the 5VDC Zener diode may be connected to the anode of the 15VDC Zener diode. In such an embodiment, clamping circuit 204 may be connected to clamp a voltage within the unbalanced range of between about +5V to about -15V. As should be apparent, various combinations and/or ratings of Zener diodes or other diodes may be used to clamp a divided sensor signal to various balanced or unbalanced voltage ranges.

25 [0025] Referring again to FIG. 3, gain circuit 206 includes an operational amplifier 214. In the exemplary embodiment, operational amplifier 214 is part #LT1355 (commercially available from Linear Technology). Alternatively, it should be appreciated, however, that in other embodiments, a gain circuit may be

constructed from a different operational amplifier, multiple discrete transistors, and/or other suitable device. Operational amplifier 214 includes a positive input 216, a negative input 218, and an output 220. During operation, operational amplifier 214 provides a gain to the voltage received from clamping circuit 204 that is sufficient to

5 adjust a single-ended output signal to a desired level. For example, in one example embodiment, the gain from operation amplifier 214 is about 1.5 V/V. As should be apparent, operational amplifiers may be connected and/or selected to provide different gains in other embodiments. In at least one embodiment, gain circuit 206 may provide a substantially nominal gain.

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[0026] In the exemplary embodiment, operational amplifier 214 is connected to power sources, to ground, and to clamping circuit 204 via multiple discrete components. Specifically, resistor R5 is connected between node A and positive input 216, and resistor R6 is connected in series between node A and negative input 218. Positive input 216 is connected to ground GND through a parallel combination of resistor R7 and capacitor C3. Negative input 218 is connected to output 220 through a parallel combination of resistor R8 and capacitor C4. Negative input 218 is also coupled to ground GND through resistor R9.

20 [0027] One or more of resistors R1-R8 and/or of capacitors C1 -C4 of isolation circuit 200 may be selected to provide a desired performance, such as output voltage, operating bandwidth, DC error, etc. In this particular example, the output voltage is represented as:

$$V_{out} = -R/R \times V_{IN-NEG} + 5 \times -R/R + (R/(r + R)) \times ((R/(R R)) + 1) \times V_{IN-POS}$$

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[0028] In the exemplary embodiment, isolation circuit 200 may provide flexible frequency response. Specifically, any of resistors R1-R8 and/or

capacitors C 1 -C4 may be selectively adjusted to alter the frequency response of isolation circuit 200. In one exemplary example, isolation circuit 200 may define a pass-band (about -3.0 dB) of between about 1.3 Hz to about 982 kHz on the positive input of operational amplifier 214, and a pass-band (about -3.0 dB) of between about 5 1.5 Hz to about 220 kHz on the positive input of operational amplifier 214.

Accordingly, in this particular example, the overall operating bandwidth (about -3.0 dB) of isolation circuit 200 is between about 1.59 Hz to about 220 kHz. As should be apparent, the operating bandwidth includes several ranges, such as a range of between about 7.0 Hz to about 150 kHz, a range of between about 10 Hz to about 150 kHz, a 10 range of between about 25 Hz to about 100 kHz, a range of between about 35 Hz to about 100 kHz and/or any other discrete values bounding a range within the operating bandwidth, etc.

[0029] Further, as shown in FIG. 3, node C is connected to ground 15 through resistor R9. In various embodiments, the single-ended output signal of isolation circuit 200 may be level shifted, by connecting node C to a power source through resistor R9. Specifically, when node C is coupled to a 5VDC power source, for example, isolation circuit 200 includes a nominal DC bias of about -9.08VDC and a DC error of about $\pm 24\text{mV}$ at $25 \pm 2^\circ\text{C}$ (and/or $\pm 66\text{mV}$ under/over temperature (-40 20 to 85°C)). It should be appreciated that isolation circuit 200 is not limited to the particular output characteristics disclosed herein. One or more of the power sources, resistors, capacitors, diodes, and operational amplifiers may be altered to provide desired output characteristics, possibly depending on requirements of a gas turbine engine assembly or other application of isolation circuits disclosed herein.

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[0030] During fabrication, isolation circuit 200 may be fabricated with common, off-the-shelf components, such as resistors, capacitors, diodes, and

operational amplifiers, etc. In some embodiments, use of common, off-the-shelf components may facilitate cost reduction and/or convenient second sourcing of required components. Further, in one embodiment, isolation circuit 200 may be constructed on less than about 1.0 square inch of a printed circuit board (not shown).

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[0031] Figure 4 illustrates an exemplary method 300 for use in providing a sensor signal independent of ground in accordance with another example embodiment of the present disclosure. While exemplary method 300 may be used with gas turbine engine assembly 10 (as shown in Fig. 1), it should be appreciated that methods described herein are not limited to the exemplary gas turbine engine assemblies and isolation circuits described herein. Likewise, isolation circuits and/or gas turbine engine assemblies contemplated by the present disclosure should not be understood to be limited to the exemplary methods described herein.

15 [0032] Method 300 includes providing 302 an isolation circuit, such as isolation circuit 200 shown in Fig. 3, to couple a sensor to a control system of a gas turbine engine assembly. The isolation circuit includes a voltage divider circuit, a clamping circuit coupled to the voltage divider circuit, and a gain circuit coupled to the clamping circuit. Method 300 also includes receiving 304 a differential input signal from the sensor, dividing 306, at a resistor network of the voltage divider circuit, an amplitude of the differential sensor signal, and generating 308, at an operational amplifier of the gain circuit, a single-ended output signal as a function of the differential sensor signal and independent of ground.

20 [0033] In some embodiments, method 300 may include clamping, at the clamping circuit, the divided differential sensor signal to a desired voltage range. Additionally, or alternatively, method 300 may include blocking, at the clamping

circuit, a DC component of the differential sensor signal.

[0034] One or more of the above-described embodiments may eliminate one or more ground loops when a sensor is coupled to more than one module. Accordingly, an isolation circuit as described herein may eliminate the need for a transformer(s) and/or a separate power source(s) for a monitoring module and/or for a control module. Moreover, an isolation circuit as described herein may provide improved frequency response over known transformers. Further still, in some embodiments, an isolation circuit as described herein may permit use of off-the-shelf components, thus improving cost savings, and/or circuit board space savings, as compared to known transformers and/or using separate power sources.

[0035] Although specific features of various embodiments may be shown in some drawings and not in others, this is for convenience only. In accordance with the principles of the invention, any feature of a drawing may be referenced and/or claimed in combination with any feature of any other drawing.

[0036] This written description uses examples to disclose the invention, including the best mode, and also to enable any person skilled in the art to practice the invention, including making and using any devices or systems and performing any incorporated methods. The patentable scope of the invention is defined by the claims, and may include other examples that occur to those skilled in the art. Such other examples are intended to be within the scope of the claims if they have structural elements that do not differ from the literal language of the claims, or if 25 they include equivalent structural elements with insubstantial differences from the literal language of the claims.

SYSTEMS AND METHODS FOR USE IN
PROVIDING A SENSOR SIGNAL INDEPENDENT
OF GROUND

PARTS LIST

- 5 10 Gas turbine engine assembly
- 12 Compressor
- 14 Combustor
- 16 Turbine
- 18 Control system
- 10 20 Inlet duct
- 21 Inlet guide vanes
- 22 Rotor shaft
- 24 Fuel control assembly
- 26 Sensor
- 15 28 Sensor
- 30 Sensor
- 32 Sensor
- 34 Sensor
- 36 Sensor
- 20 38 Load
- 40 Monitoring module
- 42 Control module
- 44 Isolation circuit
- 102 Voltage divider circuit
- 25 104 Clamping circuit
- 106 Gain circuit
- 108 Output

- 200 Isolation circuit
- 202 Voltage divider circuit
- 204 Clamping circuit
- 206 Gain circuit
- 5 207 Resistor network
- 208 Terminals
- 210 Terminals
- 212 Transient voltage suppression (TVS) device
- 214 Operational amplifier
- 10 216 Positive input
- 218 Negative input
- 220 Output
- 300 Method
- 302 providing
- 15 304 receiving
- 306 dividing
- 308 generating

Patentkrav

1. Isoleringkredsløb (44, 200) til anvendelse inden for tilvejebringelsen af et sensorsignal uafhængigt af jord, hvilket isoleringkredsløb omfatter:
 - 5 et spændingsdelerkredsløb (102, 202) til kobling med en operationel sensor (26-36), hvilket spændingsdelerkredsløb er konfigureret til at dele en amplitude af et signal modtaget fra den operationelle sensor;
 - 10 et clamping-kredsløb (104, 204), som er forbundet med spændingsdelerkredsløbet, hvilket clamping-kredsløb er konfigureret til at begrænse spænding fra spændingsdelerkredsløbet; og
 - 15 et forstærkningskredsløb (106, 206), som er forbundet med clamping-kredsløbet, hvilket forstærkningskredsløb omfatter et output (108), hvor isoleringkredsløbet tilvejebringer et single-ended outputsignal for outputtet af forstærkningskredsløbet som en funktion af sensorsignalet og uafhængigt af jord, og hvor clamping-kredsløbet (104, 204) omfatter mindst en indretning (212) til transientspændingsdæmpning (TVS);
 - 20 **kendetegnet ved, at** spændingsdelerkredsløbet (102, 202) omfatter et modstandsnetværk (207), hvilket modstandsnetværk (207) omfatter mindst tre modstande, og hvor clamping-kredsløbet (104, 204) er parallelforbundet med mindst en af de i det mindste tre modstande i modstandsnetværket, og
 - 25 hvor clamping-kredsløbet (104) omfatter mindst to kondensatorer, som er forbundet in-line mellem delerkredsløbet (102) og forstærkningskredsløbet (106), så der blokeres for en jævnstrømsspænding mellem spændingsdelerkredsløbet og forstærkningskredsløbet.
 - 30 2. Isoleringkredsløb (44, 200) ifølge krav 1, hvor forstærkningskredsløbet (106, 206) omfatter en operationel forstærker (214), som omfatter et positivt input (216) og et negativt input (218), og hvor TVS-indretningen er koblet over det positive og negative input af den operationelle forstærker.

3. Isoleringskredsløb (44, 200) ifølge et hvilket som helst af de foregående krav, hvor clamping-kredsløbet (104, 204) indbefatter en første diode, som er koblet mellem et af det positive og negative forstærkerinput (216, 218) og en positiv spændingskilde, og en anden diode, som er koblet mellem et af det 5 positive og negative forstærkerinput (216, 218) og en negativ spændingskilde.
4. Isoleringskredsløb (44, 200) ifølge et hvilket som helst af de foregående krav, yderligere omfattende en første modstand, som er forbundet in-line 10 mellem det positive input (216) og TVS-indretningen (212), og en anden modstand, som er forbundet in-line mellem det negative input (218) og TVS-indretningen.
5. Isoleringskredsløb (44, 200) ifølge et hvilket som helst af de foregående krav, hvor TVS-indretningen (212) fremmer beskyttelse af isolationskredsløbet (44, 200) mod elektromagnetisk udladning. 15
6. Isoleringskredsløb (44, 200) ifølge et hvilket som helst af de foregående krav, hvor isoleringskredsløbet (44, 200) er konfigureret til at tilvejebringe en 20 drift båndbredde i et område på mellem omkring 25 Hz til omkring 100 kHz.

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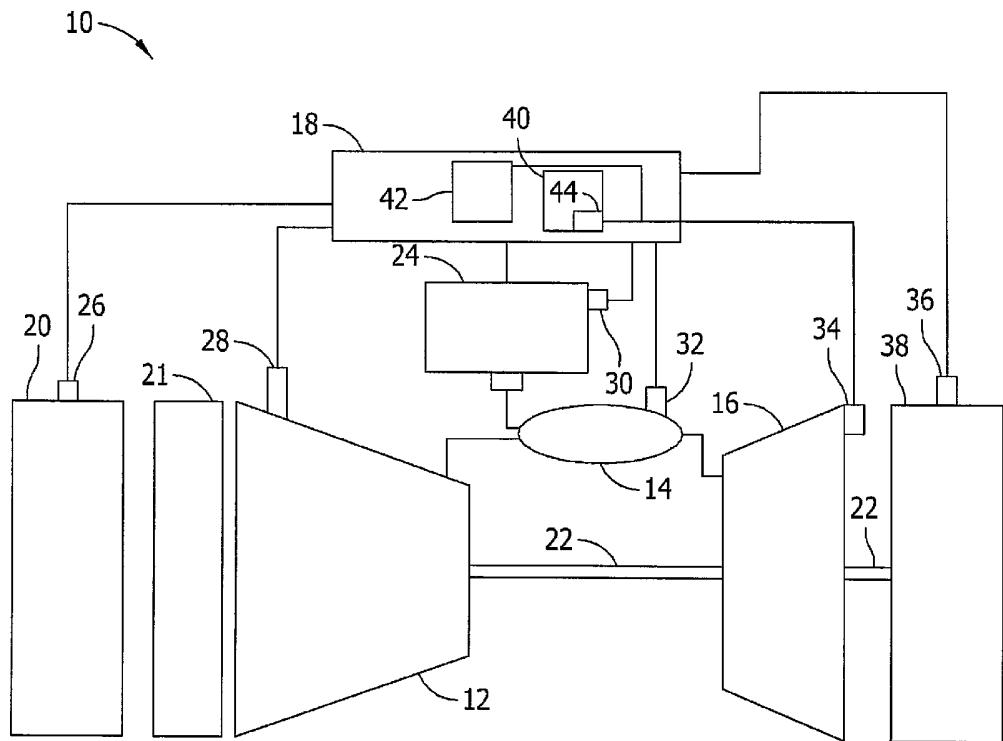


FIG. 1

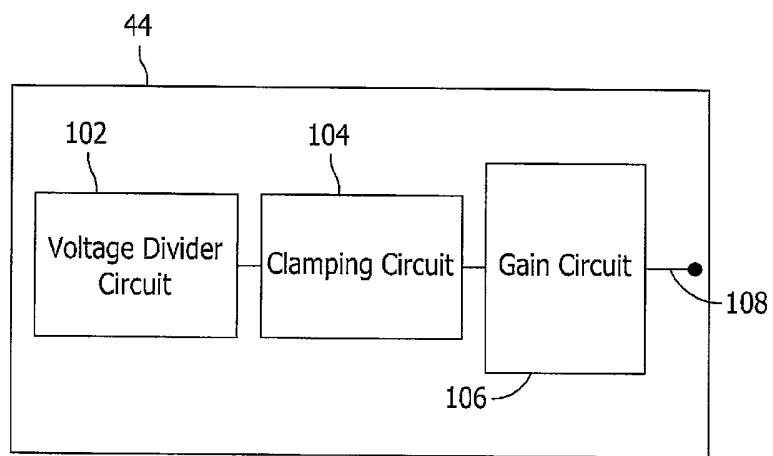


FIG. 2

2/2

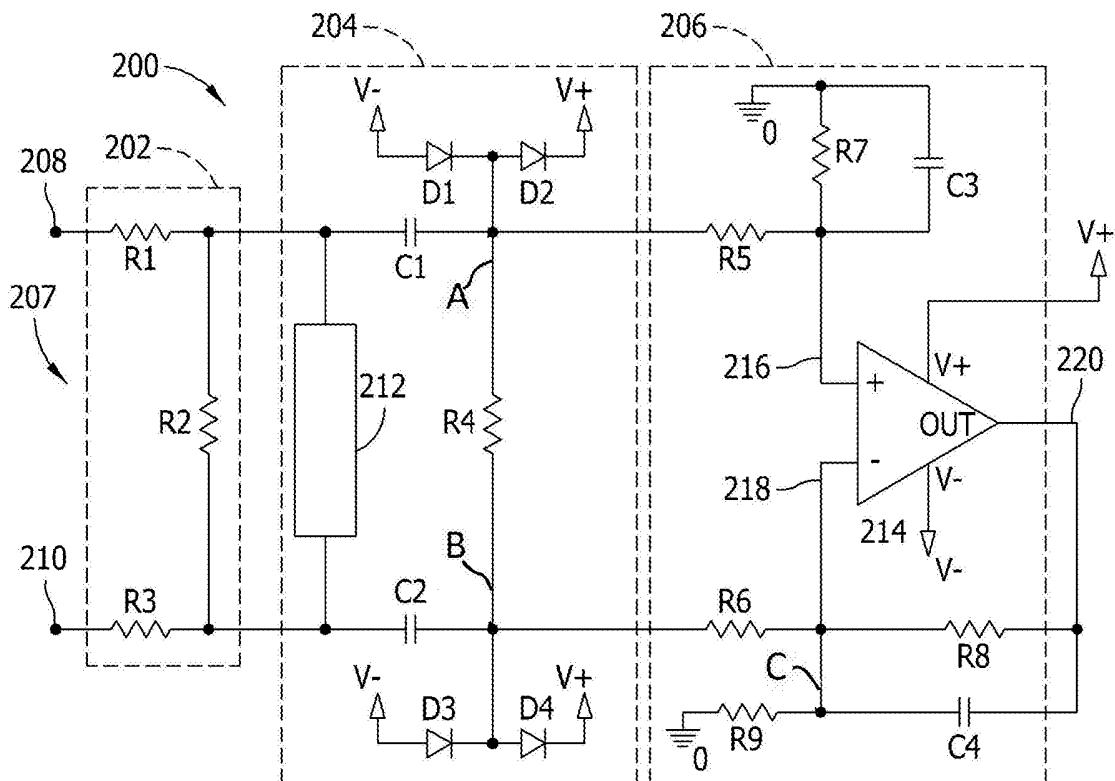


FIG. 3

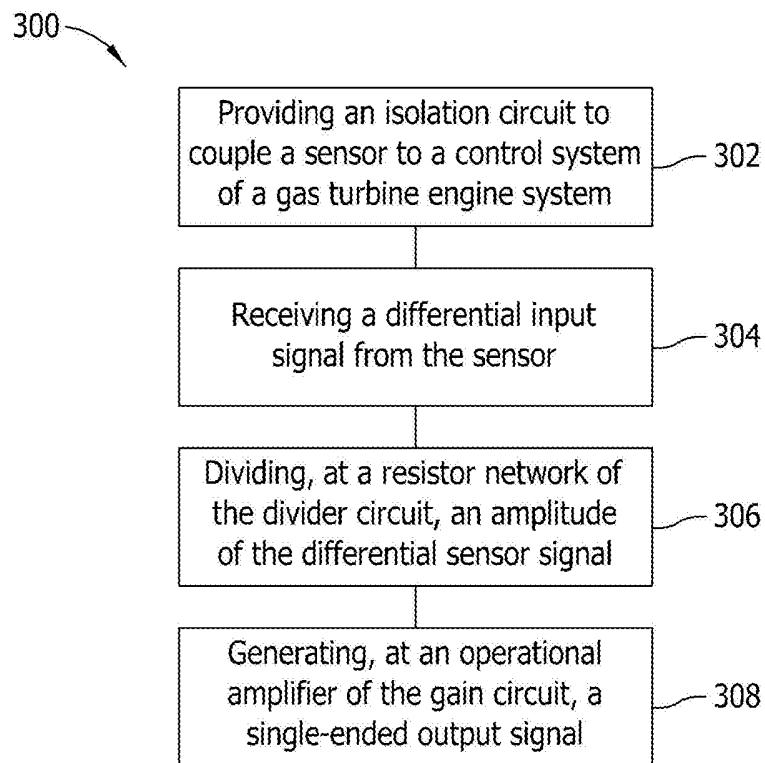


FIG. 4

SEARCH REPORT - PATENT		Application No. PA 2012 70258
<p>A. CLASSIFICATION OF SUBJECT MATTER G 01 D 5/12 (2006.01) According to International Patent Classification (IPC)</p>		
<p>B. FIELDS SEARCHED PCT-minimum documentation searched (classification system followed by classification symbols) IPC/ECLA: G01D</p>		
<p>Documentation searched other than PCT-minimum documentation DK, FI, NO, SE: Classes as above</p>		
<p>Electronic database consulted during the search (name of database and, where practicable, search terms used) EPODOC, WPI</p>		
<p>C. CLAIMS SEARCHED 1-6</p>		
<p>D. DOCUMENTS CONSIDERED TO BE RELEVANT</p>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant for claim No.
X	ZUMBAHLEN, H (ed.), Basic Linear Design, [online], 2007 [retrieved on 2012-12-12] <URL: http://web.archive.org/web/20100911024047/http://www.analog.com/library/analogdialogue/archives/43-09/EDCh%202%20other%20linear%20circuits.pdf >, ISBN 0-916550-28-1, Chapter 2: Other Linear Circuits, page 2.29, figure 2.26	1-6
X	US 2004/0000841 A1 (PHELPS et al.) 2004-01-01 figure 12	1-6
A	US 4500841 A (MOREY) 1985-02-19	-
A	US 6265976 B1 (ROESNER) 2001-07-24	-
No further documents		
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<p>Danish Patent and Trademark Office Helgeshøj Allé 81 2630 Taastrup Denmark Tel.: +45 4350 8000 Fax: +45 4350 8001</p>		<p>Date of completion of the search report 13 December 2012</p> <p>Authorized officer Carl Kortegaard</p>

SEARCH REPORT - PATENT		Application No. PA 2012 70258
D (Continued). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant for claim No.