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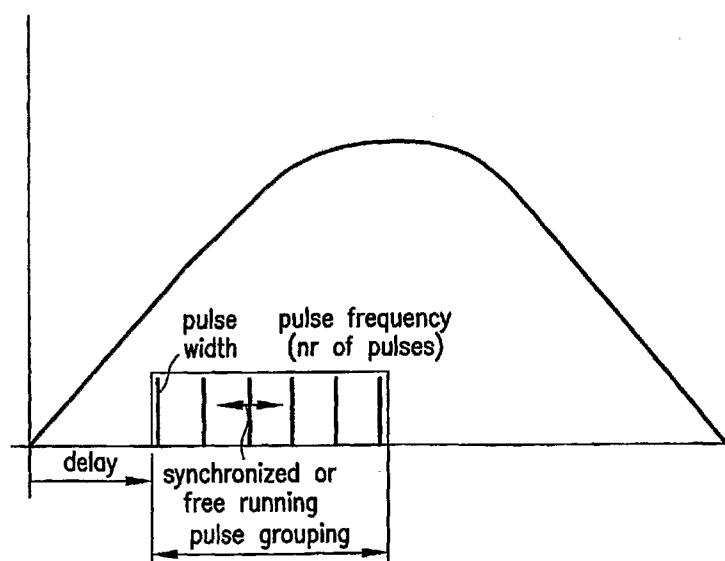
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[Continued on next page]

(54) Title: LOW SWITCHING FREQUENCY POWER FACTOR CORRECTION CIRCUIT



(57) Abstract: A power factor correction circuit for driving a switch of a boost type converter circuit having an AC line frequency input, comprising a sense circuit for sensing and comparing the output voltage of the boost type converter circuit to a reference voltage, a circuit responsive to the output of the sense circuit for providing a drive signal for driving the switch, wherein the drive signal comprises a pulsed signal comprising a pulse grouping during each half cycle of the AC line frequency comprising at least one pulse wherein the number of pulses and frequency of the pulses of the pulse grouping changes with the output of the sense circuit up to a threshold value with the pulse width of the pulses being substantially constant; and when the output of the sense circuit has reached the threshold value, the pulse width of the pulses of the pulse grouping are varied in accordance with the output of the sense circuit.

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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5,349,284 A (WHITTLE) 20 September 1994 (20.09.1994), see entire document.	1-42
☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.		
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